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[illegible]

LOW ICH9 GPIO LIST TABLE

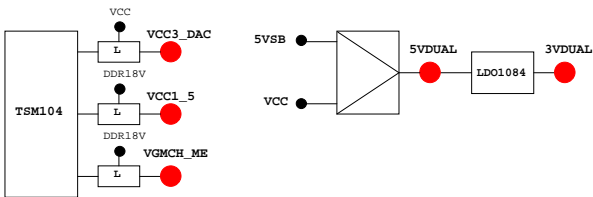
PIN NAME	PWR WELL	APPER/ PLTRST	USAGE	NOTE
GP0	MAIN	IN	VTT_GMCH_OV3	
GP1/TACH1	MAIN	IN	ICH_FAN_TACH1	P/U 8.2K VCC3
GP2/PIRQE#	MAIN	IN	~PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	IN	~PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	IN	~PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	IN	~PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	IN	ICH_FAN_TACH2	P/U 8.2K VCC3
GP7/TACH3	MAIN	IN	ICH_FAN_TACH3	P/U 8.2K VCC3
GP8	STBY	IN	DDR18V_OV4	
GP9	STBY	H-Z	GPIO9 (DUALBIOS INPUT)	
GP10	STBY	H-Z	DDR18V_OV5	P/D 100K GND/X
GP11/SMBALERT#	STBY	NATIVE	~SMBALRT	P/U 8.2K 3VDUAL
GP12	STBY	L OUT	AUDIO DETECT	P/U 8.2K VCC3
GP13	STBY	L IN	-LPCPME	P/U 8.2K 3VDUAL
GP14	STBY	H-Z	DDR18V_OV2	P/U 8.2K 3VDUAL
GP15	STBY	H-Z	SPI_WP	STP_PCI#
GP16	MAIN	L OUT	DUAL BIOS CONTROL	N/A
GP17/TACH0	MAIN	IN	ICH_FAN_TACH0	P/U 8.2K VCC3
GP18	MAIN	H OUT	MB_ID1	P/U 8.2K VCC3
GP19	MAIN	IN	VCC15_OV1	P/U 8.2K VCC3/X
GP20	MAIN	OUT	~SPI_WP0	P/U 1K 3VCL
GP21	MAIN	IN	VCC15_OV3	P/U 8.2K VCC3
GP22	MAIN	IN	VCORE_OV3	P/U 8.2K VCC3
GP23	MAIN	OUT	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	OUT	TLS	P/U 8.2K 3VDUAL
GP25	STBY	IN	MB_ID2(STP_CPU~)	P/U 8.2K 3VDUAL
GP26/S4_STATE#	STBY	OUT	MB_ID0	P/U 8.2K 3VDUAL
GP27	STBY	OUT/LOW	GPIO27 (EL_STATE0)	P/U 8.2K 3VDUAL
GP28	STBY	OUT/LOW	DUAL BIOS CONTROL	N/A
GP29/OC5#	STBY	IN	~USBOC_R	P/U FUSEVCC
GP30/OC6#	STBY	IN	~USBOC_R	P/U FUSEVCC
GP31/OC7#	STBY	IN	~USBOC_R	P/U FUSEVCC
GP32	MAIN	OUT	DUAL_BIOS	P/U 100K+1M VCC3
GP33	MAIN	OUT		
GP34	MAIN	OUT/LOW		N/A
GP35	MAIN	L OUT	400K FS CONTROL	N/A
GP36	MAIN	IN	DUAL BIOS CONTROLP/U	8.2K VCC3
GP37	MAIN	IN	150K FS CONTROL	P/U 8.2K VCC3
GP38	MAIN	IN	VCORE_OV2	P/U 8.2K VCC3
GP39	MAIN	IN	GPIO39	P/D 8.2K GND
GP48	MAIN	IN	VCORE_OV1	P/U 8.2K VCC3
GP49	MAIN	IN	STARPPING	P/D 8.2K

PIN NAME	PWR WELL	APPER/ PLTRST	USAGE	NOTE
GP50	MAIN	IN	REQ1#	
GP51	MAIN	IN	GNT1#	P/U 8.2K VCC3
GP52	MAIN	IN	REQ2#	P/U 8.2K VCC3
GP53	MAIN	IN	GNT2#	P/U 8.2K VCC3
GP54	MAIN	IN	REQ3#	P/U 8.2K VCC3
GP55	MAIN	IN	GNT3#	P/U 8.2K VCC3
GP56	STBY	IN	VCORE_OV5	
GP57	STBY	IN	VCORE_OV4	
GP58	STBY	IN	SPI_CS1#	
GP59	STBY		~USBOC_R	
GP60	STBY		LINKALRT#	

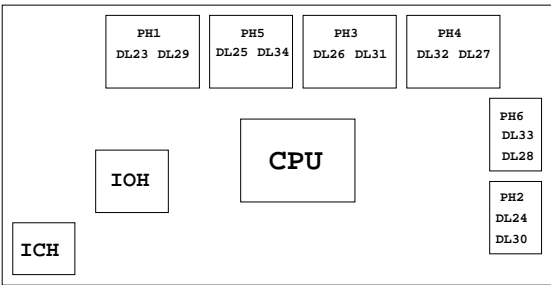
Super I/O GPIO Table

PIN NAME	USAGE	NOTE
SVC/PECI_RQT/GP14	-PECI_REQ	
PWROK1/GP13	PWROK1/ITE_PWROK	
KRST#/GP62	-KRST	
SO/GP50	-ICH_SPI_CS	
IRTX/GP47/CE2_N/JP7	CEB_N	
GP46/IRRX	-LAN2_DSM	
PSION#/GP42	-PSON	
PWROK2#/GP41	PECI_CTL	
PCIRST3#/GP10/VDIMM_STR_EN	-PCIE_RST	
RSMRST#CIRRX1/GP55	-RSMRST	
PME# /GP54	-LPCPME	
PD5/GP75/BUSS00	N/A	

PIN NAME	USAGE	NOTE
FAN_TAC2/GP52	FANIO2	
FAN_TAC3/GP37	FANIO3	
VIDO3/FAN_TAC4/GP25/DSR2#	FANIO4	
FAN_CTL2/GP51	FANPWM2	
FAN_CTL3/GP36	FANPWM3	
VID4/GP34	BEEP-	
VID3/GP33	TURBO1	
VID2/GP32	TURBO0	
VCORE_GOOD/VID6/GP63	CPUT_LED1_C	
VID5/GP35	CPUT_LED2_C	
VID1/GP31	CPUT_LED3_C	
VID0/GP30	-LAN1_DSM	NBT_LED1_C
SLCT/GP80	CPU_LED1_C	
PE/GP81	CPU_LED2_C	
BUSY/GP82	CPU_LED3_C	
PD3/GP73/BUSS11	SB_LED1_C	
PD4/GP74/BUSS12	SB_LED2_C	
VCORE_EN/VID7/GP64	IT_GP64	SB_LED3_C
PD0/GP70	NB_LED1_C	
PD1/GP71	NB_LED2_C	
PD2/GP72/BUSS10	NB_LED3_C	
GP22/SCK	LOW_PWR_1	
VIDO5/GP27/SIN2	LOW_PWR_2	
PCIRST2#/GP11	-PFMRST1	
PCIRST1#/GP12	-PFMRST2	
3VSB5W#/GP40	CSI_F0	BSEL166_1
SUSCH#/GP53	CSI_F1	BSEL166_2
GP23/SI	BSEL166_3/CsisBSL	
VIDO0/GP20/CTS2#	CPUT_LED1_C	BSEL166_4
GP65/VDDA_EN/GB_01	MB_ID2	
PD6/GP76/BUSS01	MB_ID3	
PD7/GP77/BUSS02	MB_ID4	
AFD#/GP86/SMB_C_R	⚡ PIN	FST_2X8
INIT#/GP85/SMBD_M	SEC_2x8	GTLREF_AD2
ACK#/GP83	DDR_LED1_C	
VIDO1/GP21/DCD2#	DDR_LED2_C	
STB#/GP87/SMB_C_M	DDR_LED3_C	
PWRON#GP44	VCORE_OV1	
PANSWH#/GP43	PWRBT5W	
KDAT/GP61	-PWRBT5W	
KCLK/GP60	KDAT	
MDAT/GP57	KCLK	
MACL/GP56	MDAT	
GP66/VLDT_EN/GB_02	NBT_LED1_C	MCLK
SVD/PCIRSTIN# /CIRTX/GP15	PWM2_CR	
KDAT/GP61	PWM2_CR	
GP67/CPU_PG/GB_03	EN_LOADLINE	IT_GP67/-EN_PWM2
SLIN#/GP84/SMBD_R	-EN_PWM2	
PSI_L/FAN_CLT5/CIRRX2/GP16	-THERM	
VIDO4/GP26/SOUT2	DDR18V_PH2_EN	
VIDO2/FAN_TAC5/GP24/DSR2#	DDR18V_LED	
VIDO6/GP17/RI2#	1_1V_PH_EN	
VIDO7/JP6/DTR2#	JP6	
PD5/GP75/BUSS00	SB_LED3_C	



PWM各相位的擺法如下：

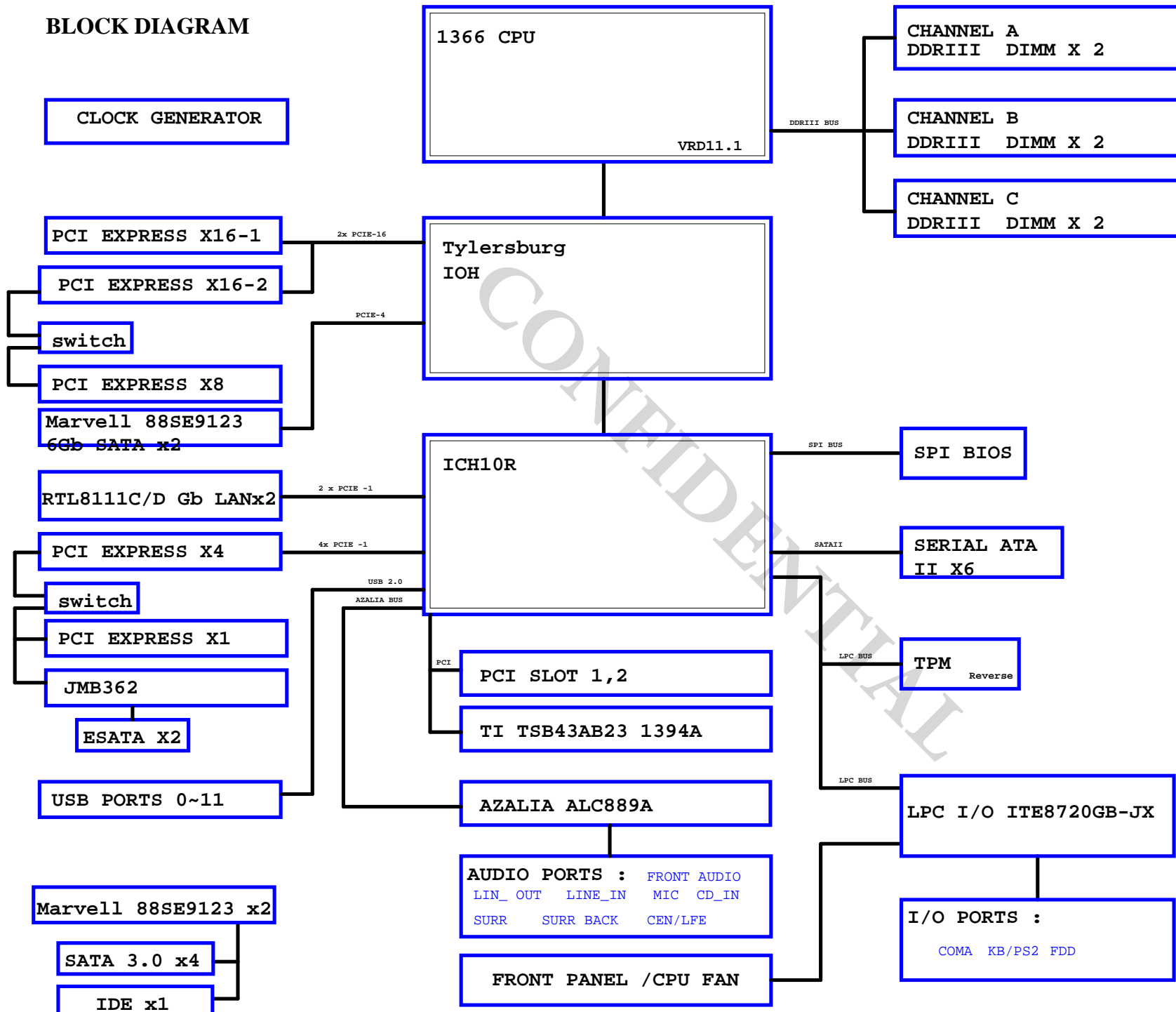


BIOS超電壓對應表：

線路圖名稱	BIOS選項
VTT_REF	DRAM Termination
CHAC-CHCC	address
DDR15V	DRAM voltage
VCC18_PLL	CPU PLL
VCCA1_1	CSI PLL
CPU Vcore	CPU Vcore
VCC15	ICH I/O
VCC1_1	IOH core
MCH_RAMVREF	MCH/DRAM Reference(不開放)
VTTD	CPU Termination
VCCA1_5	PCIE
CHA-CHC	Date
VCC1_1_ICH	ICH core

	3 pin FAN control	4 pin FAN control	FAN speed	Controller
CPU FAN	FANPWM1	FANPWM3	FANIO1	IT8718
	ICH_FAN_PWM2	ICH_FAN_PWM0	ICH_FAN_TACH0	ICH8
SYS FAN	FANPWM2	N/A	FANIO2	IT8718
	ICH_FAN_PWM1	N/A	ICH_FAN_TACH1	ICH8
PWR FAN	N/A	N/A	FANIO3	IT8718
			ICH_FAN_TACH2	ICH8

BLOCK DIAGRAM



LGA1366A

(18) DCLKA3 <-> E20 DDR0_CLK_P3
(18) DCLKA3 <-> E19 DDR0_CLK_N3
(18) DCLKA2 <-> F18 DDR0_CLK_P2
(18) DCLKA2 <-> F18 DDR0_CLK_N2
(18) DCLKA1 <-> D19 DDR0_CLK_P1
(18) DCLKA1 <-> D19 DDR0_CLK_N1
(18) DCLKA0 <-> K19 DDR0_CLK_P0
(18) DCLKA0 <-> K19 DDR0_CLK_N0

B8<-> DDR0_CS_7"
C11<-> DDR0_CS_6"
A7<-> DDR0_CS_5"
(18) -CSA5 <-> -CSA5 DDR0_CS_4"
(18) -CSA4 <-> -CSA4 DDR0_CS_3"
(18) -CSA3 <-> -CSA3 DDR0_CS_2"
(18) -CSA1 <-> -CSA1 DDR0_CS_1"
(18) -CSA0 <-> -CSA0 DDR0_CS_0"

A31<-> DDR0_ODT_7
C32<-> DDR0_ODT_6
C31<-> DDR0_ODT_5
C31<-> DDR0_ODT_4
C7<-> DDR0_ODT_3
MODT_A2<-> B11 DDR0_ODT_2
C2<-> DDR0_ODT_1
MODT_A0<-> F12 DDR0_ODT_0

(18) -SRASA <-> -SRASA DDR0_RAS*
(18) -SCASA <-> -SCASA DDR0_CAS*
(18) -SWEA <-> -SWEA DDR0_WE*

(18) SBAA2 <-> SBA2A DDR0_BA2
(18) SBA1 <-> SBA1A DDR0_BA1
(18) SBA0 <-> SBA0A DDR0_BA0

(18) CKEA3 <-> CKEA3 DDR0_CKE_3
(18) CKEA2 <-> CKEA2 DDR0_CKE_2
(18) CKEA1 <-> CKEA1 DDR0_CKE_1
(18) CKEA0 <-> CKEA0 DDR0_CKE_0

MAAA15<-> B29 DDR0_MA_15
MAAA14<-> A28 DDR0_MA_14
MAAA13<-> A10 DDR0_MA_13
MAAA12<-> B26 DDR0_MA_12
MAAA11<-> A26 DDR0_MA_11
MAAA10<-> B19 DDR0_MA_10
MAAA9<-> C25 DDR0_MA_9
MAAA8<-> B25 DDR0_MA_8
MAAA7<-> A25 DDR0_MA_7
MAAA6<-> C24 DDR0_MA_6
MAAA5<-> B24 DDR0_MA_5
MAAA4<-> C23 DDR0_MA_4
MAAA3<-> B22 DDR0_MA_3
MAAA2<-> D21 DDR0_MA_2
MAAA1<-> A20 DDR0_MA_1
MAA00<-> A20 DDR0_MA_0

B20<-> DDR0_MA_PAR

B33<-> DDR0_PAR_ERR_3"
A27<-> DDR0_PAR_ERR_2"
B26<-> DDR0_PAR_ERR_1"
D25<-> DDR0_PAR_ERR_0"

-DQSA0<-> T43 DDR0_DQS_P0
-DQSA0<-> U43 DDR0_DQS_N0
-DQSA1<-> L41 DDR0_DQS_P1
-DQSA1<-> M41 DDR0_DQS_N1
-DQSA2<-> F41 DDR0_DQS_P2
-DQSA2<-> G41 DDR0_DQS_N2
-DQSA3<-> B39 DDR0_DQS_P3
-DQSA3<-> C39 DDR0_DQS_N3
-DQSA4<-> E4 DDR0_DQS_P4
-DQSA4<-> F4 DDR0_DQS_N4
-DQSA5<-> K2 DDR0_DQS_P5
-DQSA5<-> L2 DDR0_DQS_N5
-DQSA6<-> R2 DDR0_DQS_P6
-DQSA6<-> S2 DDR0_DQS_N6
-DQSA7<-> W2 DDR0_DQS_P7
-DQSA7<-> X2 DDR0_DQS_N7
-DQSA8<-> D34 DDR0_DQS_P8
-DQSA8<-> E34 DDR0_DQS_N8

V43<-> DDR0_DQS_P9
V42<-> DDR0_DQS_N9
V42<-> DDR0_DQS_P10
V43<-> DDR0_DQS_N10
H42<-> DDR0_DQS_P11
H43<-> DDR0_DQS_N11
G43<-> DDR0_DQS_P12
D39<-> DDR0_DQS_N12
C39<-> DDR0_DQS_P13
D5<-> DDR0_DQS_N13
D4<-> DDR0_DQS_P14
J1<-> DDR0_DQS_N14
J2<-> DDR0_DQS_P15
P2<-> DDR0_DQS_N15
P1<-> DDR0_DQS_P16
V2<-> DDR0_DQS_N16
V3<-> DDR0_DQS_P17
B36<-> DDR0_DQS_N17

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DDR0_DQ_63<-> W4 MDA63
DDR0_DQ_62<-> V4 MDA62
DDR0_DQ_61<-> U3 MDA61
DDR0_DQ_60<-> L1 MDA60
DDR0_DQ_59<-> Y3 MDA59
DDR0_DQ_58<-> Y2 MDA58
DDR0_DQ_57<-> L4 MDA57
DDR0_DQ_56<-> T3 MDA56
DDR0_DQ_55<-> N3 MDA55
DDR0_DQ_54<-> R4 MDA54
DDR0_DQ_53<-> M3 MDA53
DDR0_DQ_52<-> T2 MDA52
DDR0_DQ_51<-> T1 MDA51
DDR0_DQ_50<-> N2 MDA50
DDR0_DQ_49<-> L2 MDA49
DDR0_DQ_48<-> L1 MDA48
DDR0_DQ_47<-> L3 MDA47
DDR0_DQ_46<-> L3 MDA46
DDR0_DQ_45<-> G1 MDA45
DDR0_DQ_44<-> M1 MDA44
DDR0_DQ_43<-> L1 MDA43
DDR0_DQ_42<-> H1 MDA42
DDR0_DQ_41<-> H2 MDA41
DDR0_DQ_40<-> F2 MDA40
DDR0_DQ_39<-> F3 MDA39
DDR0_DQ_38<-> C3 MDA38
DDR0_DQ_37<-> B6 MDA37
DDR0_DQ_36<-> G3 MDA36
DDR0_DQ_35<-> F1 MDA35
DDR0_DQ_34<-> C4 MDA34
DDR0_DQ_33<-> B6 MDA33
DDR0_DQ_32<-> B38 MDA32
DDR0_DQ_31<-> C38 MDA31
DDR0_DQ_30<-> D42 MDA30
DDR0_DQ_29<-> D41 MDA29
DDR0_DQ_28<-> D37 MDA28
DDR0_DQ_27<-> A38 MDA27
DDR0_DQ_26<-> C41 MDA26
DDR0_DQ_25<-> D40 MDA25
DDR0_DQ_24<-> F42 MDA24
DDR0_DQ_23<-> F43 MDA23
DDR0_DQ_22<-> J41 MDA22
DDR0_DQ_21<-> J42 MDA21
DDR0_DQ_20<-> E43 MDA20
DDR0_DQ_19<-> E42 MDA19
DDR0_DQ_18<-> H43 MDA18
DDR0_DQ_17<-> H41 MDA17
DDR0_DQ_16<-> L42 MDA16
DDR0_DQ_15<-> L43 MDA15
DDR0_DQ_14<-> P41 MDA14
DDR0_DQ_13<-> P42 MDA13
DDR0_DQ_12<-> K43 MDA12
DDR0_DQ_11<-> K42 MDA11
DDR0_DQ_10<-> N43 MDA10
DDR0_DQ_9<-> N41 MDA9
DDR0_DQ_8<-> T42 MDA8
DDR0_DQ_7<-> U41 MDA7
DDR0_DQ_6<-> W42 MDA6
DDR0_DQ_5<-> W40 MDA5
DDR0_DQ_4<-> R42 MDA4
DDR0_DQ_3<-> R43 MDA3
DDR0_DQ_2<-> V41 MDA2
DDR0_DQ_1<-> W41 MDA1
DDR0_DQ_0<-> W41 MDA0

C34<-> SACB7
B34<-> SACB6
A37<-> SACB5
C37<-> SACB4
C33<-> SACB3
F32<-> SACB2
A36<-> SACB1
C36<-> SACB0

AA8<-> DDR_COMP0 R3872 100/4/1

D32<-> DDR3_RST0 (18)

(18) MDA[0..63] <-> MDA[0..63]
(18) MAA[0..15] <-> MAA[0..15]

MODT_A[0..3] <-> MODT_A[0..3] (18)

(18) DQSA[0..8] <-> DQSA[0..8]
(18) -DQSA[0..8] <-> -DQSA[0..8]

(18) SACB[0..7] <-> SACB[0..7]

MDB[0..63] <-> MDB[0..63]
MAAB[0..15] <-> MAAB[0..15]

MODT_B[0..3] <-> MODT_B[0..3] (19)

(19) DQSB[0..8] <-> DQSB[0..8]
(19) -DQSB[0..8] <-> -DQSB[0..8]

(19) SBCB[0..7] <-> SBCB[0..7]

(19) DCLKB3 <-> H18 DDR1_CLK_P3
(19) DCLKB3 <-> H18 DDR1_CLK_N3
(19) DCLKB2 <-> K18 DDR1_CLK_P2
(19) DCLKB2 <-> K18 DDR1_CLK_N2
(19) DCLKB1 <-> G19 DDR1_CLK_P1
(19) DCLKB1 <-> G19 DDR1_CLK_N1
(19) DCLKB0 <-> C21 DDR1_CLK_P0
(19) DCLKB0 <-> C21 DDR1_CLK_N0

E19<-> DDR1_CS_7"
C14<-> DDR1_CS_6"
(19) -CSB5 <-> -CSB5 DDR1_CS_5"
(19) -CSB4 <-> -CSB4 DDR1_CS_4"
(19) -CSB3 <-> -CSB3 DDR1_CS_3"
(19) -CSB1 <-> -CSB1 DDR1_CS_2"
(19) -CSB0 <-> -CSB0 DDR1_CS_1"
(19) -CSB0 <-> -CSB0 DDR1_CS_0"

G28<-> DDR1_ODT_7
H29<-> DDR1_ODT_6
F28<-> DDR1_ODT_5
F28<-> DDR1_ODT_4
MODT_B3<-> F11 DDR1_ODT_3
MODT_B2<-> D14 DDR1_ODT_2
MODT_B1<-> C8 DDR1_ODT_1
MODT_B0<-> D11 DDR1_ODT_0

(19) -SRASB <-> -SRASB DDR1_RAS*
(19) -SCASB <-> -SCASB DDR1_CAS*
(19) -SWEB <-> -SWEB DDR1_WE*

(19) SBA2 <-> SBA2A DDR1_BA_2
(19) SBA1 <-> SBA1A DDR1_BA_1
(19) SBA0 <-> SBA0A DDR1_BA_0

(19) CKEB3 <-> CKEB3 DDR1_CKE_3
(19) CKEB2 <-> CKEB2 DDR1_CKE_2
(19) CKEB1 <-> CKEB1 DDR1_CKE_1
(19) CKEB0 <-> CKEB0 DDR1_CKE_0

MAAB15<-> F26 DDR1_MA_15
MAAB14<-> H26 DDR1_MA_14
MAAB13<-> B14 DDR1_MA_13
MAAB12<-> E24 DDR1_MA_12
MAAB11<-> E23 DDR1_MA_11
MAAB10<-> H14 DDR1_MA_10
MAAB9<-> G24 DDR1_MA_9
MAAB8<-> E22 DDR1_MA_8
MAAB7<-> D22 DDR1_MA_7
MAAB6<-> J27 DDR1_MA_6
MAAB5<-> F22 DDR1_MA_5
MAAB4<-> K28 DDR1_MA_4
MAAB3<-> L28 DDR1_MA_3
MAAB2<-> J17 DDR1_MA_2
MAAB1<-> J16 DDR1_MA_1
MAAB0<-> J14 DDR1_MA_0

D20<-> DDR1_MA_PAR

F27<-> DDR1_PAR_ERR_3"
F26<-> DDR1_PAR_ERR_2"
E25<-> DDR1_PAR_ERR_1"
C22<-> DDR1_PAR_ERR_0"

DQSB0<-> Y38 DDR1_DQS_P0
-DQSB0<-> Y37 DDR1_DQS_N0
DQSB1<-> R36 DDR1_DQS_P1
-DQSB1<-> R37 DDR1_DQS_N1
DQSB2<-> L36 DDR1_DQS_P2
-DQSB2<-> L36 DDR1_DQS_N2
DQSB3<-> L30 DDR1_DQS_P3
-DQSB3<-> L31 DDR1_DQS_N3
DQSB4<-> E7 DDR1_DQS_P4
-DQSB4<-> D7 DDR1_DQS_N4
DQSB5<-> G6 DDR1_DQS_P5
-DQSB5<-> G6 DDR1_DQS_N5
DQSB6<-> L6 DDR1_DQS_P6
-DQSB6<-> L6 DDR1_DQS_N6
DQSB7<-> Y9 DDR1_DQS_P7
-DQSB7<-> Y9 DDR1_DQS_N7
DQSB8<-> G33 DDR1_DQS_P8
-DQSB8<-> G34 DDR1_DQS_N8

AA40<-> DDR1_DQS_P9
AA41<-> DDR1_DQS_N9
F36<-> DDR1_DQS_P10
P37<-> DDR1_DQS_N10
L37<-> DDR1_DQS_P11
K37<-> DDR1_DQS_N11
K34<-> DDR1_DQS_P12
K33<-> DDR1_DQS_N12
F3<-> DDR1_DQS_P13
F7<-> DDR1_DQS_N13
H7<-> DDR1_DQS_P14
J7<-> DDR1_DQS_N14
M5<-> DDR1_DQS_P15
Y4<-> DDR1_DQS_N15
Y5<-> DDR1_DQS_P16
Y5<-> DDR1_DQS_N16
F36<-> DDR1_DQS_P17
E36<-> DDR1_DQS_N17

CPU-SK/1366P/S/15

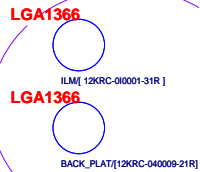
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DDR1_DQ_63<-> W9 MDB63
DDR1_DQ_62<-> A27 MDB62
DDR1_DQ_61<-> W5 MDB61
DDR1_DQ_60<-> W9 MDB60
DDR1_DQ_59<-> W10 MDB59
DDR1_DQ_58<-> Y10 MDB58
DDR1_DQ_57<-> W7 MDB57
DDR1_DQ_56<-> W6 MDB56
DDR1_DQ_55<-> R7 MDB55
DDR1_DQ_54<-> R6 MDB54
DDR1_DQ_53<-> J4 MDB53
DDR1_DQ_52<-> T5 MDB52
DDR1_DQ_51<-> R5 MDB51
DDR1_DQ_50<-> K5 MDB50
DDR1_DQ_49<-> K4 MDB49
DDR1_DQ_48<-> J5 MDB48
DDR1_DQ_47<-> G5 MDB47
DDR1_DQ_46<-> H9 MDB46
DDR1_DQ_45<-> G9 MDB45
DDR1_DQ_44<-> H4 MDB44
DDR1_DQ_43<-> G4 MDB43
DDR1_DQ_42<-> J6 MDB42
DDR1_DQ_41<-> F6 MDB41
DDR1_DQ_40<-> H8 MDB40
DDR1_DQ_39<-> D6 MDB39
DDR1_DQ_38<-> D6 MDB38
DDR1_DQ_37<-> F10 MDB37
DDR1_DQ_36<-> F5 MDB36
DDR1_DQ_35<-> F5 MDB35
DDR1_DQ_34<-> E8 MDB34
DDR1_DQ_33<-> E9 MDB33
DDR1_DQ_32<-> K30 MDB32
DDR1_DQ_31<-> L32 MDB31
DDR1_DQ_30<-> H34 MDB30
DDR1_DQ_29<-> H34 MDB29
DDR1_DQ_28<-> J32 MDB28
DDR1_DQ_27<-> J32 MDB27
DDR1_DQ_26<-> K32 MDB26
DDR1_DQ_25<-> L33 MDB25
DDR1_DQ_24<-> H33 MDB24
DDR1_DQ_23<-> H36 MDB23
DDR1_DQ_22<-> L36 MDB22
DDR1_DQ_21<-> M36 MDB21
DDR1_DQ_20<-> N34 MDB20
DDR1_DQ_19<-> M35 MDB19
DDR1_DQ_18<-> K35 MDB18
DDR1_DQ_17<-> M34 MDB17
DDR1_DQ_16<-> M35 MDB16
DDR1_DQ_15<-> N38 MDB15
DDR1_DQ_14<-> N37 MDB14
DDR1_DQ_13<-> R36 MDB13
DDR1_DQ_12<-> R34 MDB12
DDR1_DQ_11<-> N39 MDB11
DDR1_DQ_10<-> P39 MDB10
DDR1_DQ_9<-> P35 MDB9
DDR1_DQ_8<-> P34 MDB8
DDR1_DQ_7<-> Y39 MDB7
DDR1_DQ_6<-> Y40 MDB6
DDR1_DQ_5<-> AB36 MDB5
DDR1_DQ_4<-> AB36 MDB4
DDR1_DQ_3<-> Y34 MDB3
DDR1_DQ_2<-> Y35 MDB2
DDR1_DQ_1<-> AB36 MDB1
DDR1_DQ_0<-> AA37 MDB0

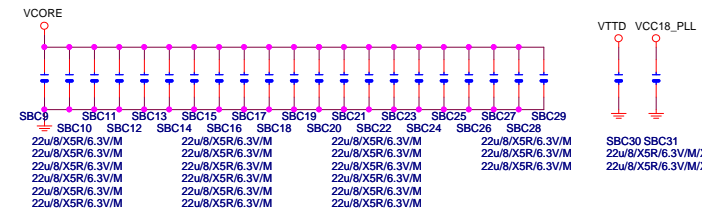
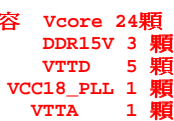
G35<-> SBCB7
F34<-> SBCB6
F37<-> SBCB5
E37<-> SBCB4
G36<-> SBCB3
F36<-> SBCB2
F36<-> SBCB1
D36<-> SBCB0

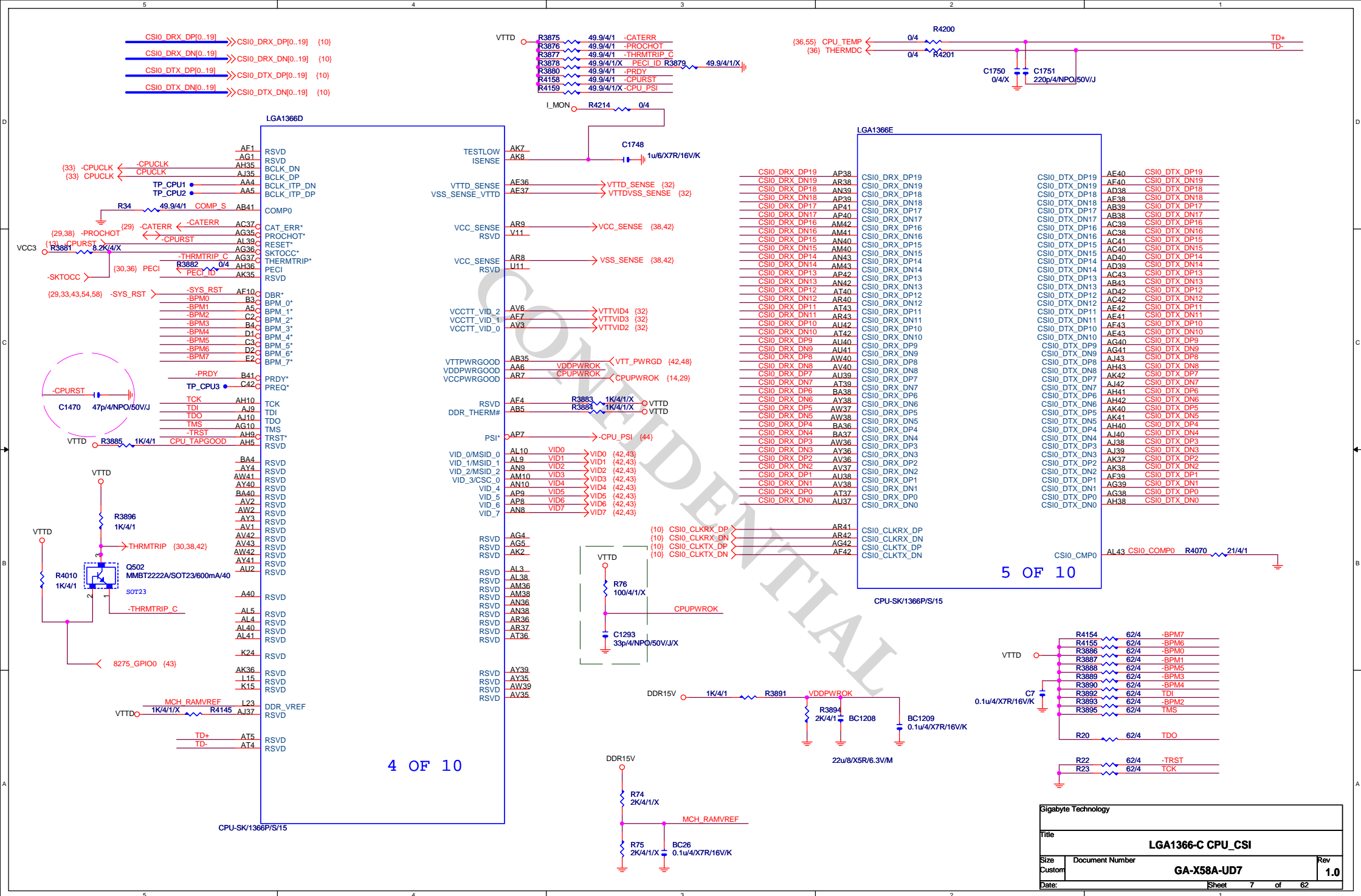
Y7<-> DDR_COMP1

D29<-> DDR3_RST1 (19)



CPU-SK/1366P/S/15





LGA1366I

B42 VSS
B37 VSS
B2 VSS
A41 VSS
A39 VSS
A35 VSS
A6 VSS
A4 VSS
C6 VSS
E6 VSS
E1 VSS
D43 VSS
D38 VSS
D33 VSS
D8 VSS
D3 VSS
C43 VSS
C40 VSS
C35 VSS
E36 VSS
E41 VSS
F4 VSS
F9 VSS
F29 VSS
F34 VSS
F39 VSS
G2 VSS
G7 VSS
G12 VSS
G32 VSS
G37 VSS
G42 VSS
H5 VSS
H10 VSS
H30 VSS
H35 VSS
BA39 VSS
BA35 VSS
BA29 VSS
BA26 VSS
BA20 VSS
BA17 VSS
BA14 VSS
BA11 VSS
BA5 VSS
BA3 VSS
AY42 VSS
AY37 VSS
AY29 VSS
AY26 VSS
AY23 VSS
AY32 VSS
AY22 VSS
AY20 VSS
AY17 VSS
AY14 VSS
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AP20 VSS
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AP14 VSS
AP11 VSS
AP10 VSS
AP6 VSS
AP5 VSS
AP1 VSS
AN41 VSS
AN37 VSS
AN35 VSS
AN32 VSS
AN29 VSS
AN26 VSS
AN23 VSS
AN22 VSS
AN20 VSS
AN17 VSS
AN14 VSS
AN11 VSS

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CPU-SK/1366P/S/15

LGA1366J

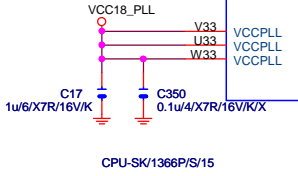
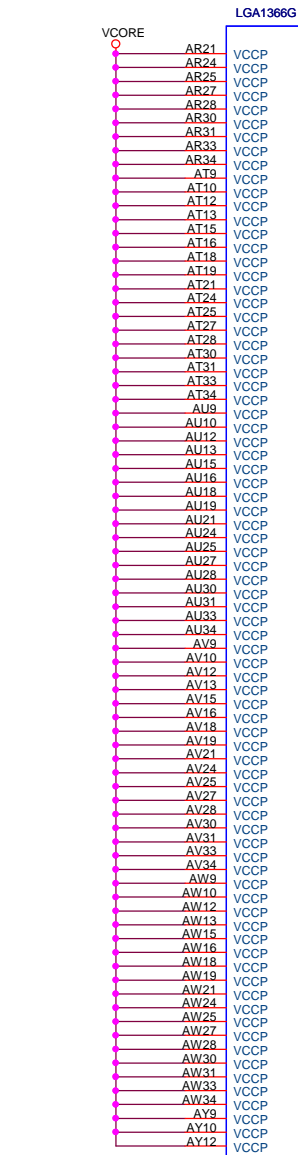
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AN3 VSS
AM39 VSS
AM37 VSS
AM35 VSS
AM32 VSS
AM29 VSS
AM26 VSS
AM23 VSS
AM22 VSS
AM20 VSS
AM17 VSS
AM14 VSS
AM11 VSS
AM9 VSS
AM5 VSS
AL42 VSS
AL37 VSS
AL36 VSS
AL35 VSS
AL32 VSS
AL29 VSS
AL26 VSS
AL23 VSS
AL22 VSS
AL20 VSS
AL17 VSS
AL14 VSS
AL11 VSS
AL7 VSS
AL2 VSS
AL1 VSS
AK43 VSS
AK39 VSS
AK34 VSS
AK32 VSS
AK29 VSS
AK26 VSS
AK23 VSS
AK22 VSS
AK20 VSS
AK17 VSS
AK14 VSS
AK10 VSS
AK9 VSS
AK3 VSS
AJ41 VSS
AJ36 VSS
AJ34 VSS
AJ5 VSS
AH39 VSS
AH37 VSS
AH34 VSS
AH7 VSS
AH1 VSS
AG43 VSS
AG33 VSS
AG11 VSS
AG3 VSS
AG3 VSS
AF41 VSS
AF38 VSS
AF35 VSS
AF5 VSS
AE39 VSS
AE7 VSS
AE2 VSS
AD43 VSS
AD41 VSS
AD37 VSS
AD33 VSS
AD11 VSS
AC36 VSS
AC9 VSS
AC7 VSS
AC5 VSS
AC2 VSS
AB42 VSS

AB40 VSS
AB37 VSS
AB7 VSS
AB4 VSS
AA39 VSS
AA38 VSS
AA34 VSS
AA9 VSS
AA3 VSS
Y41 VSS
Y36 VSS
Y33 VSS
Y11 VSS
Y6 VSS
Y1 VSS
W43 VSS
W38 VSS
W8 VSS
W3 VSS
V40 VSS
V35 VSS
V10 VSS
V5 VSS
U42 VSS
U37 VSS
U7 VSS
U2 VSS
T39 VSS
T34 VSS
T9 VSS
T4 VSS
R41 VSS
R36 VSS
R6 VSS
R1 VSS
P43 VSS
P38 VSS
P33 VSS
P11 VSS
P8 VSS
P3 VSS
N40 VSS
N35 VSS
N10 VSS
N5 VSS
M42 VSS
M37 VSS
M32 VSS
M30 VSS
M28 VSS
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M18 VSS
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M2 VSS
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L9 VSS
L4 VSS
K41 VSS
K36 VSS
K31 VSS
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J33 VSS
J13 VSS
J8 VSS
J3 VSS
H40 VSS

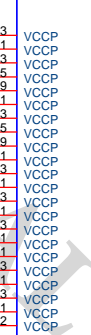
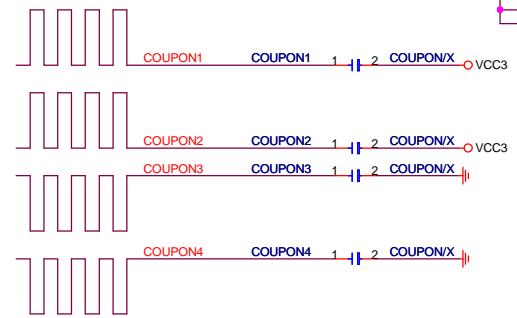
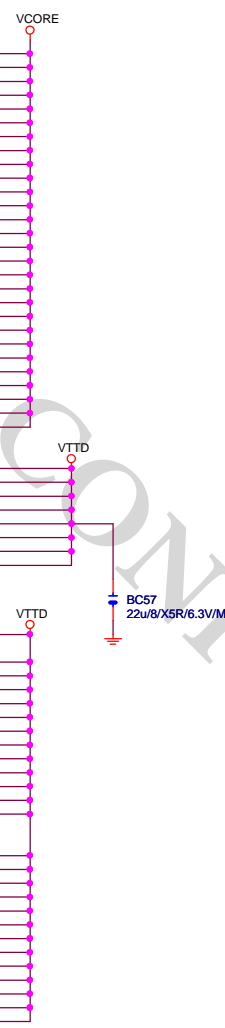
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CPU-SK/1366P/S/15

Gigabyte Technology		
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LGA1366-D GND		
Size	Document Number	Rev
Custom	GA-X58A-UD7	1.0
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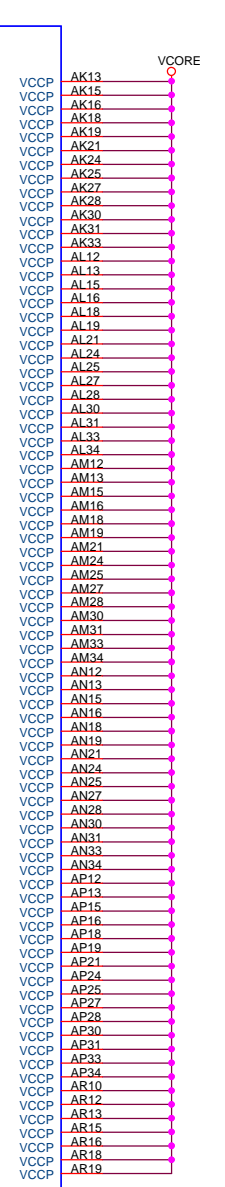


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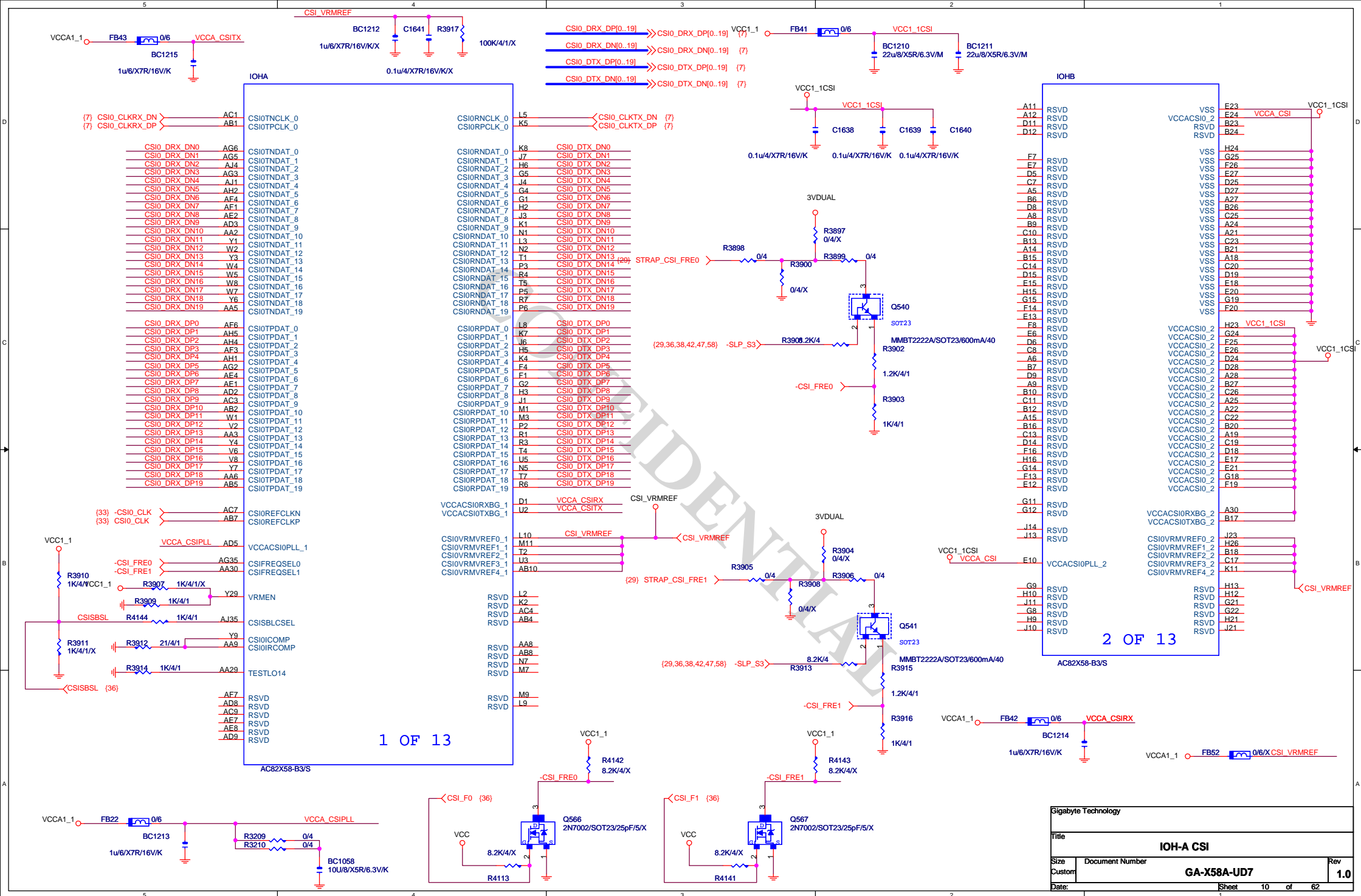


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Gigabyte Technology		
Title		
LGA1366-E CPU_PWR		
Size	Document Number	Rev
Custom	GA-X58A-UD7	1.0
Date:	Sheet	9 of 62

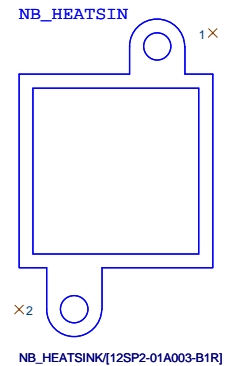
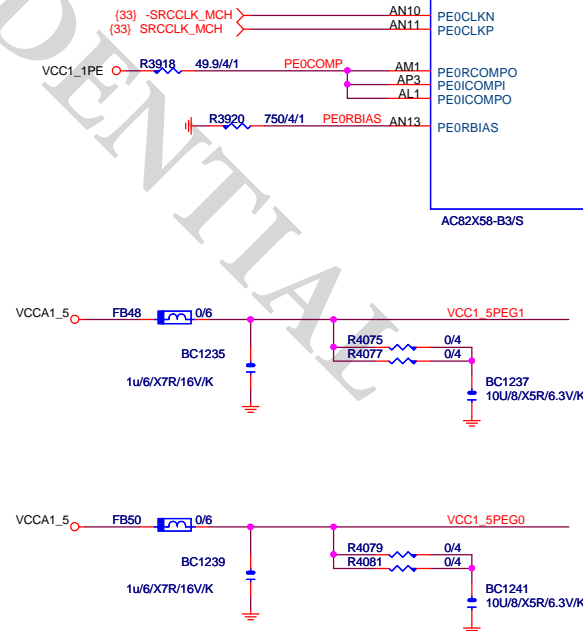
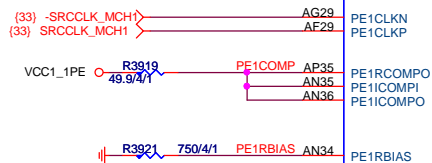
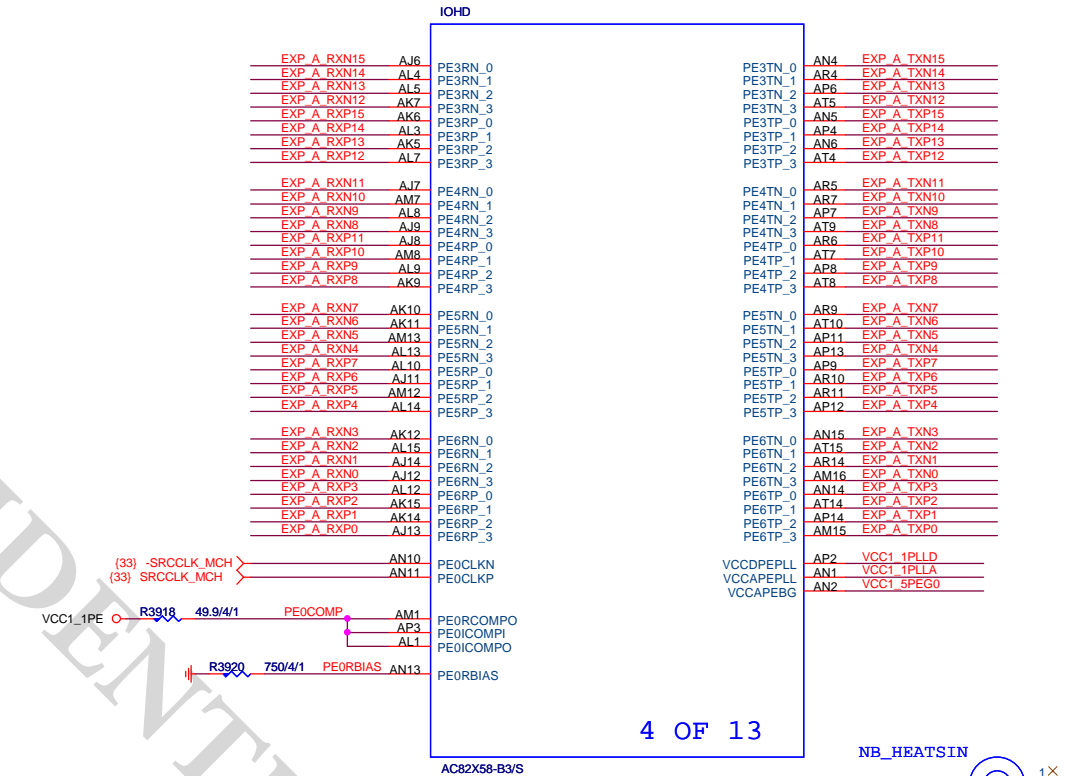


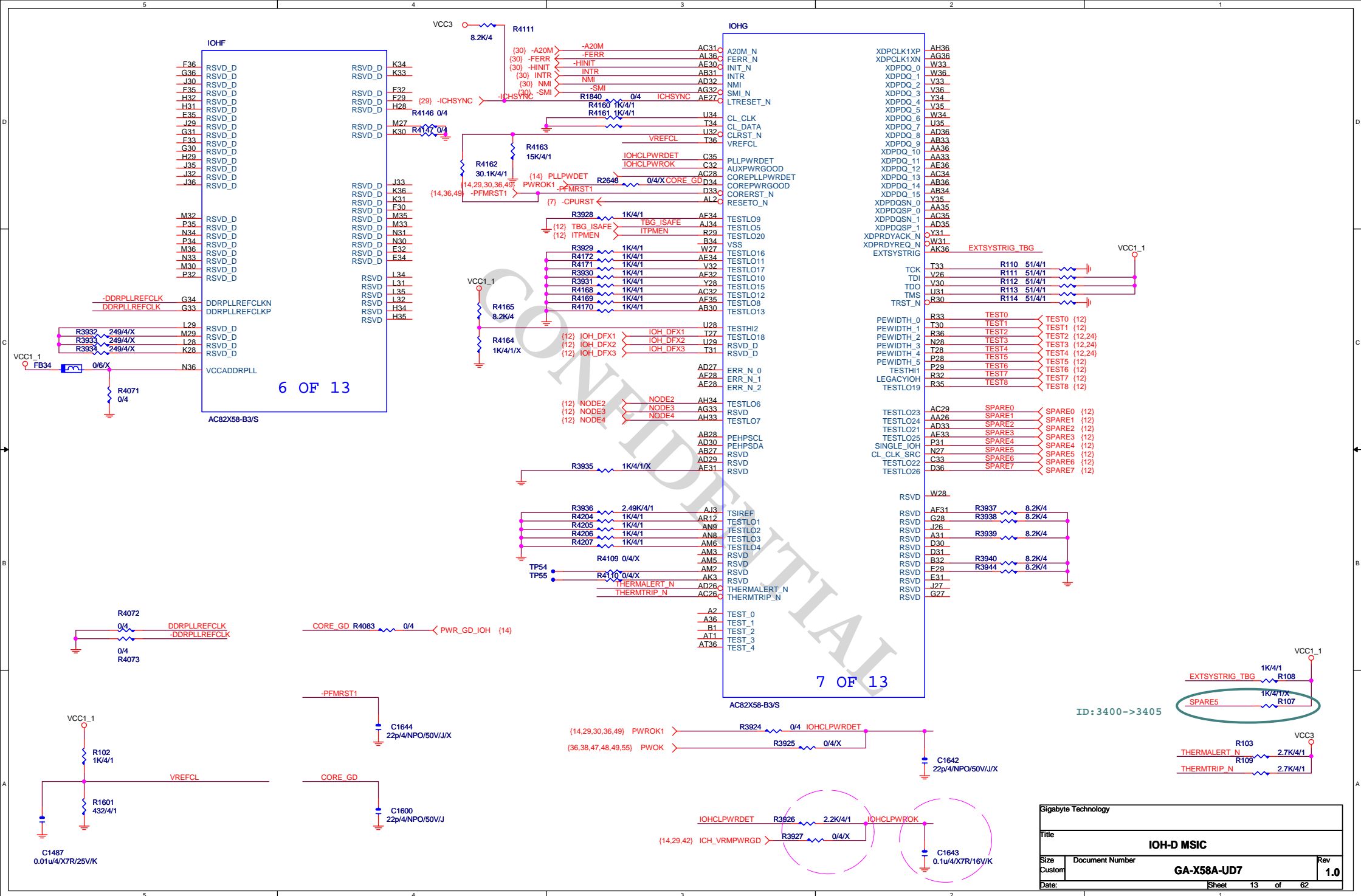
EXP_A_TXP[0..7] >>> EXP_A_TXP[0..7] (23)
EXP_A_TXN[0..7] >>> EXP_A_TXN[0..7] (23)
EXP_A_RXP[0..7] >>> EXP_A_RXP[0..7] (23)
EXP_A_RXN[0..7] >>> EXP_A_RXN[0..7] (23)

EXP_A_TXP[8..15] >>> EXP_A_TXP[8..15] (22)
EXP_A_TXN[8..15] >>> EXP_A_TXN[8..15] (22)
EXP_A_RXP[8..15] >>> EXP_A_RXP[8..15] (22)
EXP_A_RXN[8..15] >>> EXP_A_RXN[8..15] (22)

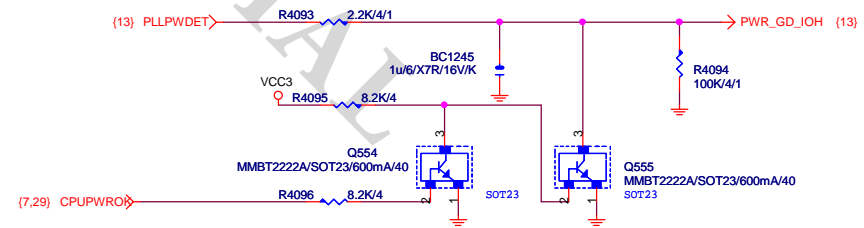
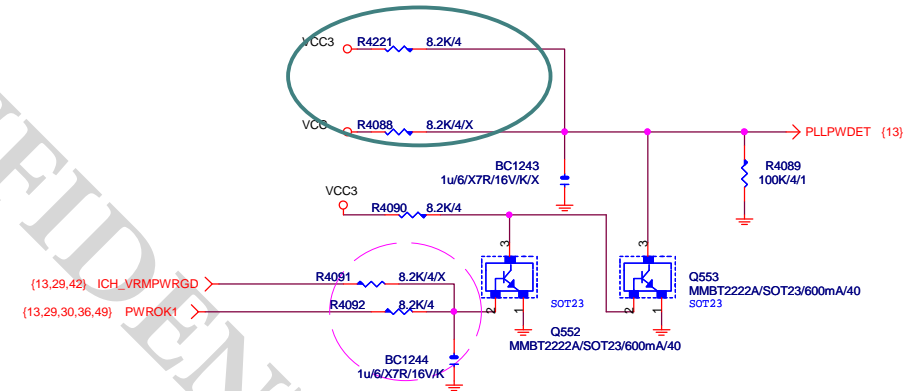
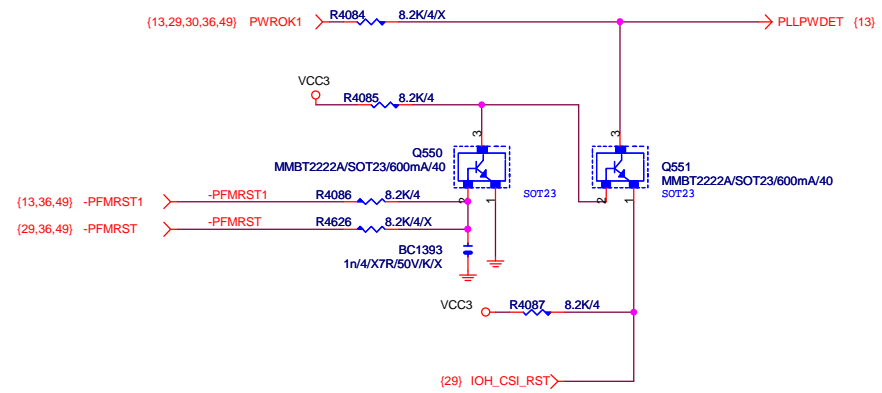
EXP_C_TXP[0..7] >>> EXP_C_TXP[0..7] (26)
EXP_C_TXN[0..7] >>> EXP_C_TXN[0..7] (26)
EXP_C_RXP[0..7] >>> EXP_C_RXP[0..7] (26)
EXP_C_RXN[0..7] >>> EXP_C_RXN[0..7] (26)

EXP_C_TXP[8..15] >>> EXP_C_TXP[8..15] (25)
EXP_C_TXN[8..15] >>> EXP_C_TXN[8..15] (25)
EXP_C_RXP[8..15] >>> EXP_C_RXP[8..15] (25)
EXP_C_RXN[8..15] >>> EXP_C_RXN[8..15] (25)

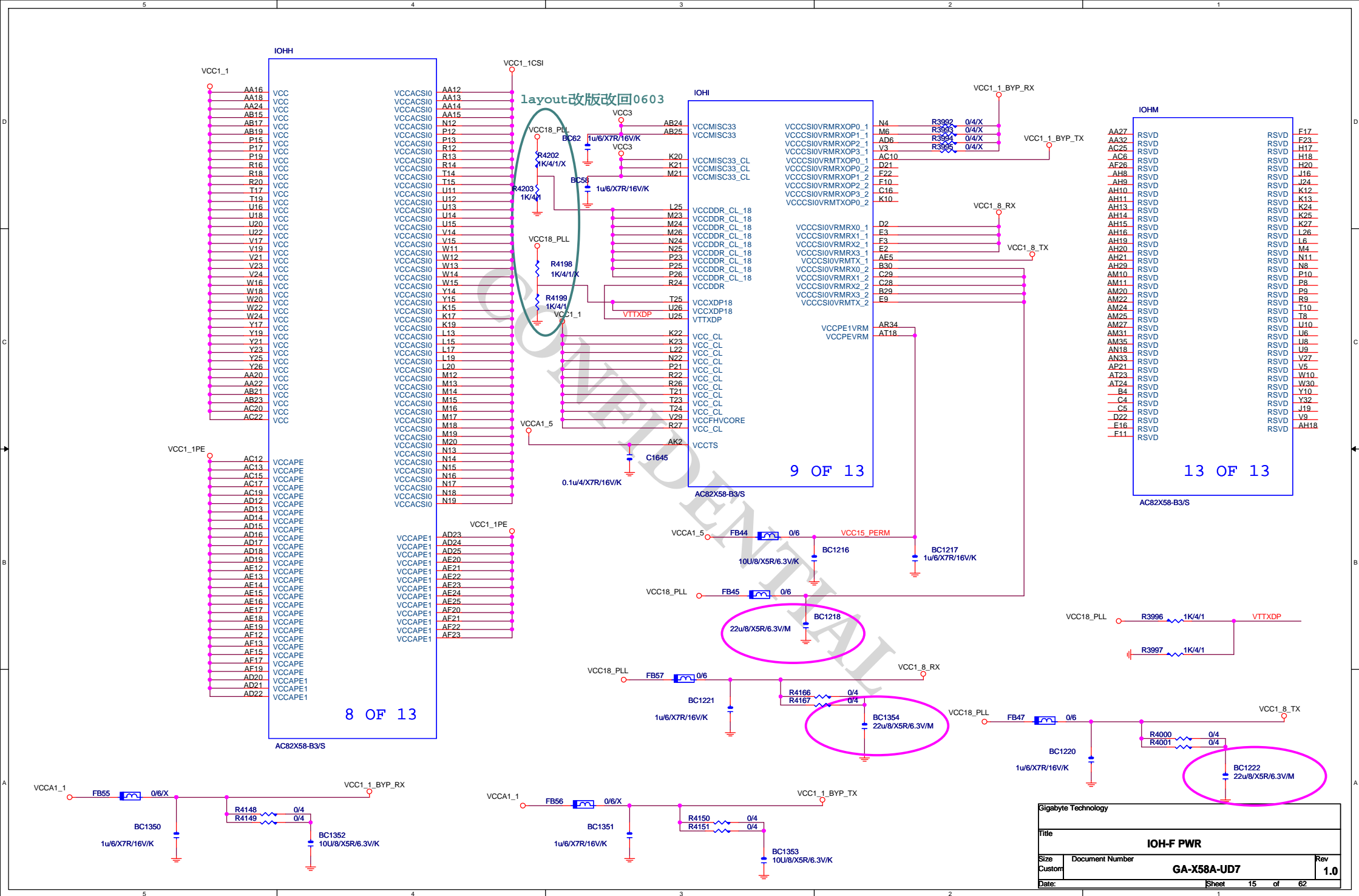




Gigabyte Technology			
Title IOH-D MSIC			
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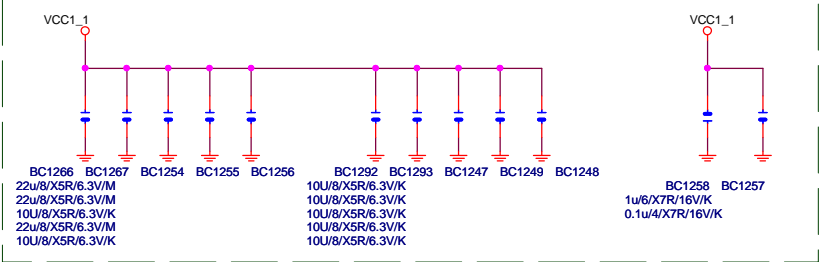


Gigabyte Technology			
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IOH-E_MISC_STRAP			
Size	Document Number		Rev
Custom	GA-X58A-UD7		1.0
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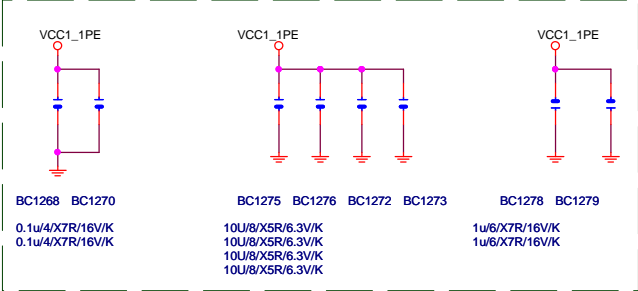


TOP Side

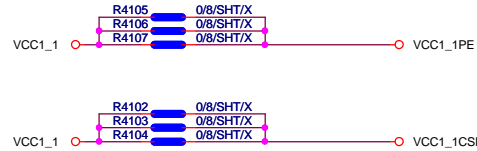
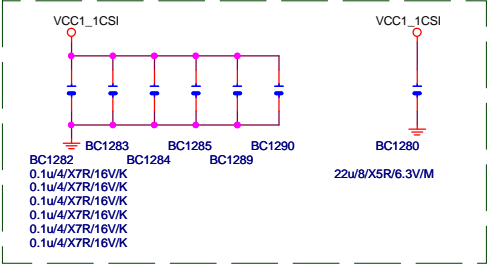
VCC1_1



VCC1_1PE

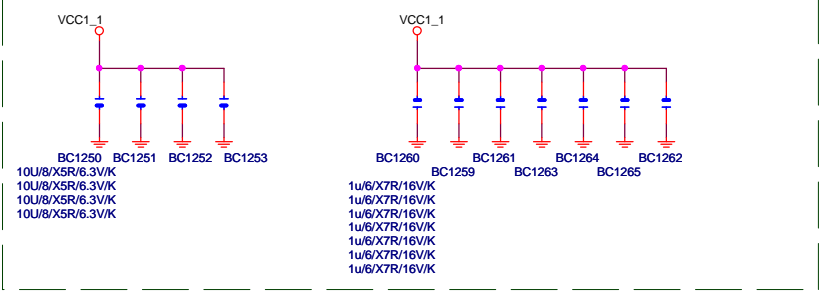


VCC1_1CSI

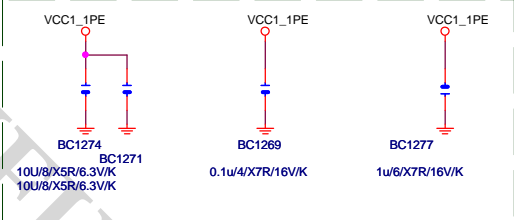


Bottom Side

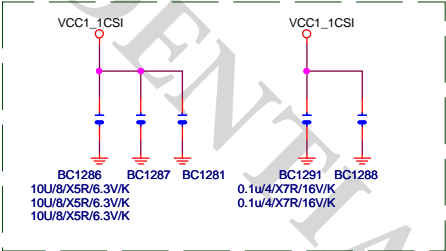
VCC1_1



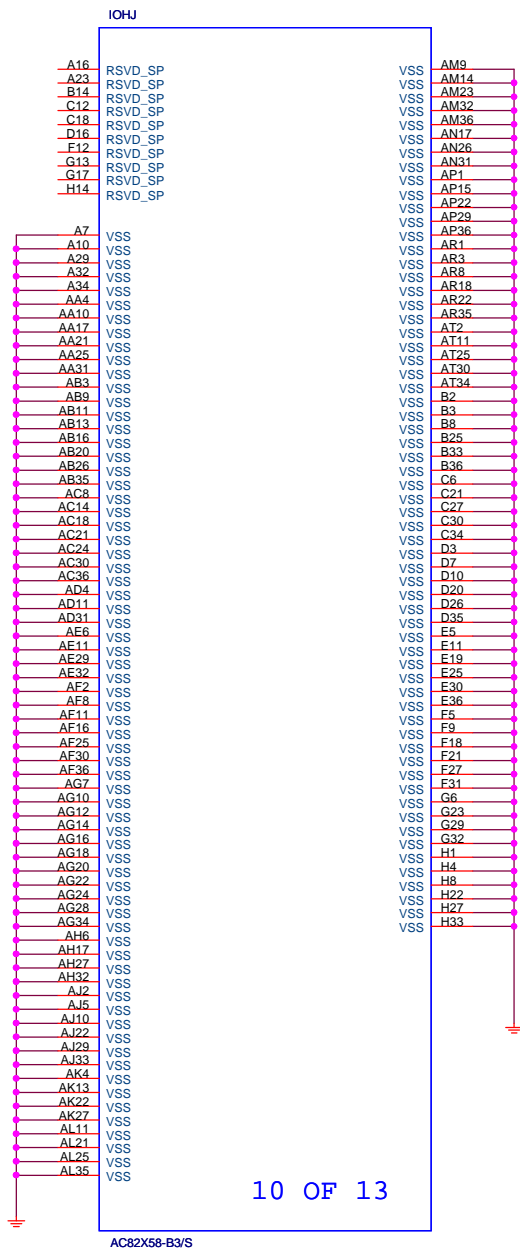
VCC1_1PE



VCC1_1CSI

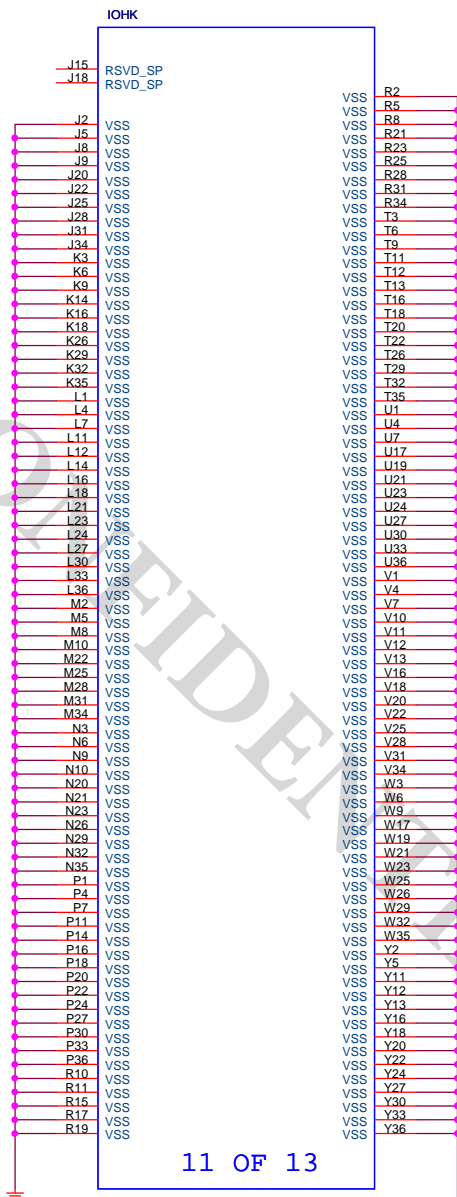


Gigabyte Technology			
Title			
IOH-G PWR_1			
Size		Document Number	
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		1.0	
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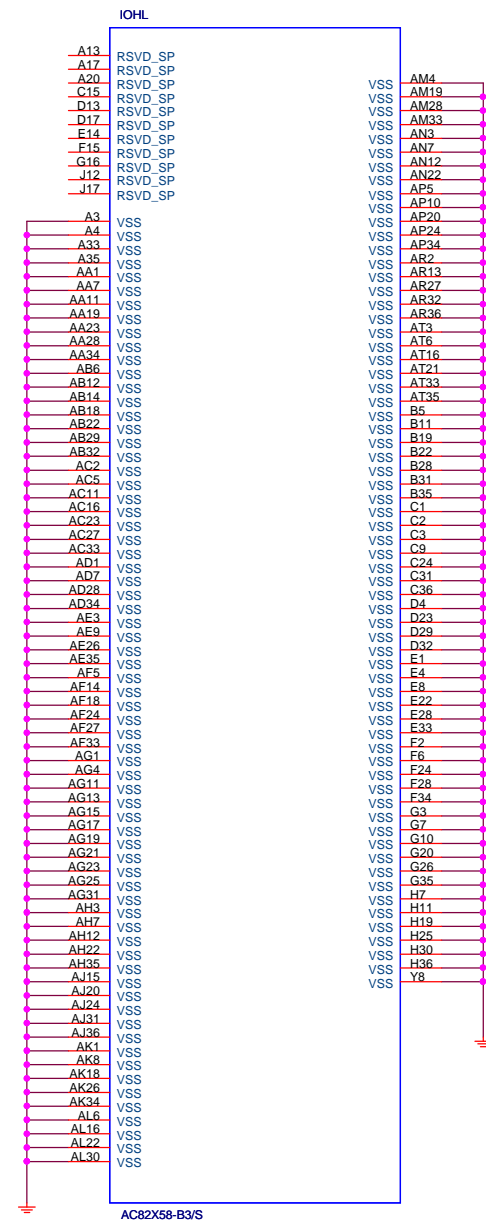
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AC82X58-B3/S



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AC82X58-B3/S



AC82X58-B3/S

Gigabyte Technology			
Title IOH-H GND			
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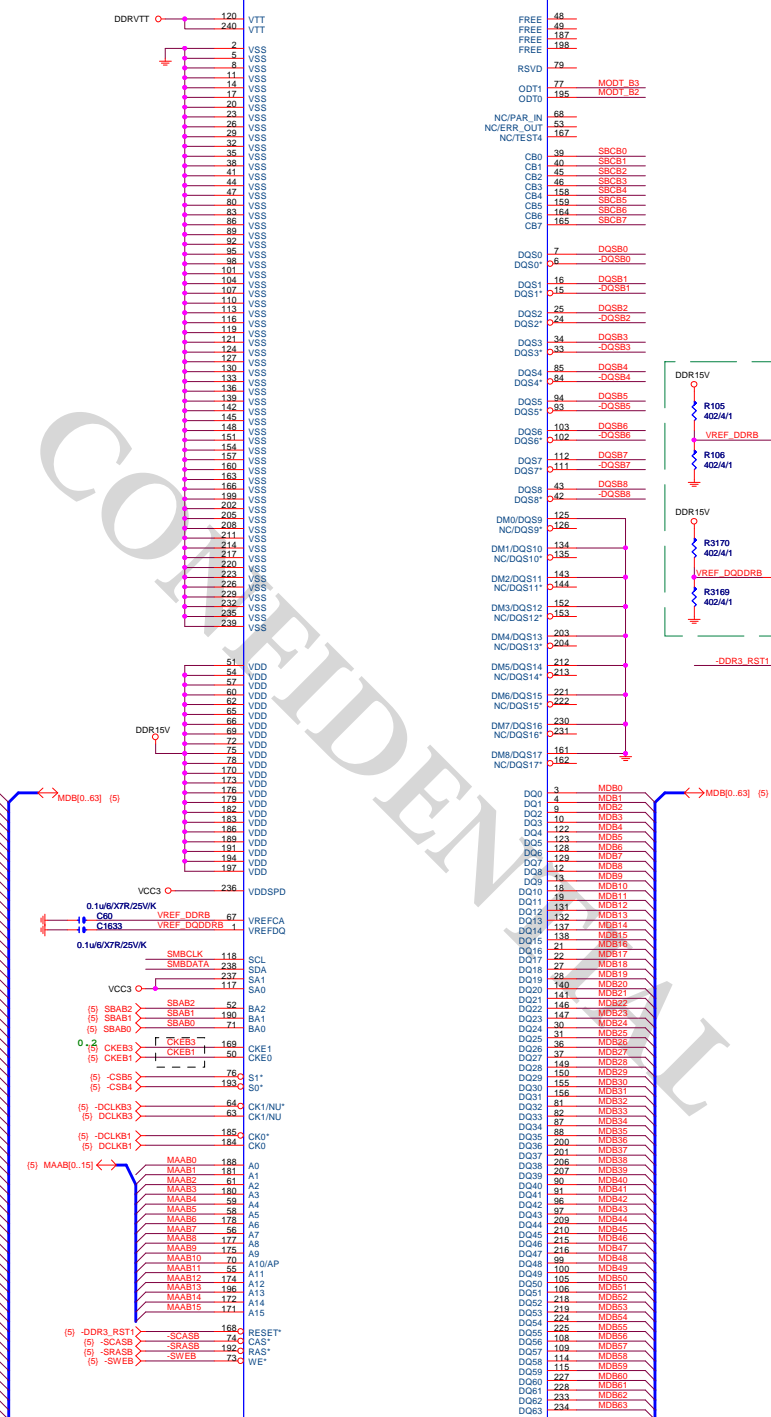
DDR3_3



slot 白色

DDR3/240/WH/V/D

DDR3_4



slot 蓝色

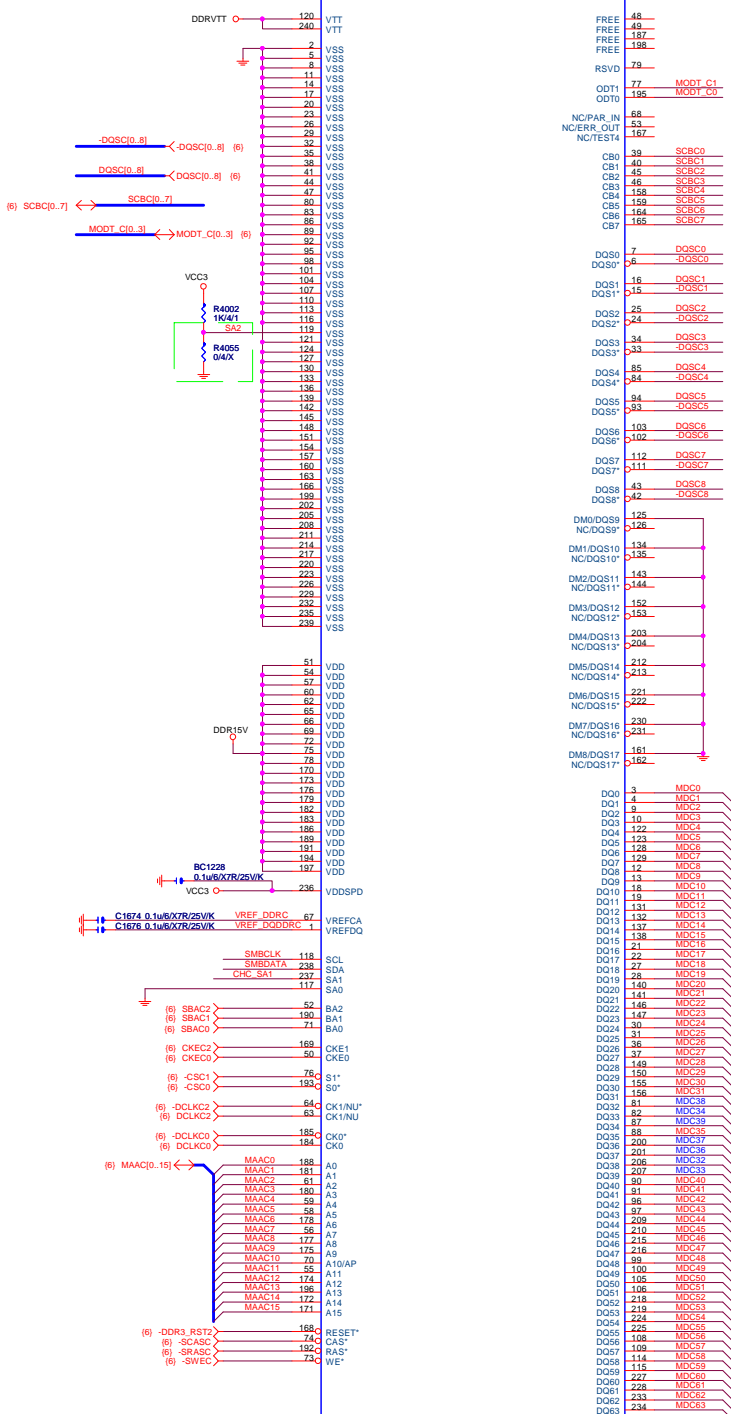
DDR3/240/BU/V/D

(18,20,22,24,25,27,29,32,34,35,43,57,58,59,61) SMBCLK SMBCLK
(18,20,22,24,25,27,29,32,34,35,43,57,58,59,61) SMBDATA SMBDATA

Sigbyte Technology

Title	DDR3 CHANNEL B 3	Rev	1.0
Size	Document Number	GA-X58A-UD7	
Custom			
Date:	Sheet	19	of 62

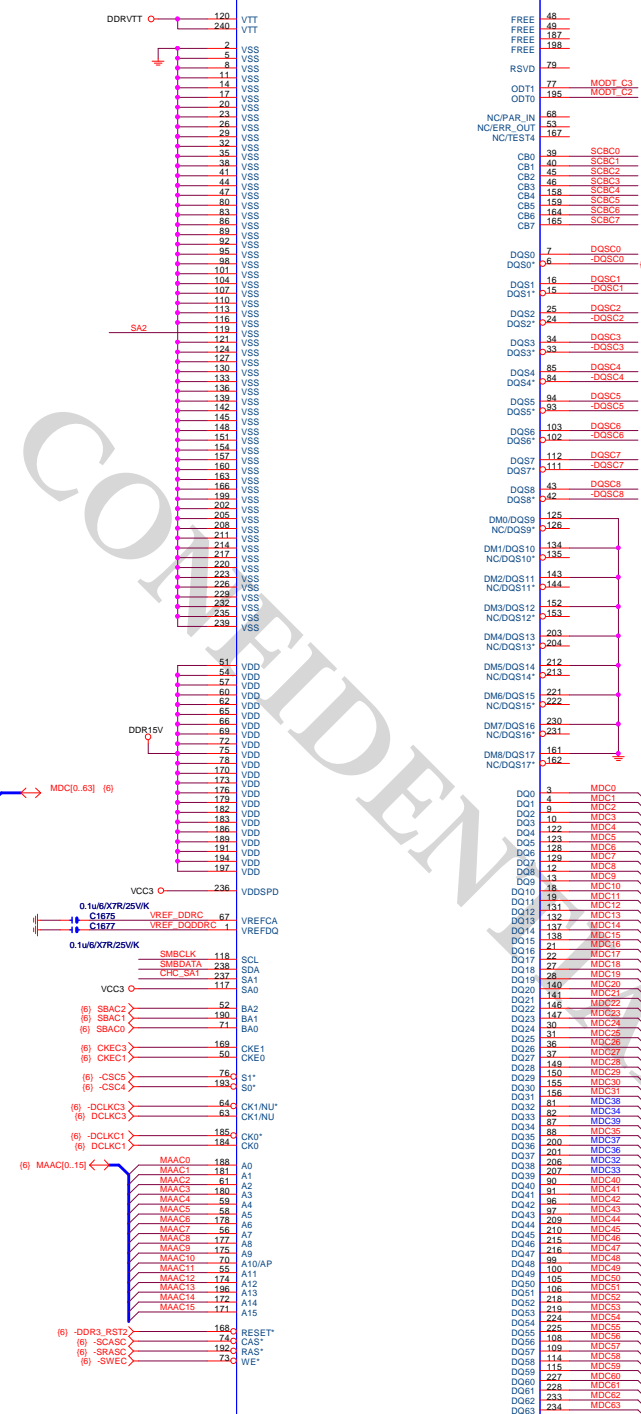
DDR3_5



DDR3/240/WH/VA/D

slot白色

DDR3_6



DDR3/240/BLU/VA/D

slot蓝色

DDR3_6



Signature Technology

File: **DDR3 CHANNEL C 5,6**

Size: **GA-X58A-UD7**

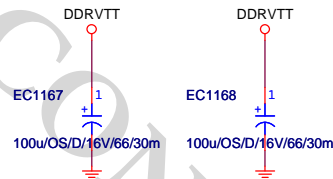
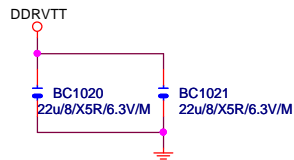
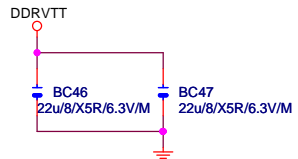
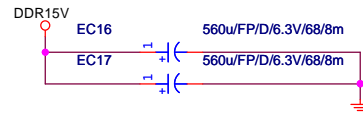
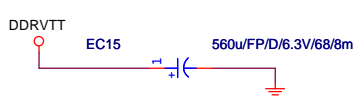
Customer: **Rev 1.0**

Date: **Sheet 20 of 62**

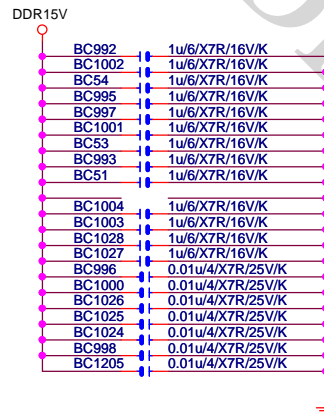
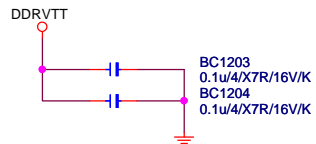
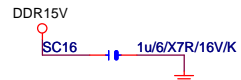
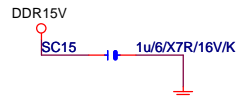
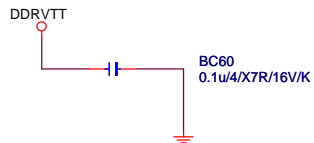
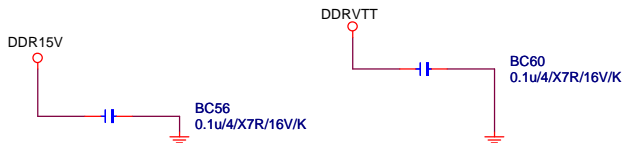
(18,19,22,24,25,27,29,32,34,35,43,57,58,59,61) SMBCLK SMBDATA
(18,19,22,24,25,27,29,32,34,35,43,57,58,59,61) SMBDATA

DDR TERMINATION CHANNEL A

DDRVTT Decouple

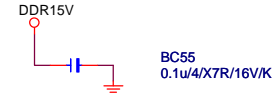


DDR18V Decouple

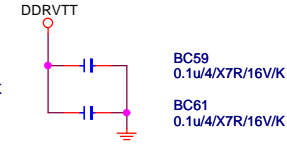


DDR TERMINATION CHANNEL B

DDR18V Decouple



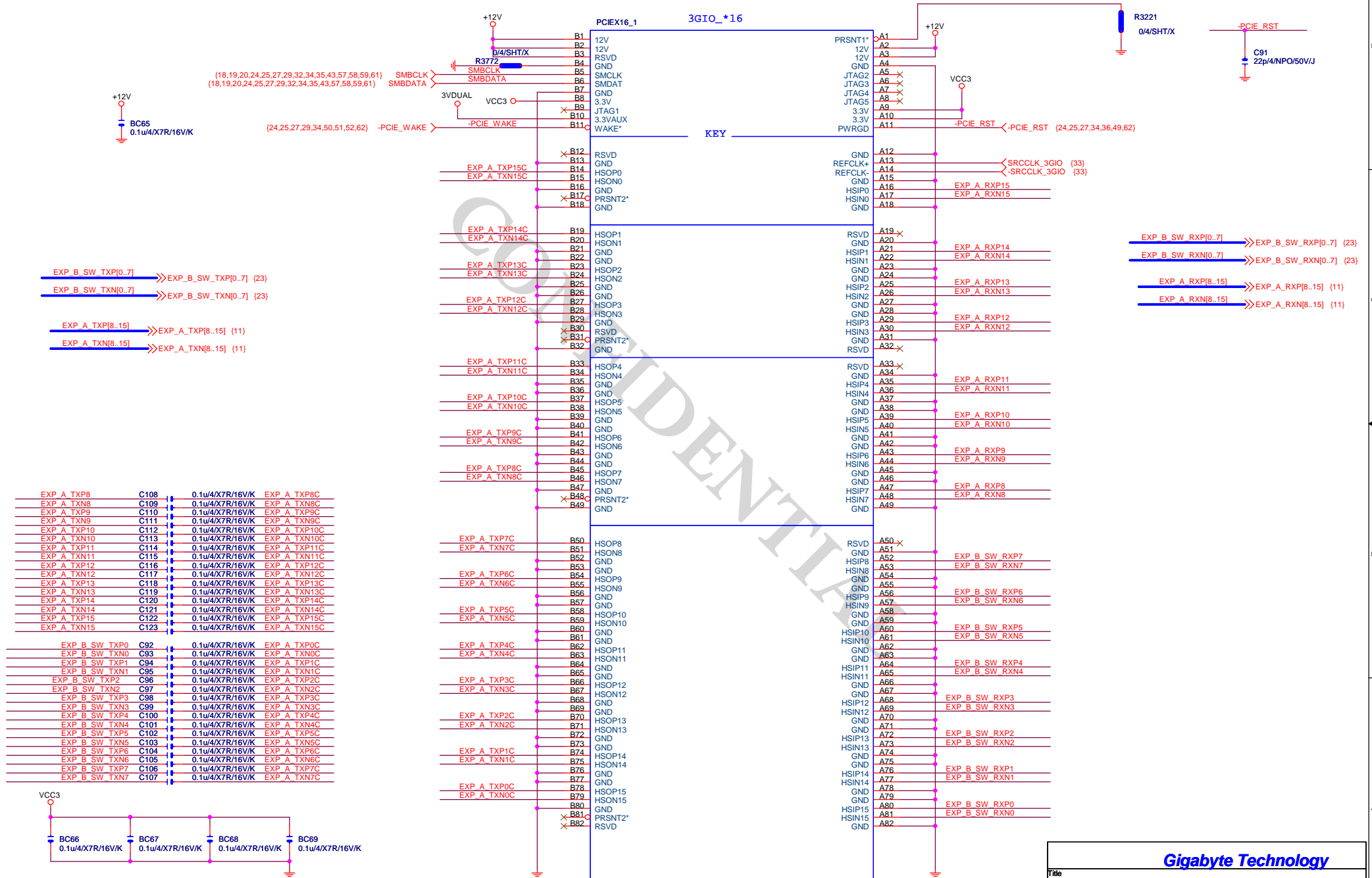
DDRVTT Decouple



Gigabyte Technology

Title			DDR II TERMINATOR		
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Custom					1.0
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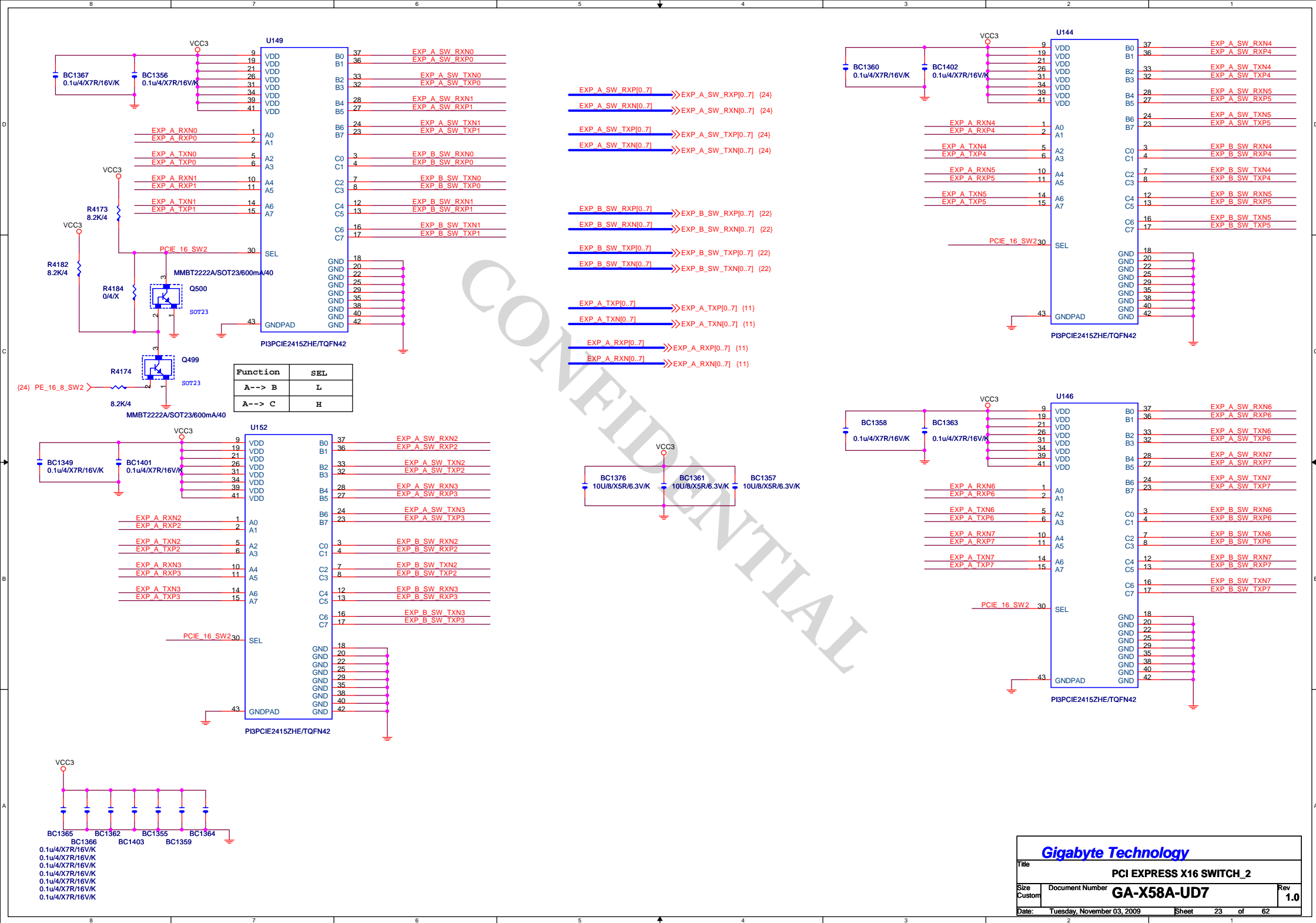
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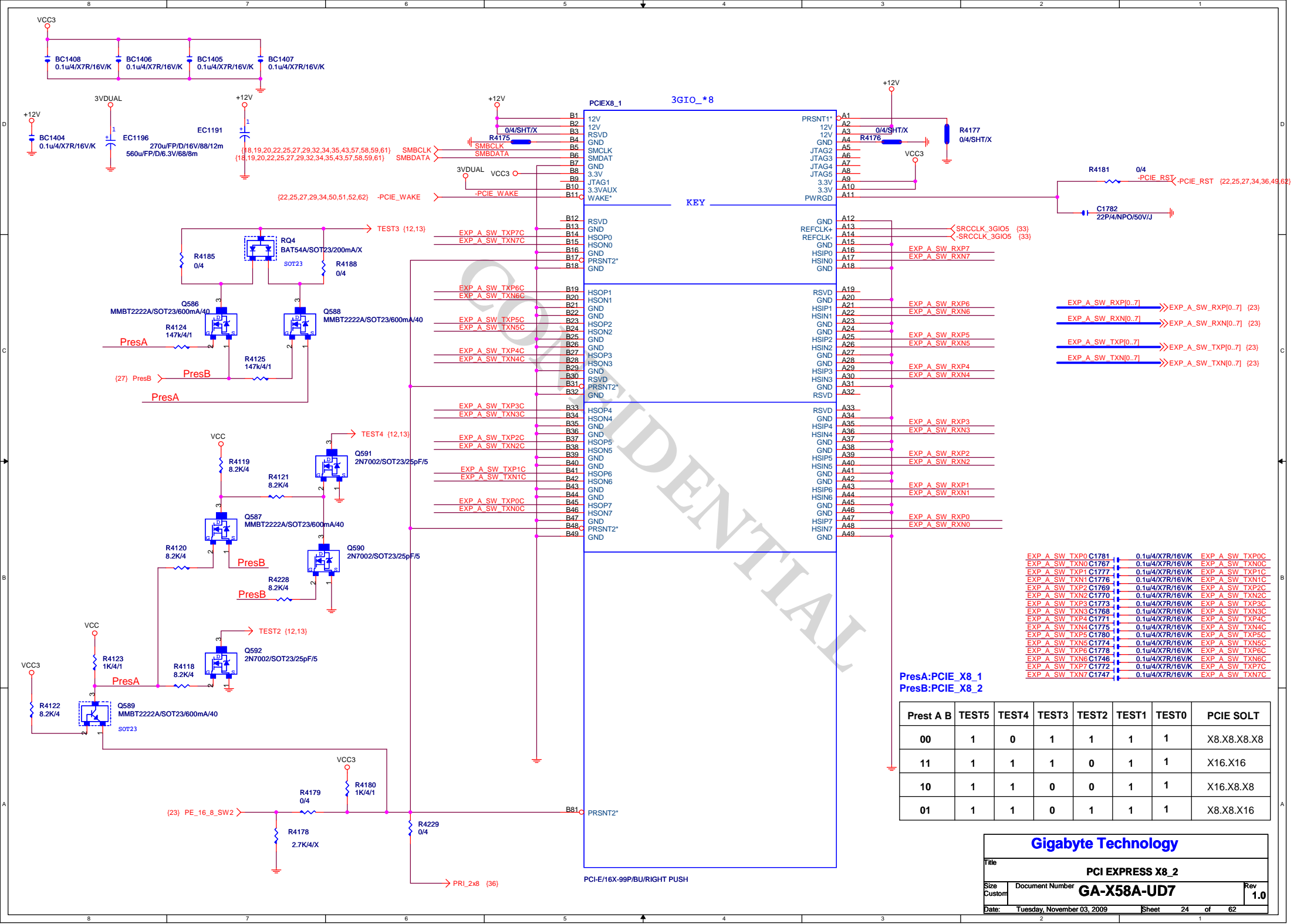


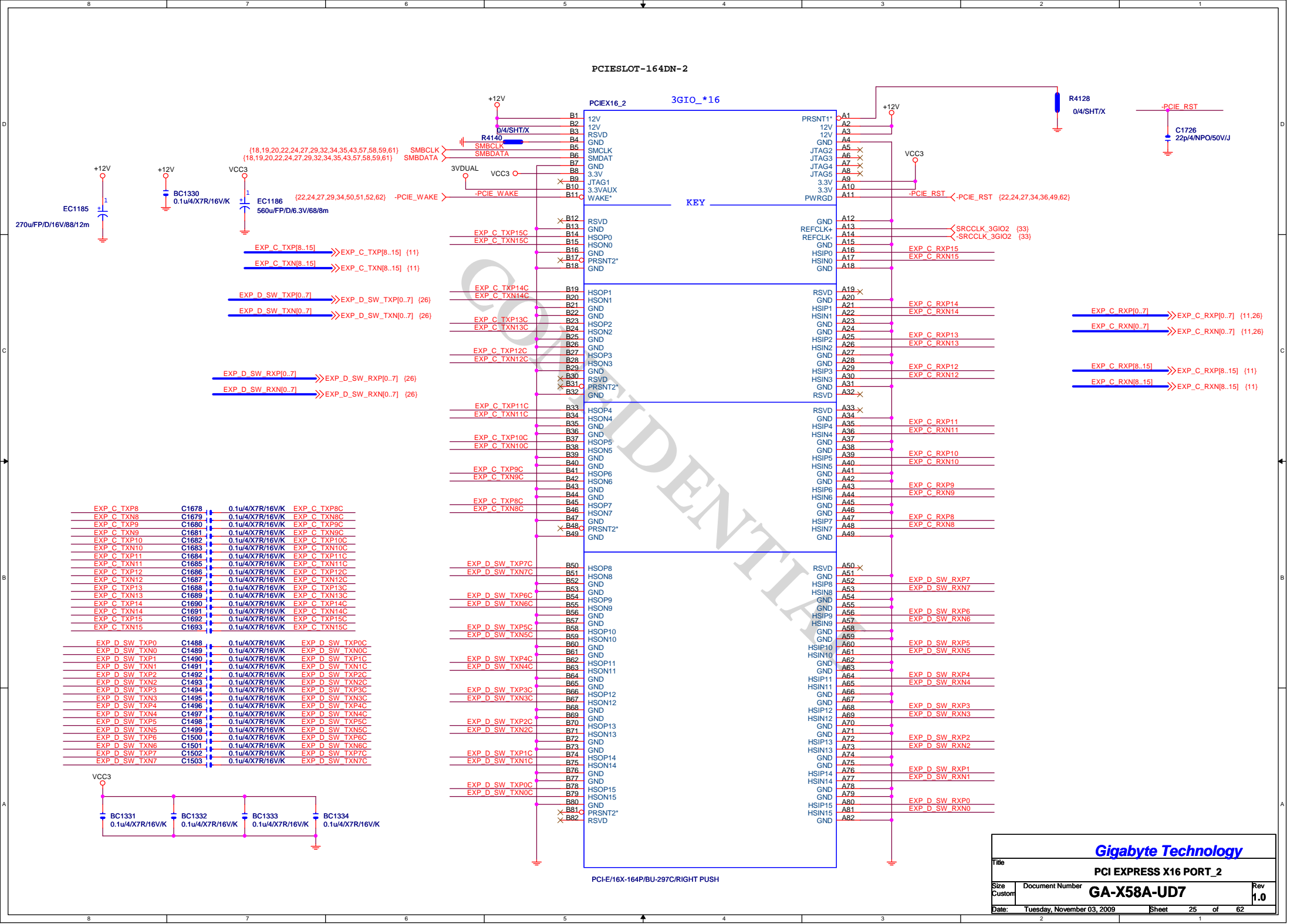
PCIE/16X-164P/BU-297C/RIGHT PUSH

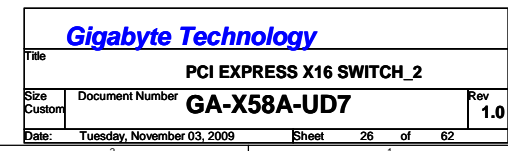
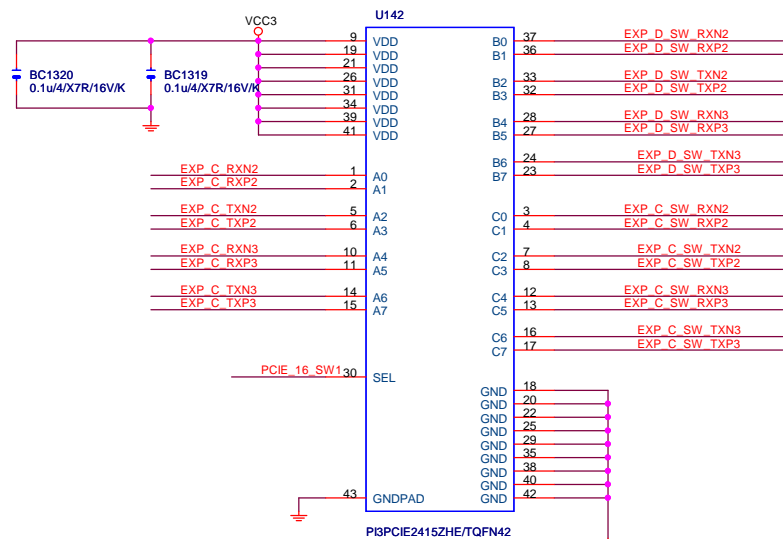
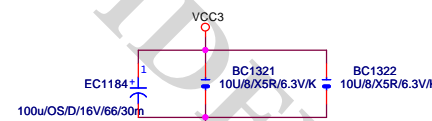
Gigabyte Technology

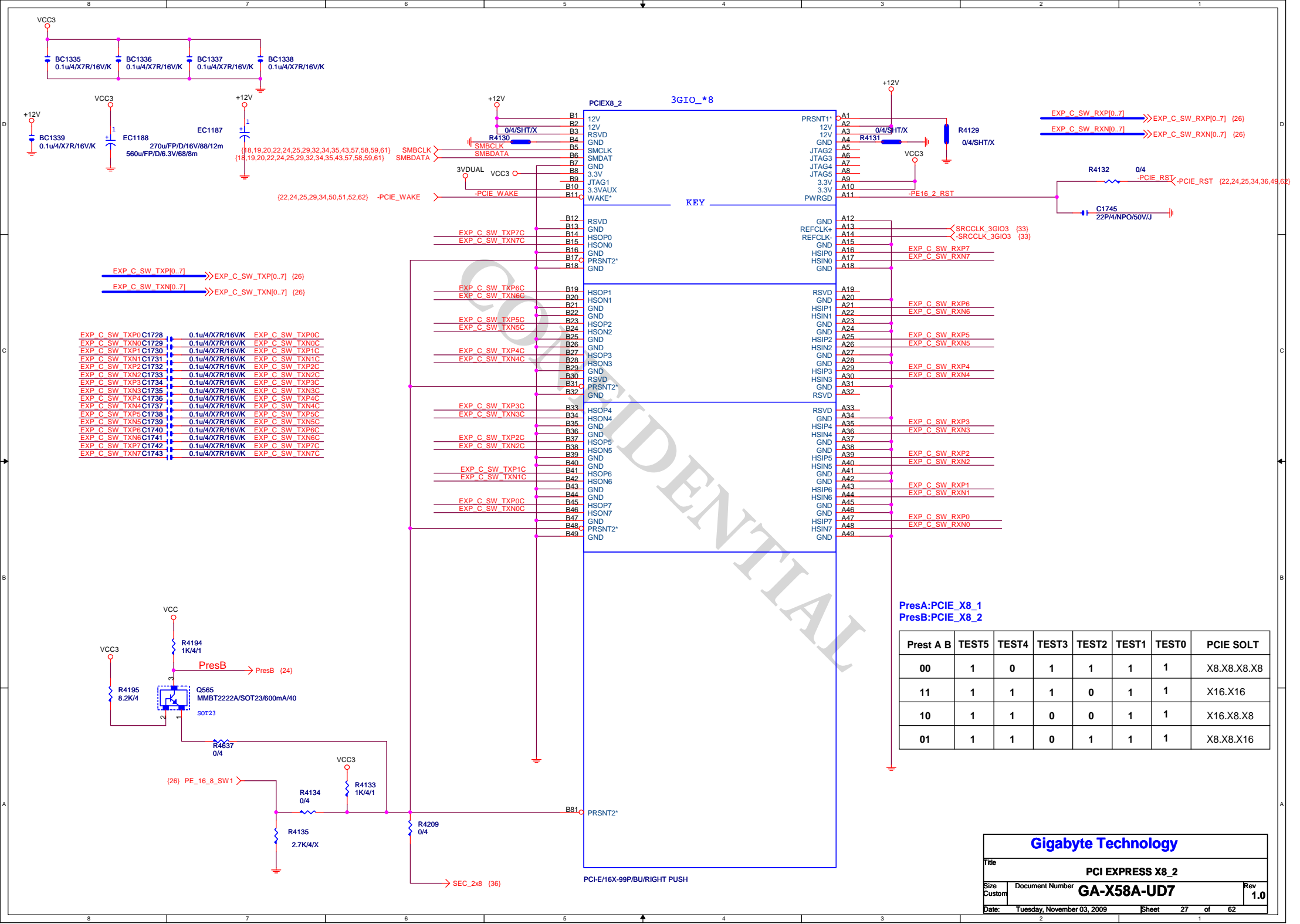
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Size	Document Number	GA-X58A-UD7	
Custom		Rev 1.0	
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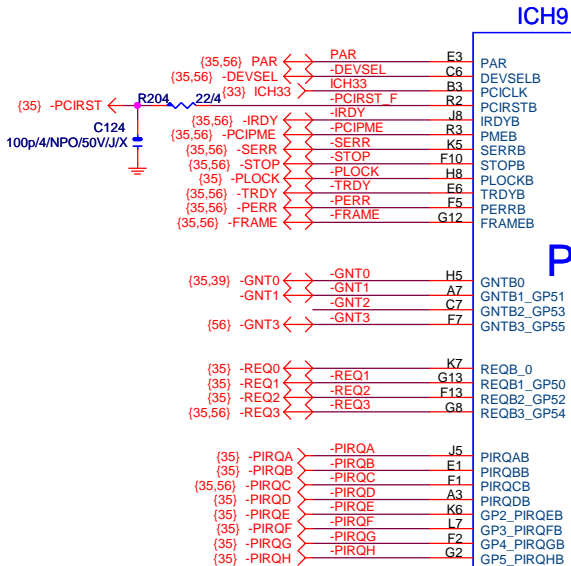












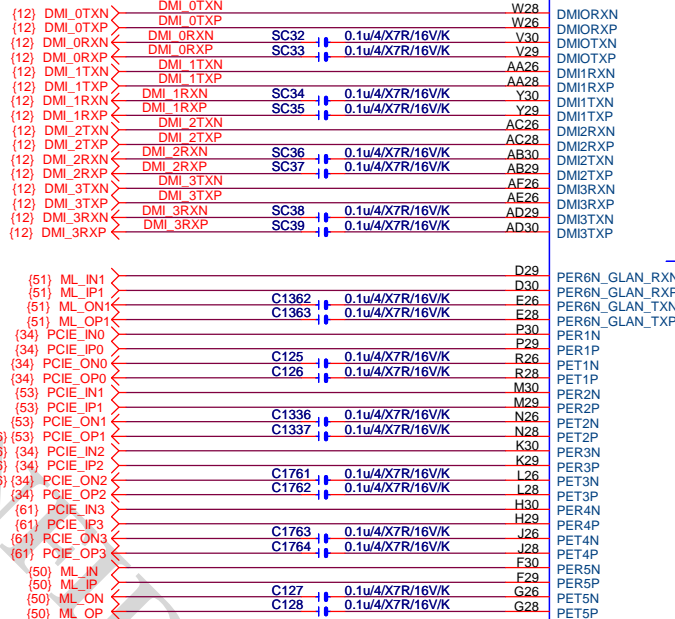
PCI

1 OF 6

ICH GPIO Table

PIN NAME	USAGE	NOTE
GP9_WOL_EN(GPIO9)	8268_P18	
GP20(GPIO20)	8268_P18	
GP0	-PECI_REQ	
GP8	STRAP_CSI_FRE1	
GP12	STRAP_CSI_FRE0	
GP27_QRT_STATE0	3VDUAL_ICH	原ISOLATEB_1
GP26_S4_STATEB	3VDUAL_ICH	原ISOLATEB_2
CLGPIO5_GP57	F_LED1_C	
GP1_TACH1	F_LED2_C	
GP22_SCLOCK	F_LED3_C	
GP28_SLOAD	F_LED4_C	
GP21_SATA0GP	F_LED5_C	
GP6_TACH2	NBT_LED2_C	
GP39_SDATAOUT0	-CPU_PSI_DIS	
GP34(GPIO34)	-SPI_WP0	
GP48_SDATAOUT1	-EN_PWM	
GP19_SATA1GP	-ACZ_DET	
GP25	-CPU_STOP	
GP36_SATA2GP	GPIO36(FS)	
GP37_SATA3GP	SATA3GP	
SMBALERTB_GP11	-SMBALRT	
GP10_ALERTB	ICH_GP10(-CATERR)	原-LAN1_DSM
GP13	-LPCPME	

ICH10R[10HB1-038280-F0R]



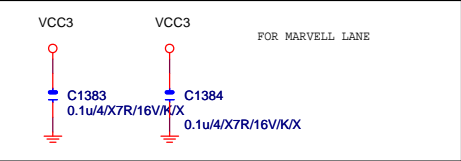
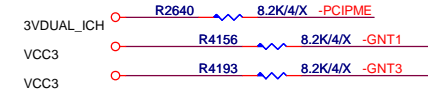
ICH9

DMI

USB

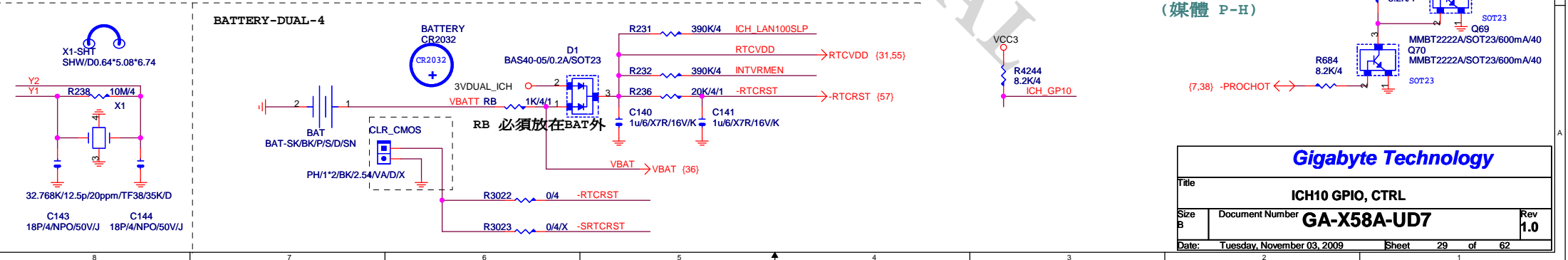
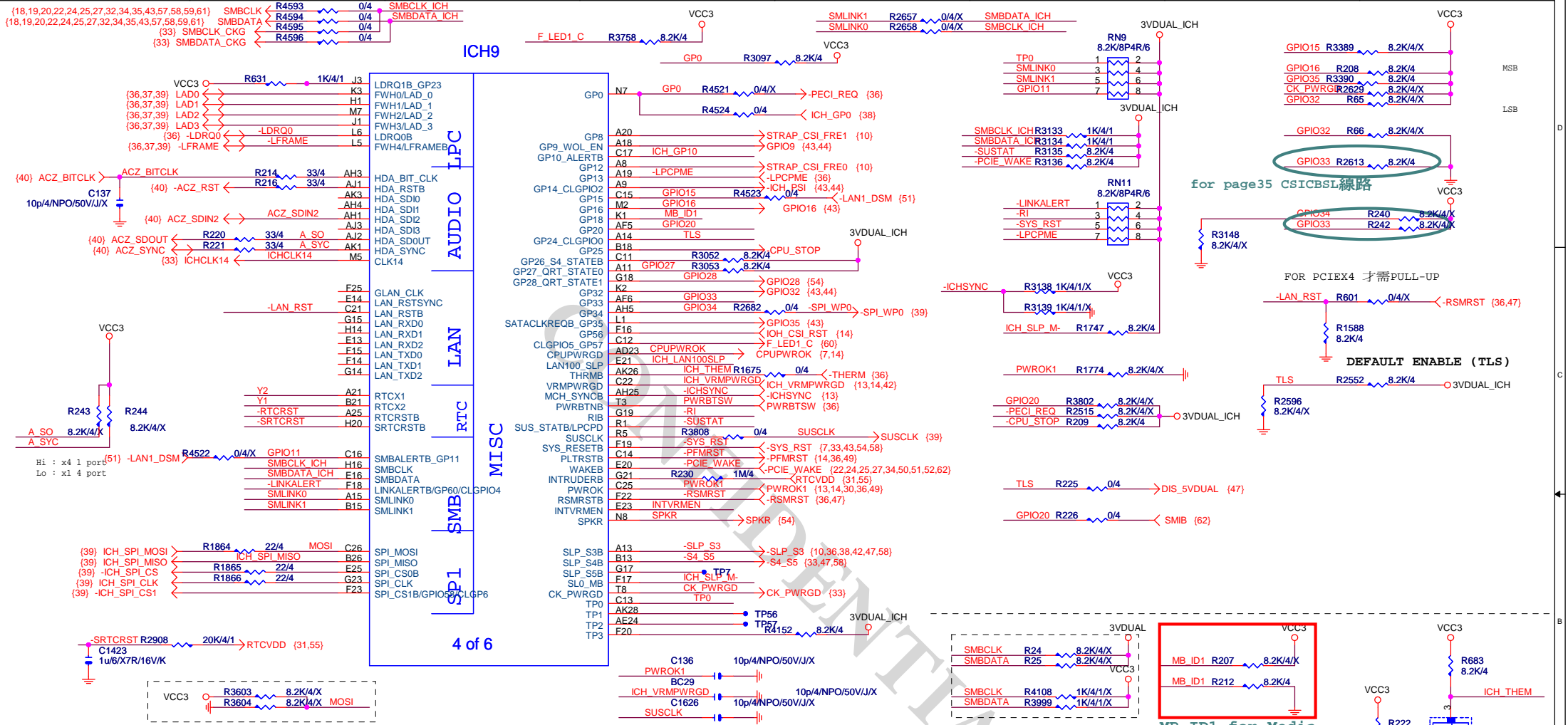
PCI-E

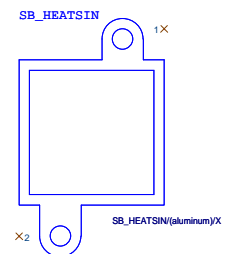
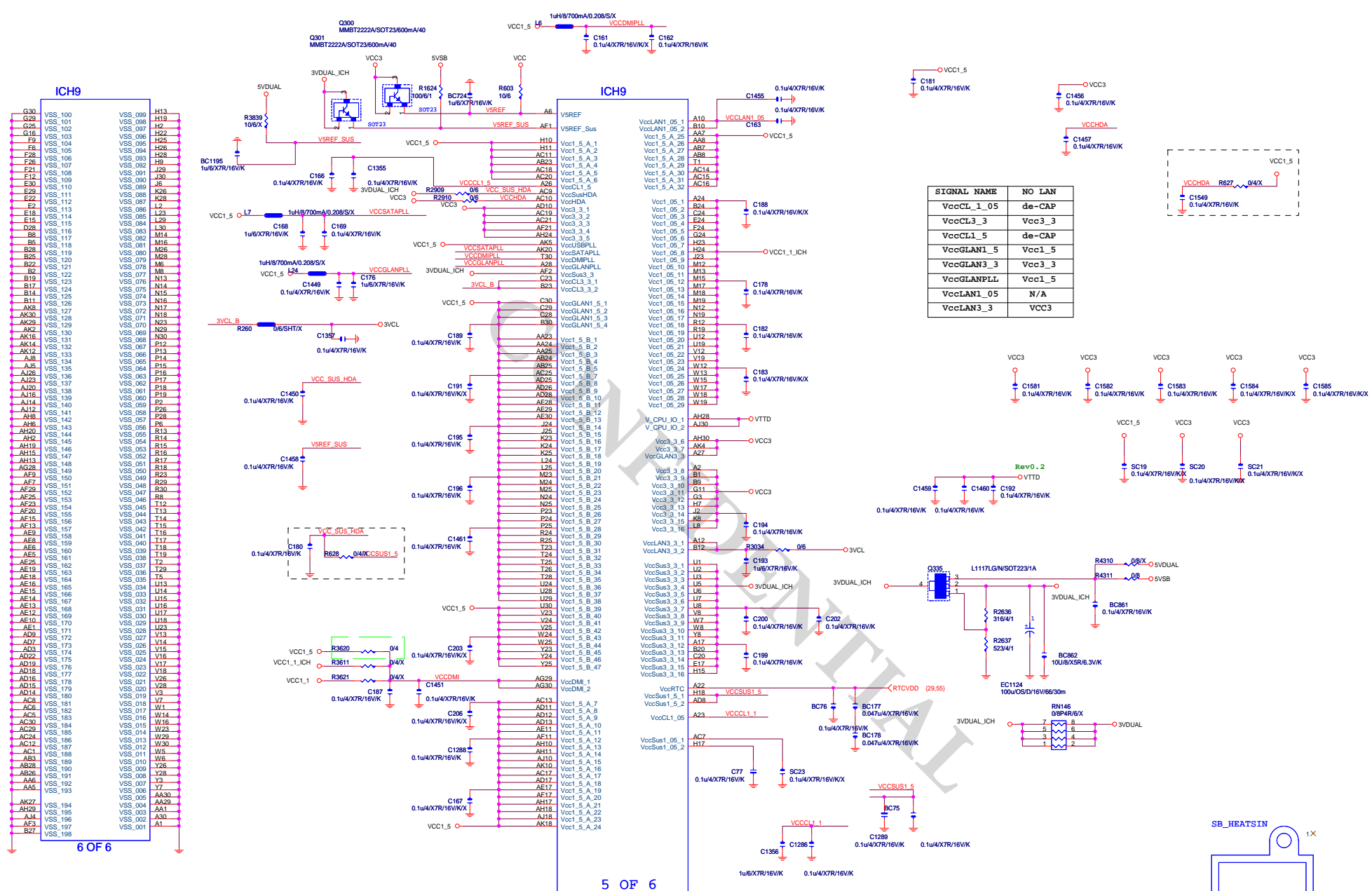
2 OF 6

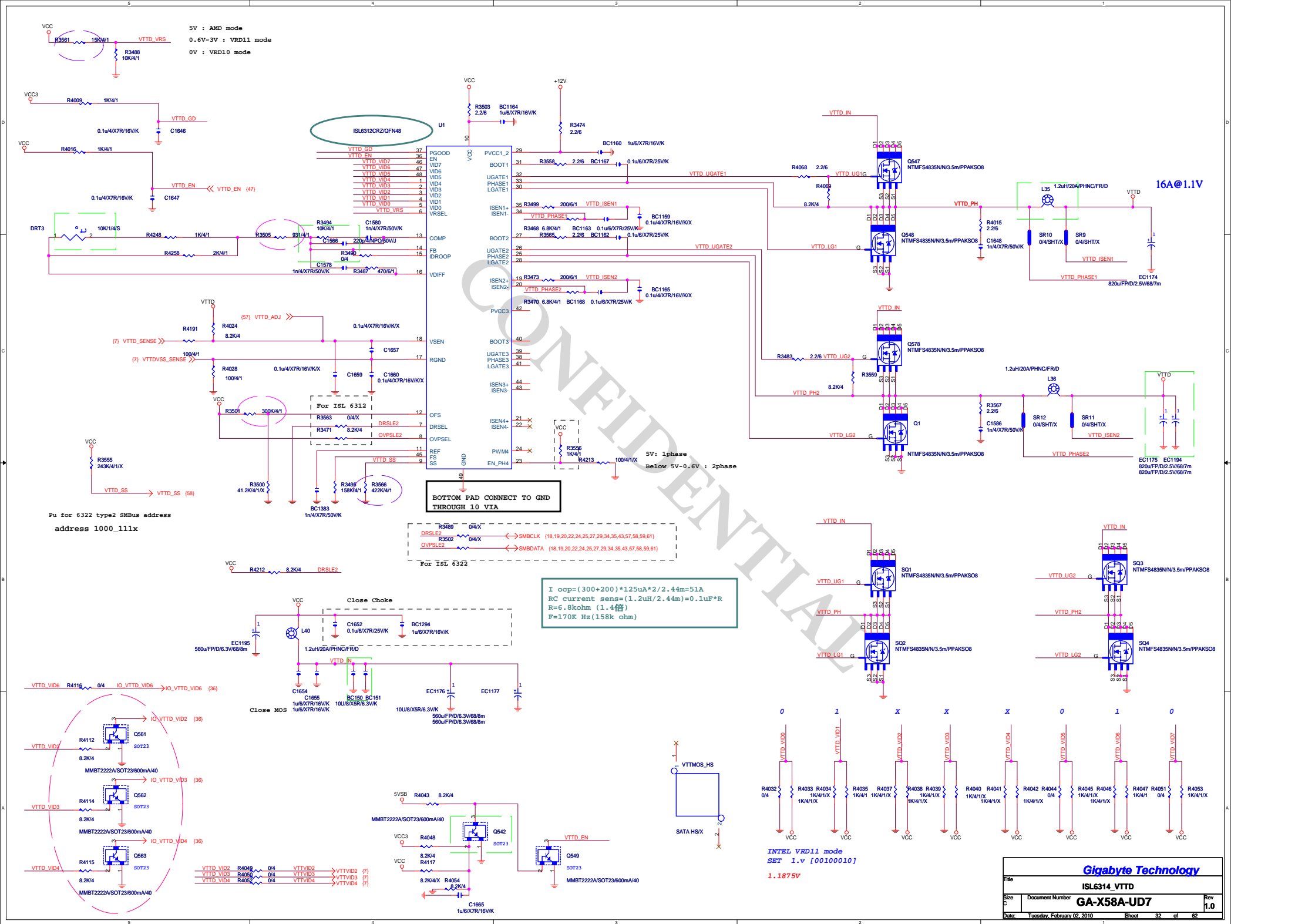


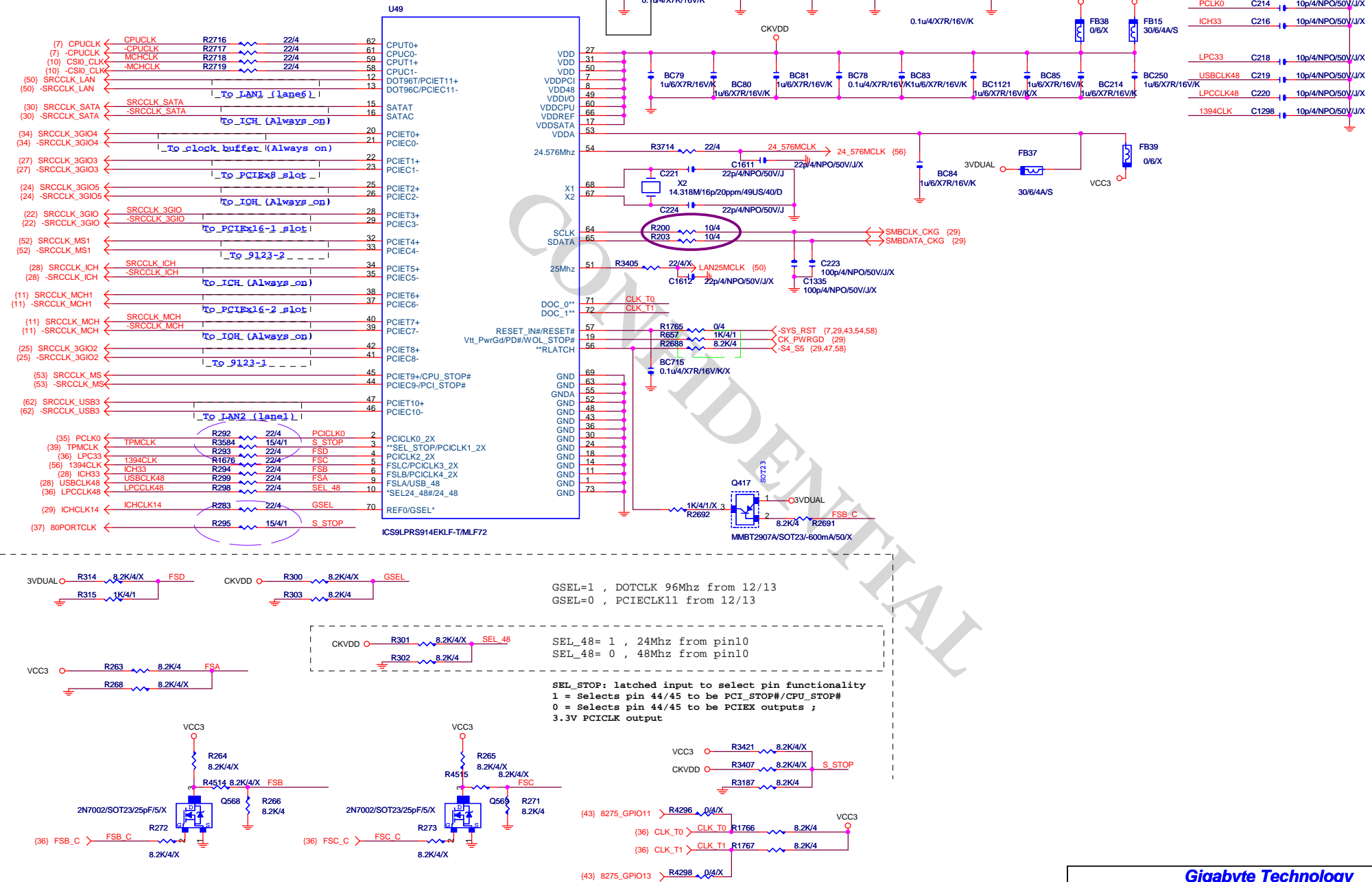
Gigabyte Technology

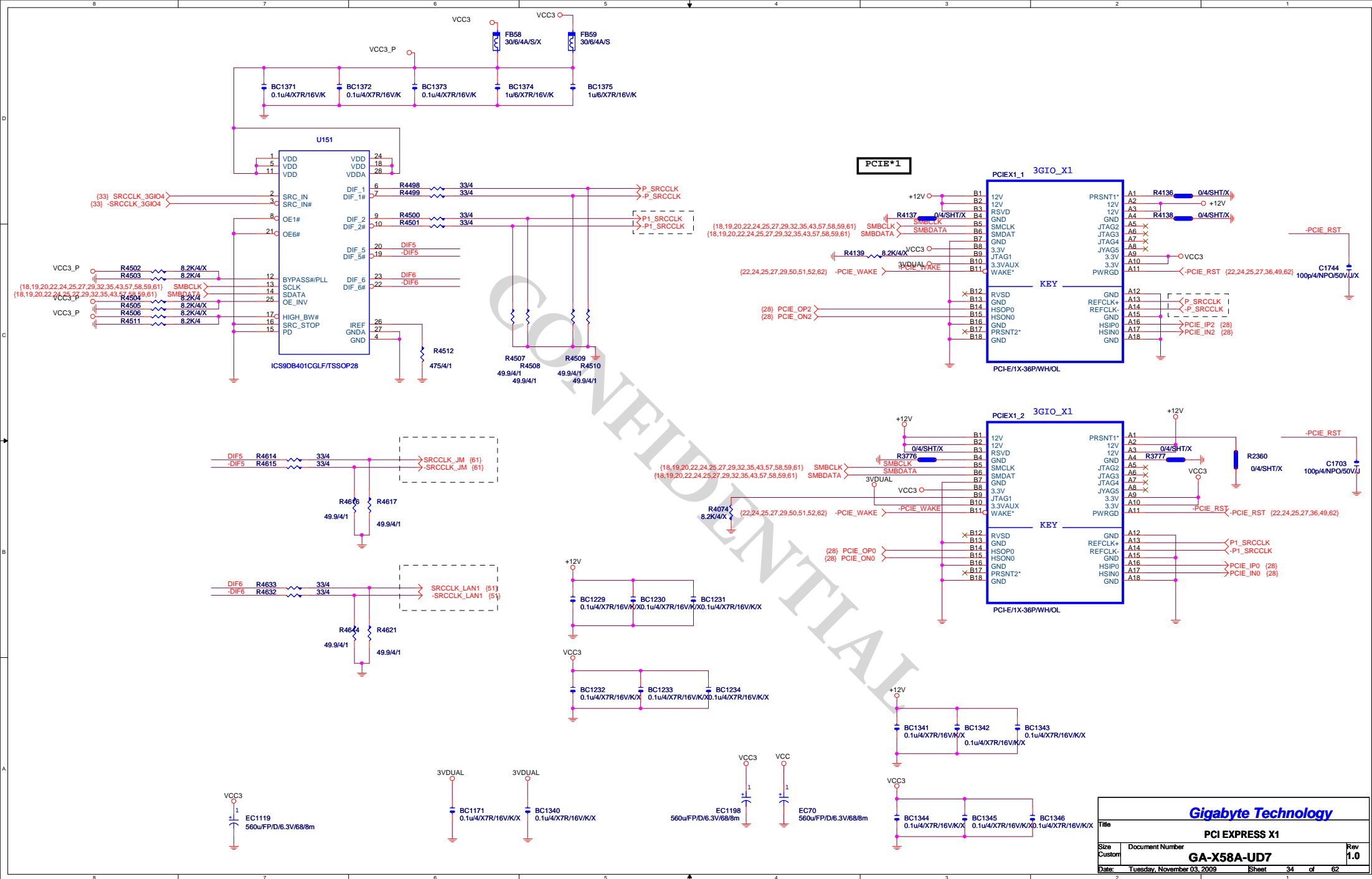
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Size	Document Number		Rev		
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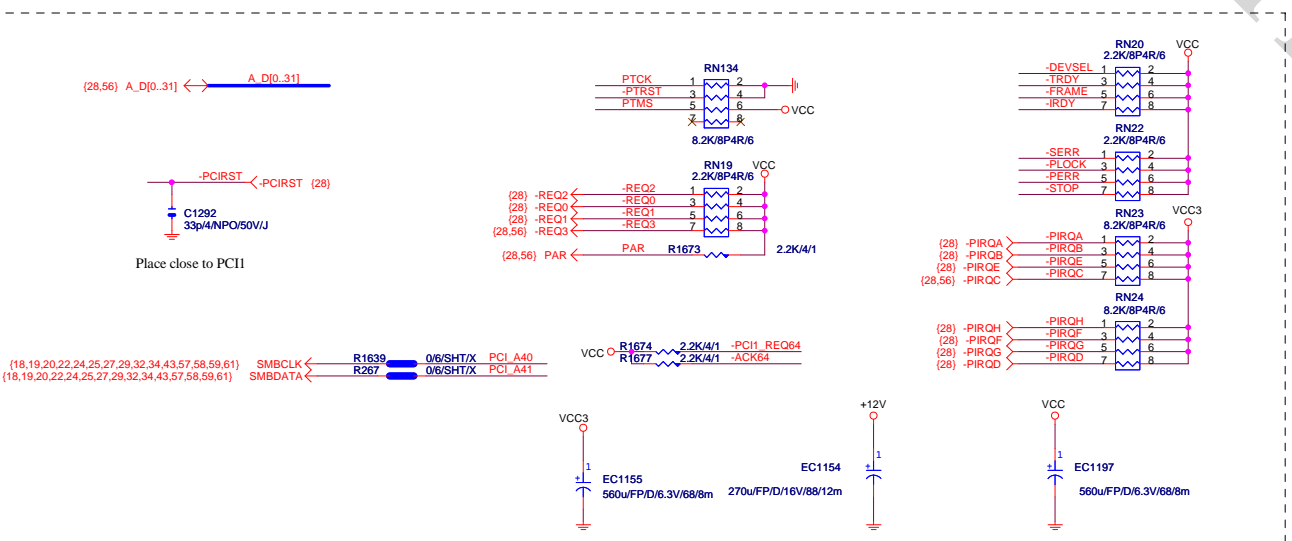
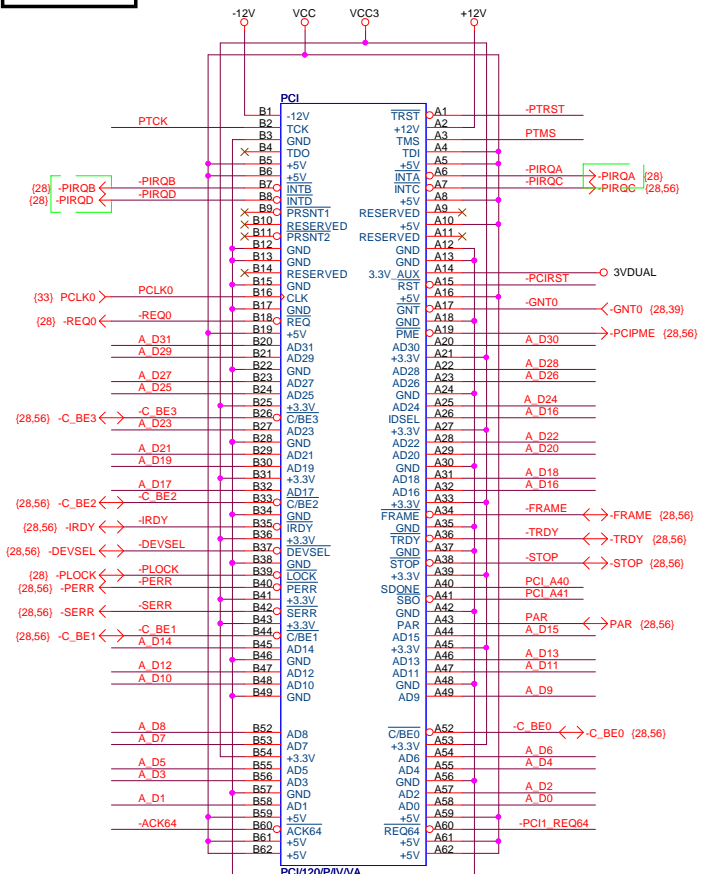


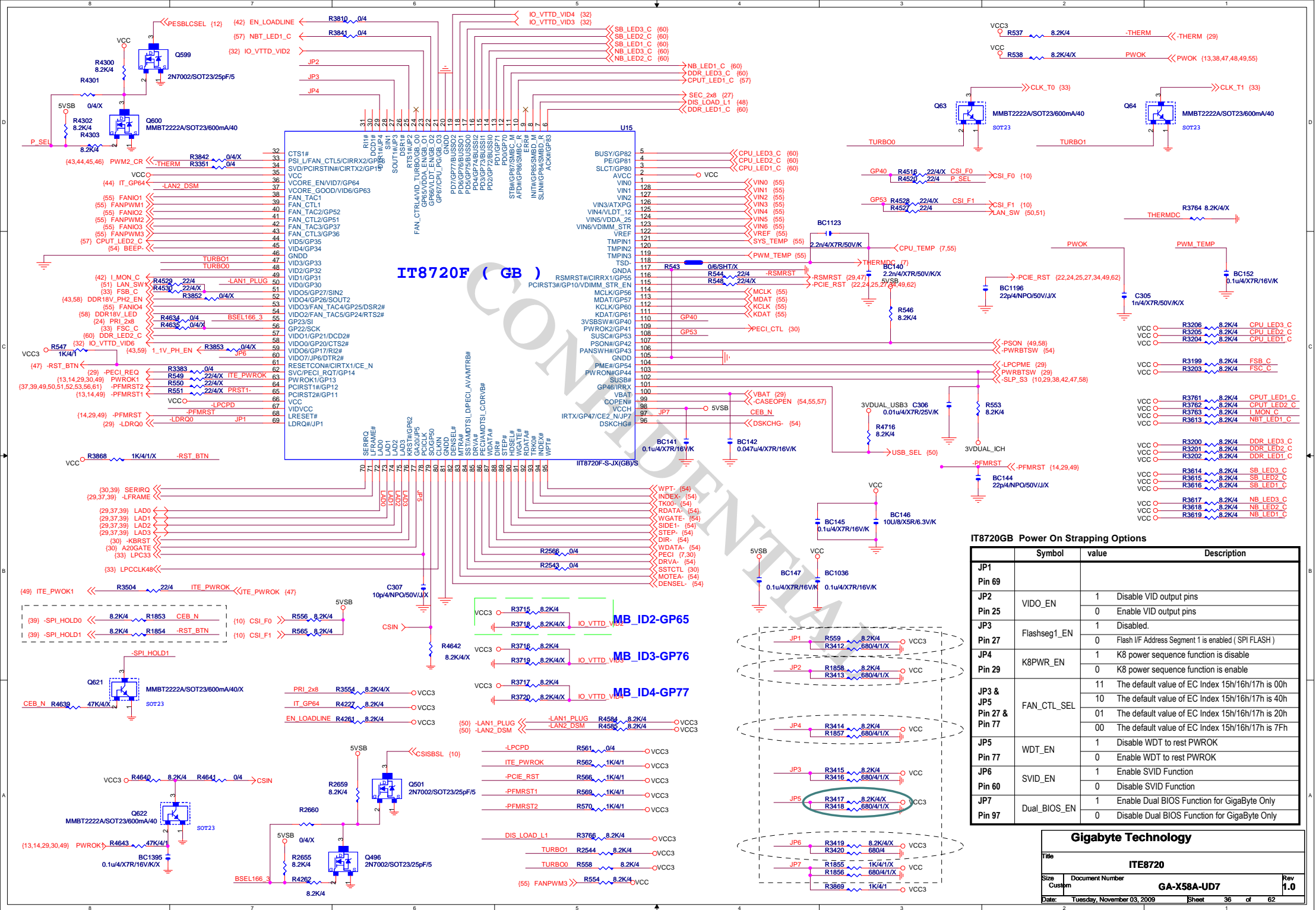


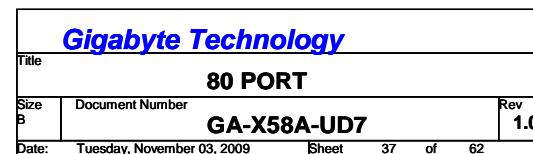
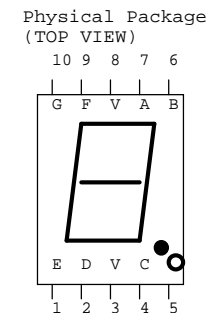




PCI1,2 SLOT

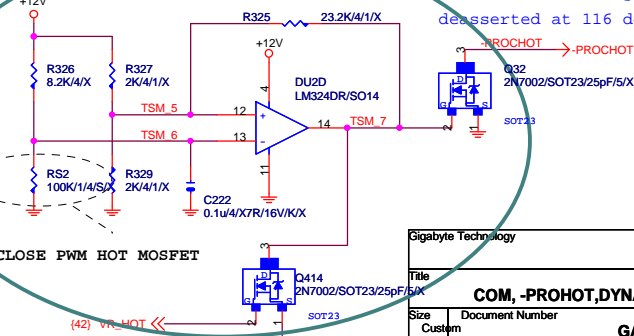




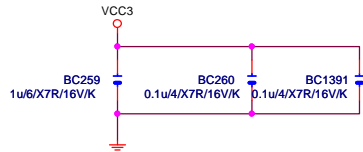
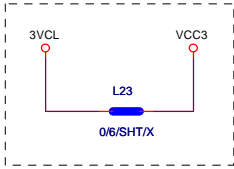




CLOSE PWM HOT MOSFET

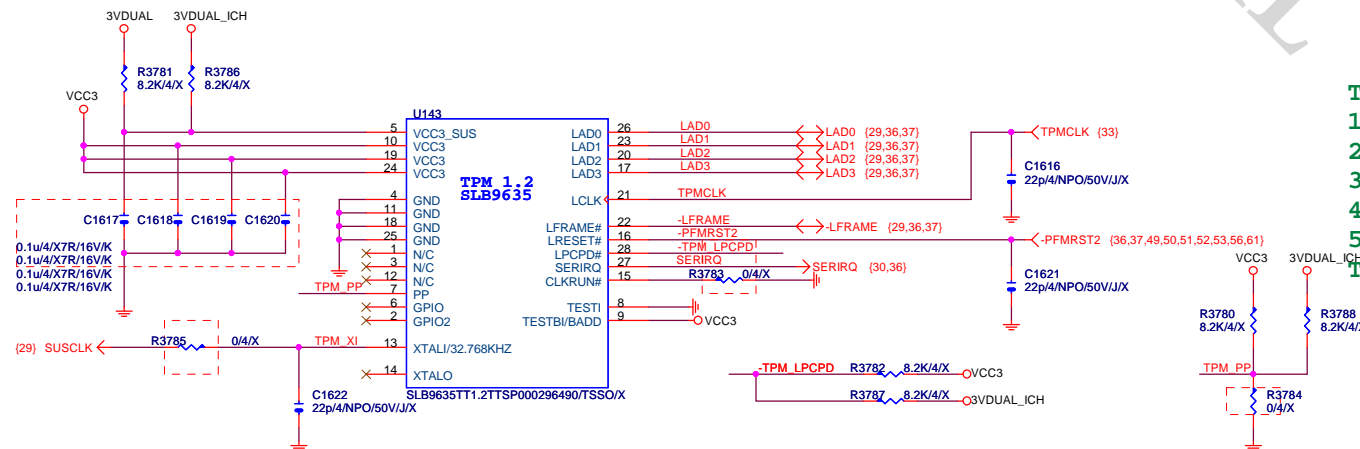


Gigabyte Technology			
Title			
COM, -PROHOT,DYNAMIC OC +12V保潔線路			
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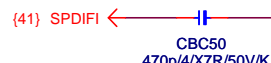
TPM



TPM Function
 1.C1617.C16118.C1619.C1620
 2.U143
 3.R3782.R3783.R3784.R3785
 4.R3584=15 ohm(TPM)不上(no TPM)
 5.R295=15 ohm(TPM)22 ohm(no TPM)

Gigabyte Technology		
Title DUAL BIOS TPM		
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```
{41} CEN <-
{41} LFE <-
{41} S_SURR_L <-
{41} S_SURR_R <-
```



CR14/CBC4 close to SouthBridge

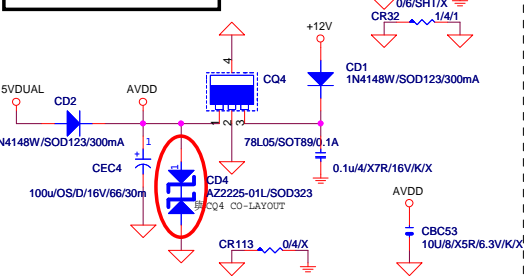
Digital Area

Analog Area

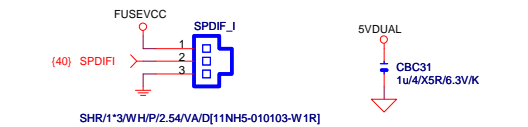
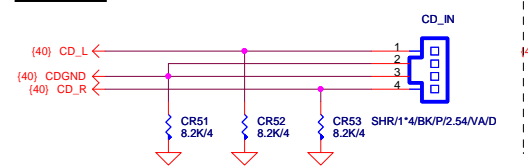
JD resistors close to pin13 of CODEC

Can Support Amp Out

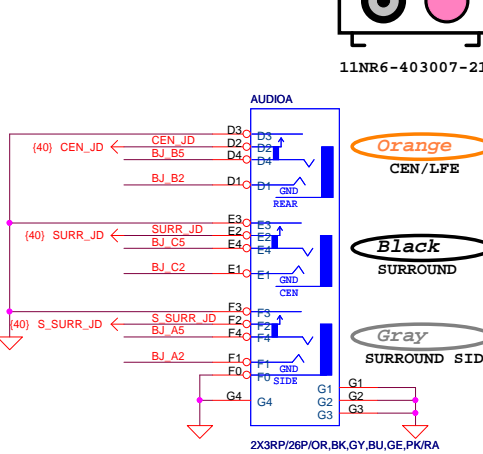
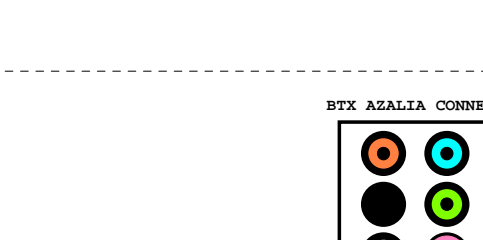
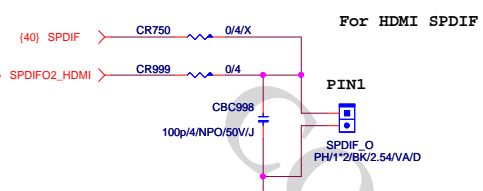
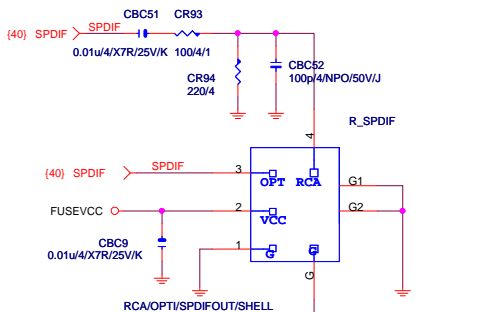
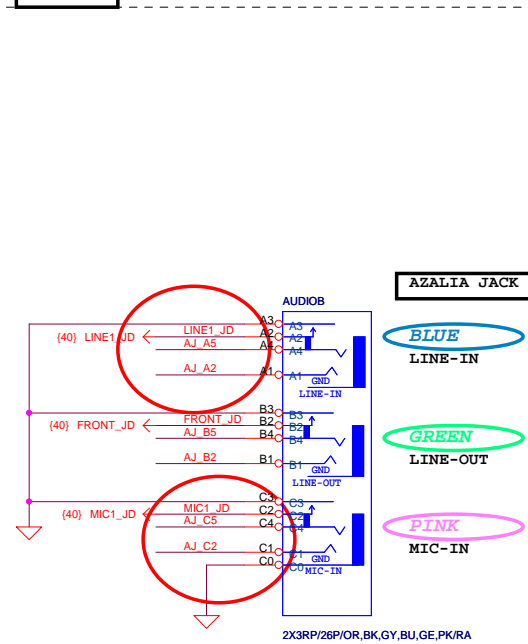
CODEC POWER/EMI PAD



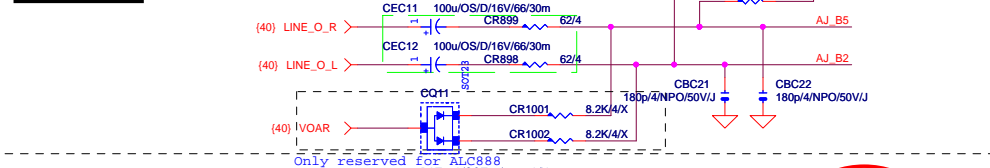
CD IN



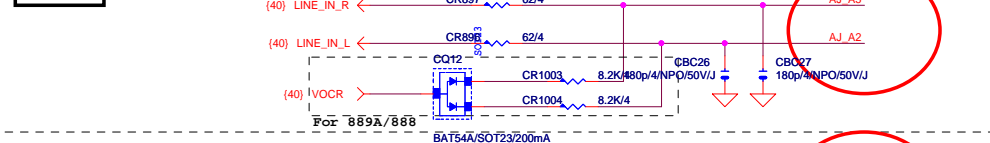
SPDIF_IN



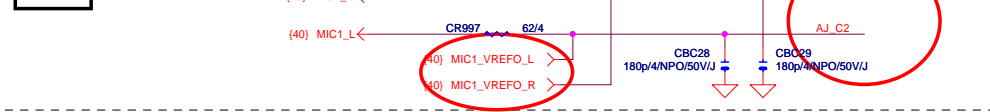
LINE-OUT



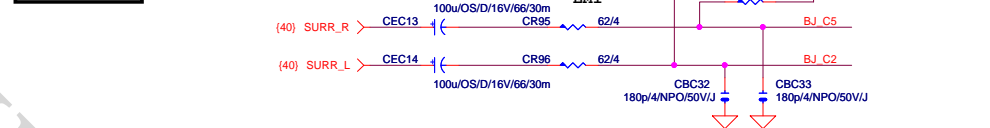
LINE-IN



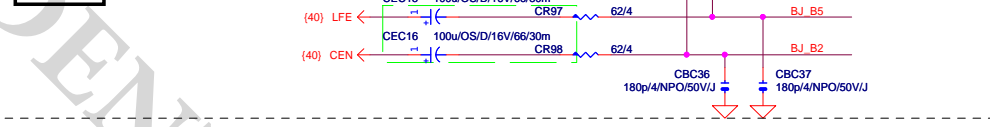
MIC-IN



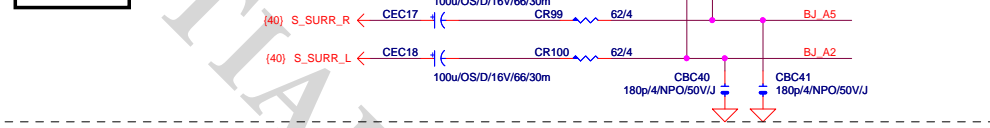
SURROUND



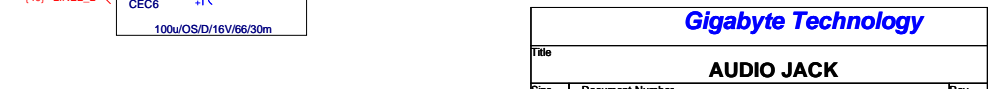
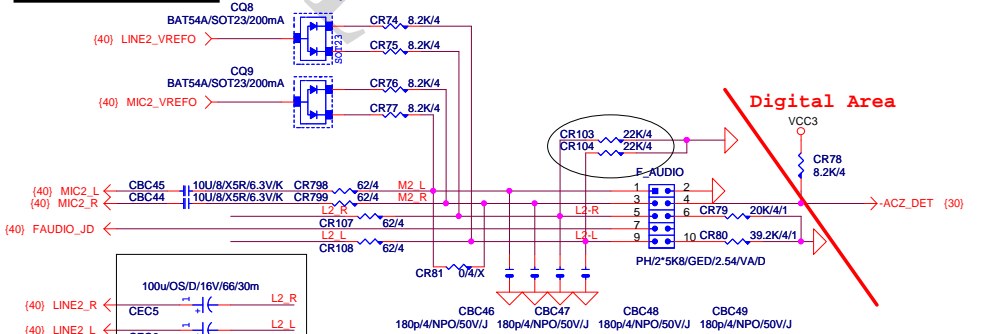
CEN/LFE



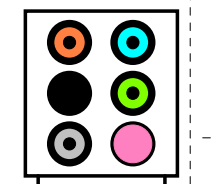
SURR BACK



AZALIA FRONT PANEL

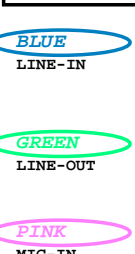


BTX AZALIA CONNECTOR

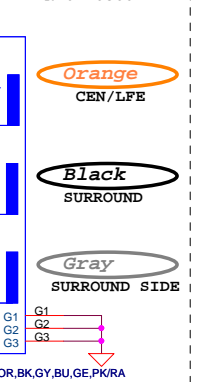


11NR6-403007-21R

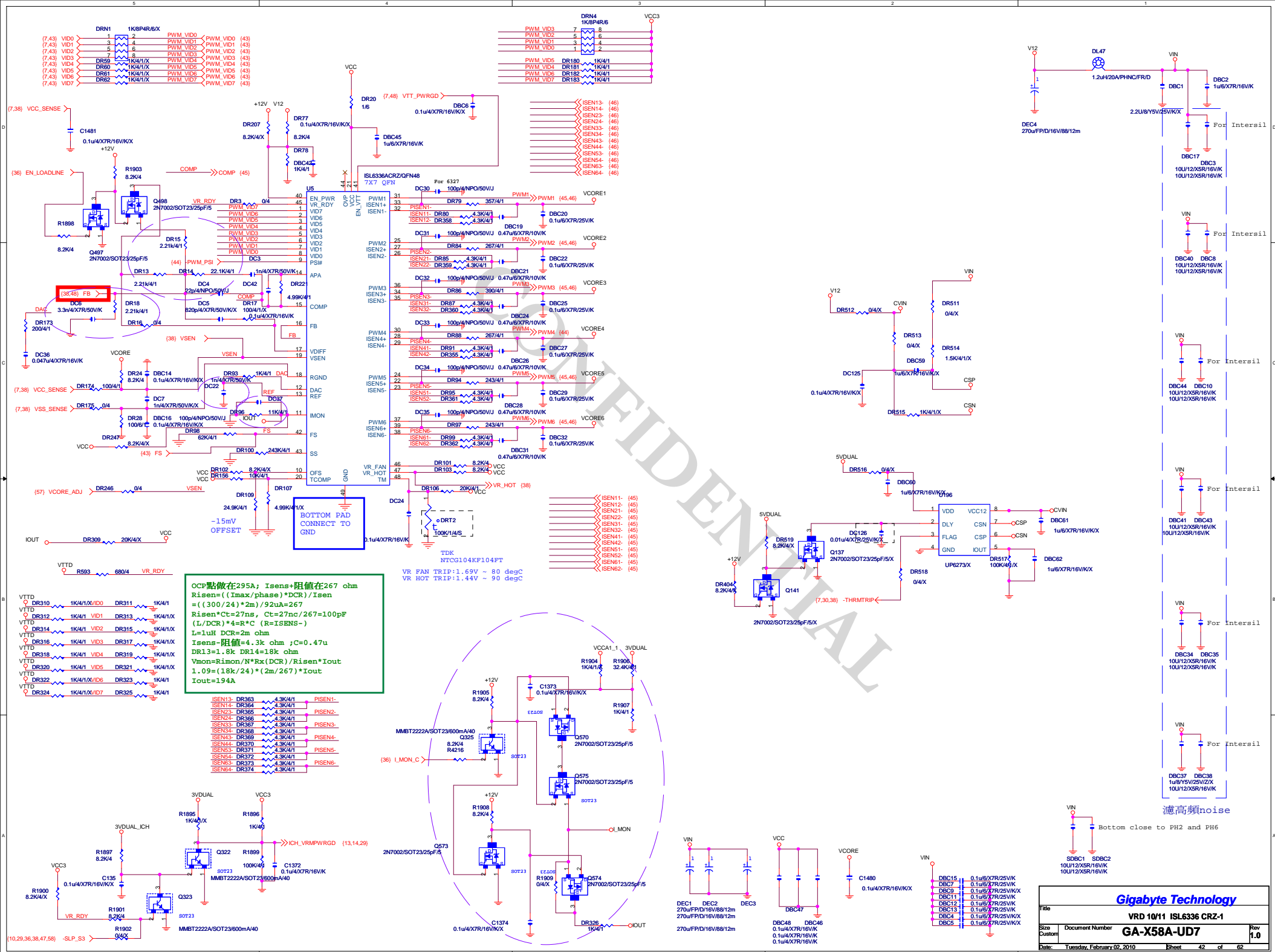
AZALIA JACK



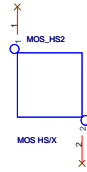
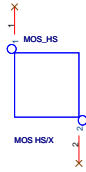
2X3RP/26P/OR,BK,GY,BU,GE,PK/RA



Gigabyte Technology		
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Custom		
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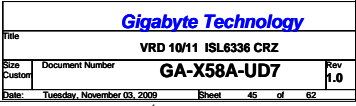


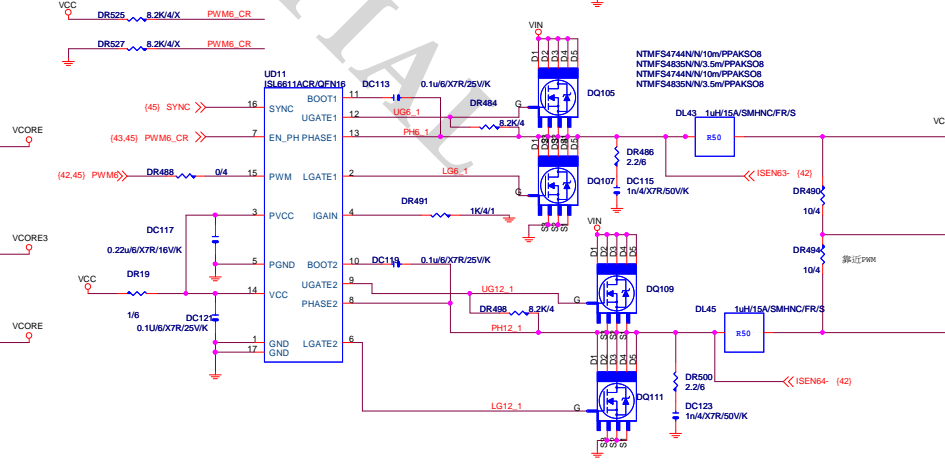
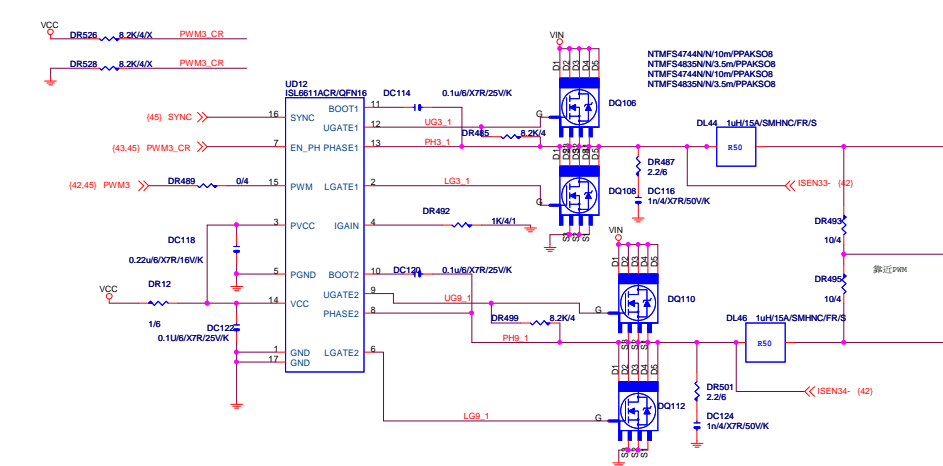
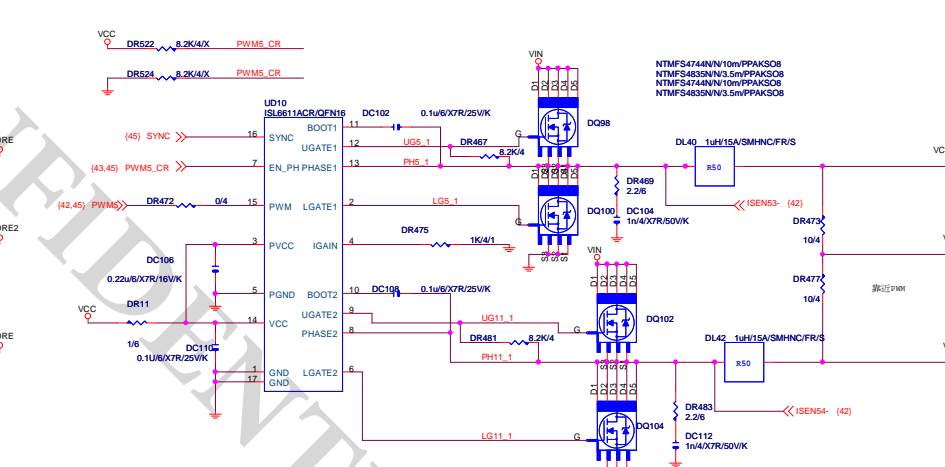
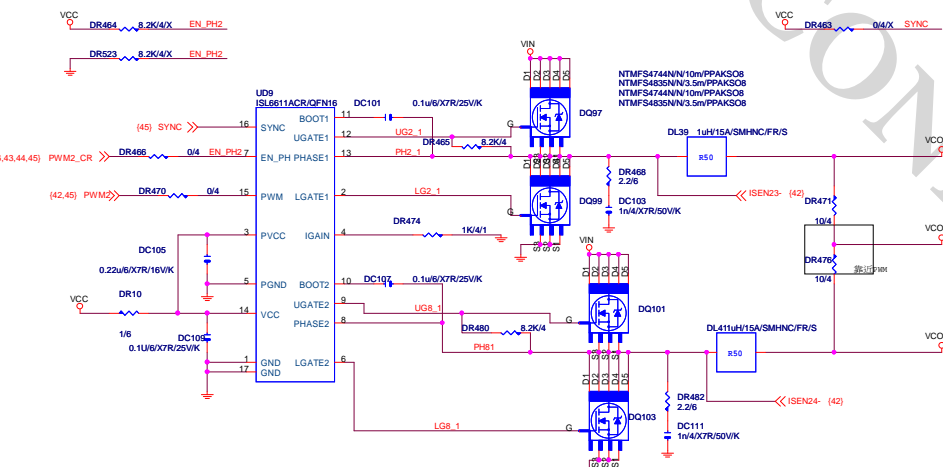
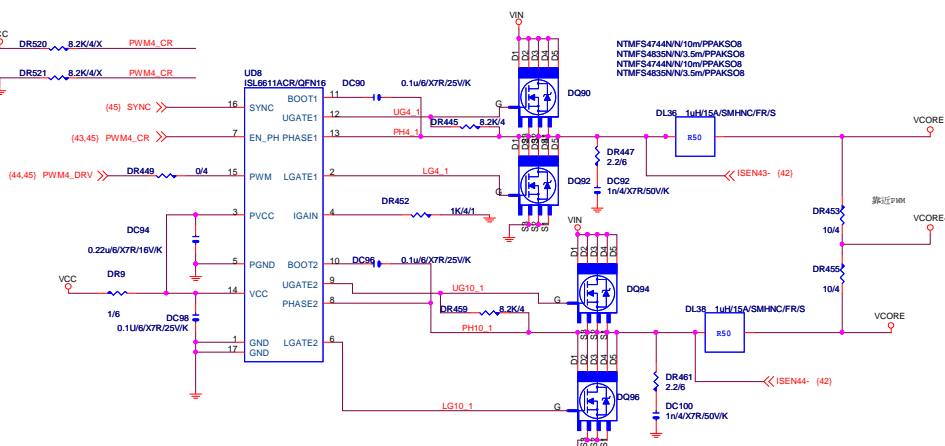
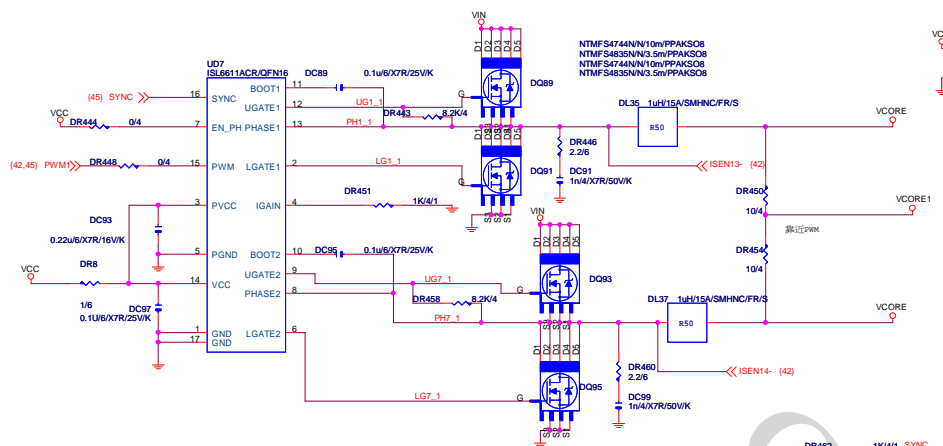
MOS HEATSINK



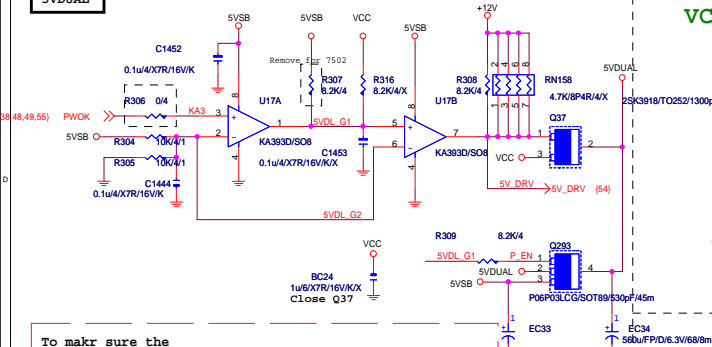
CONFIDENTIAL

CONFIDENTIAL

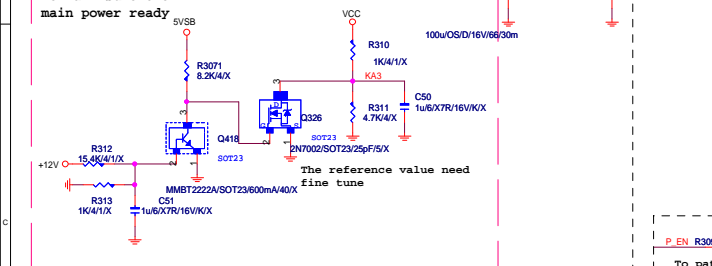




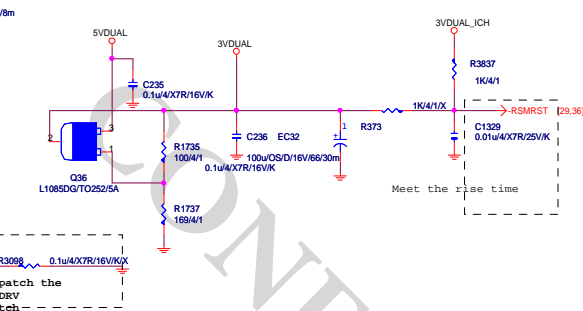
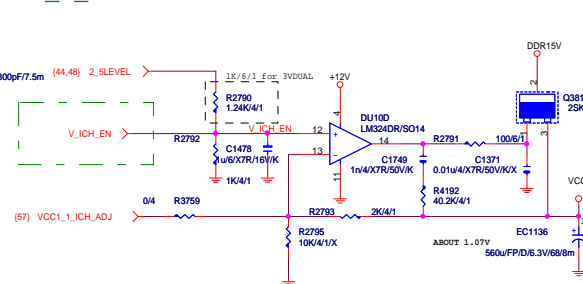
5VDUAL



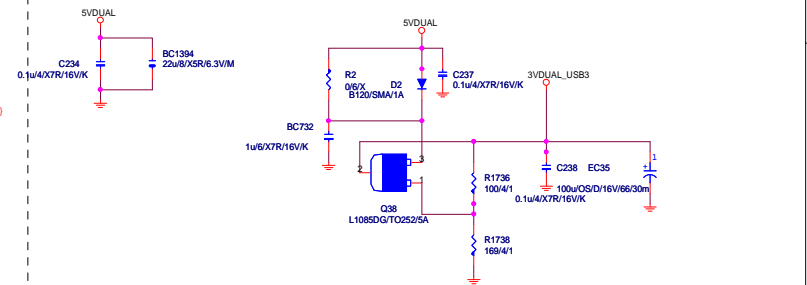
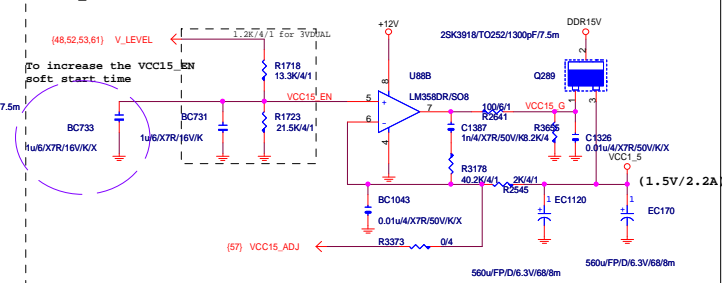
To make sure the
main power ready



VCC1_1_ICH

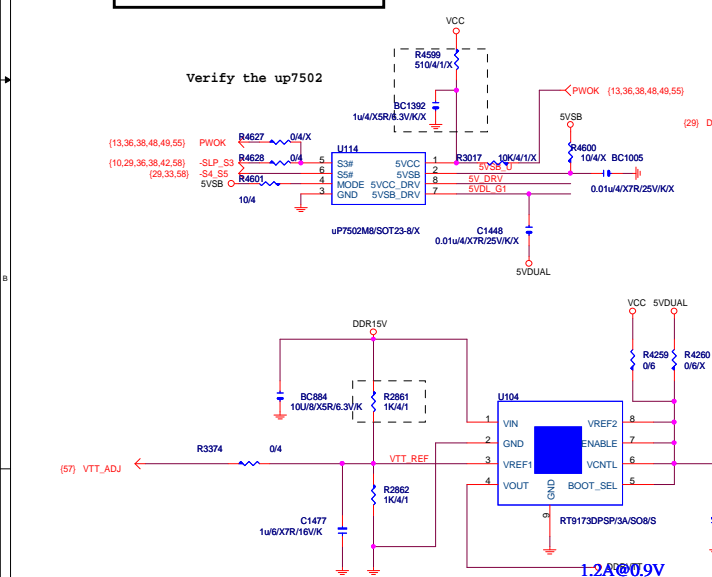


VCC1_5

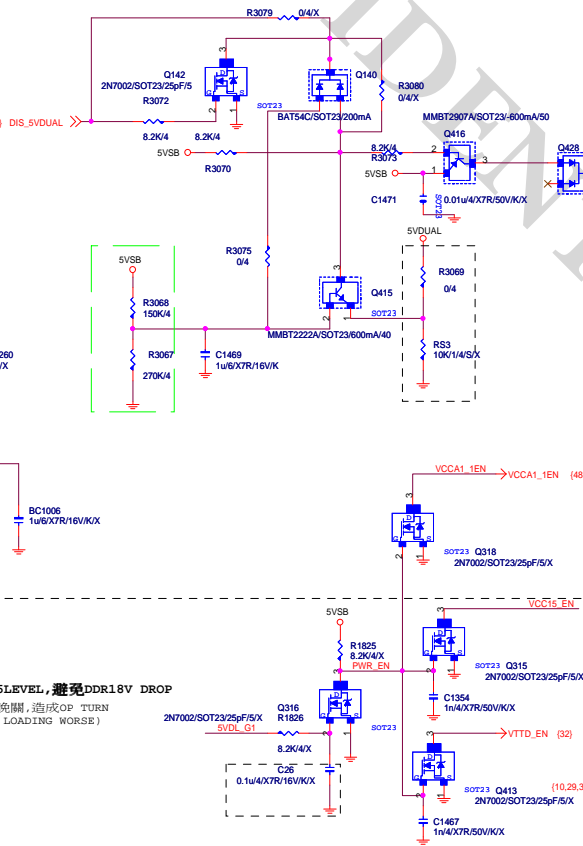


DDR18V/DDRVTT/VCC1_05/VTT_GMCH

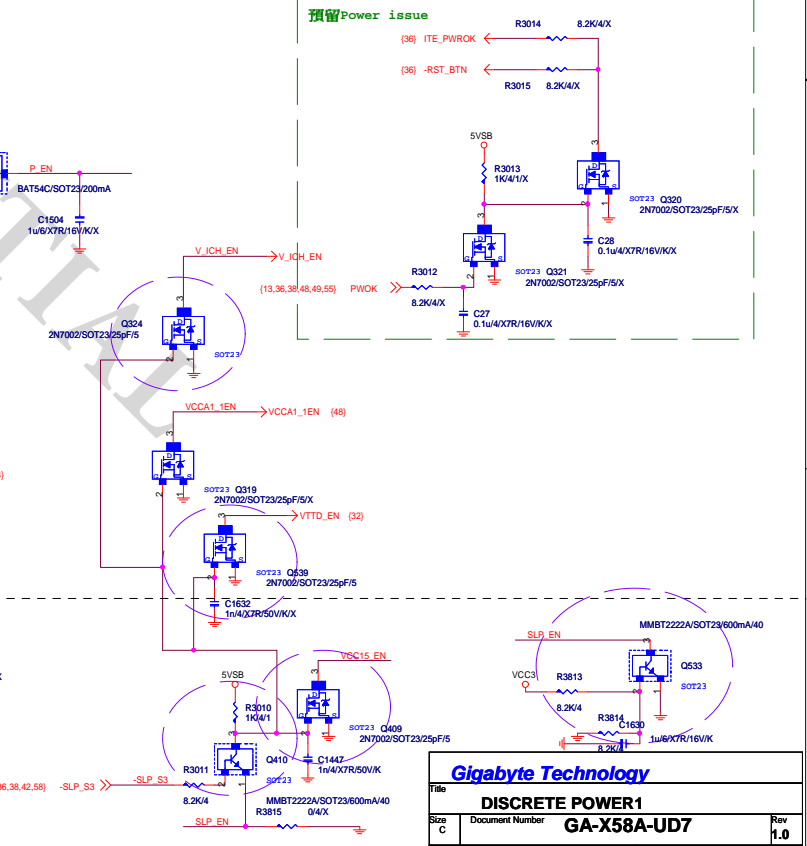
Verify the up7502



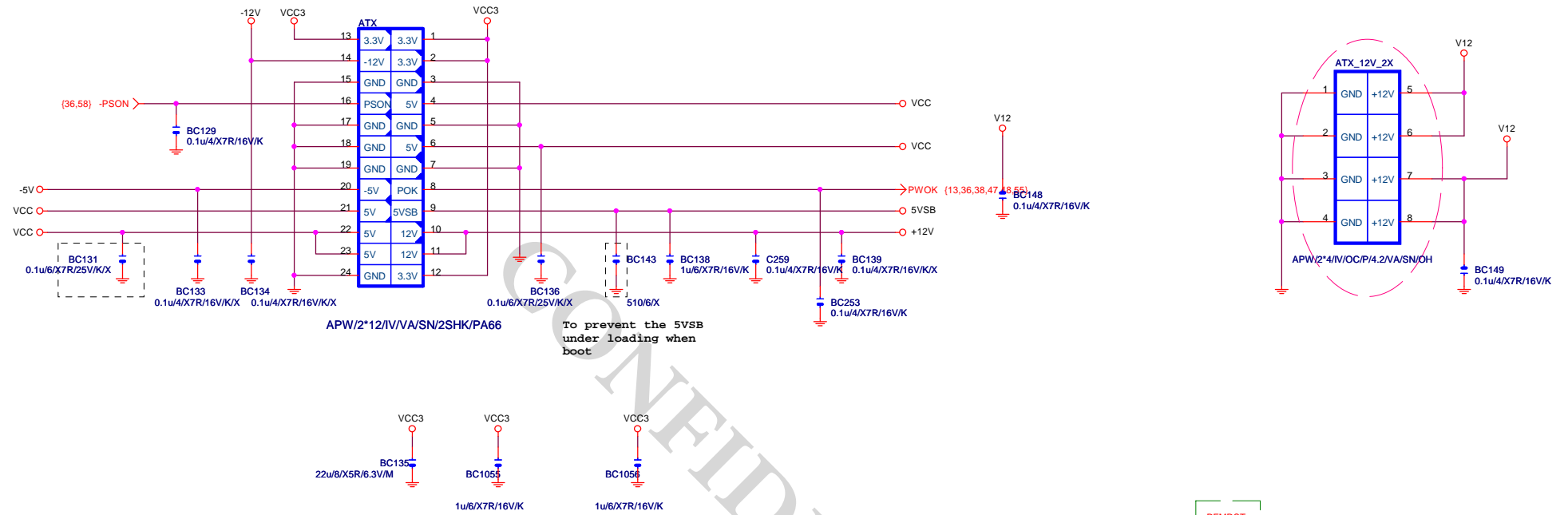
S3 TURN OFF 2_5LEVEL, 避免DDR18V DROP
(因PSU +12V太晚關, 造成OP TURN ON, 導致DDR18V LOADING WORSE)



預留Power issue



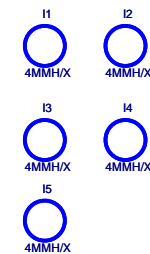
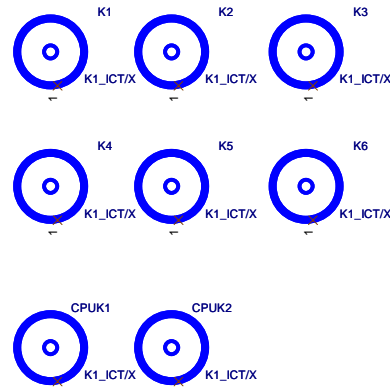
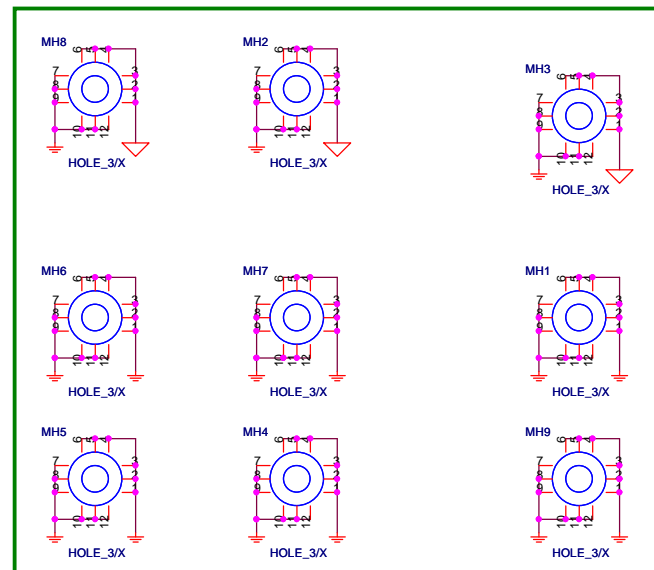
ATX POWER CONNECTOR



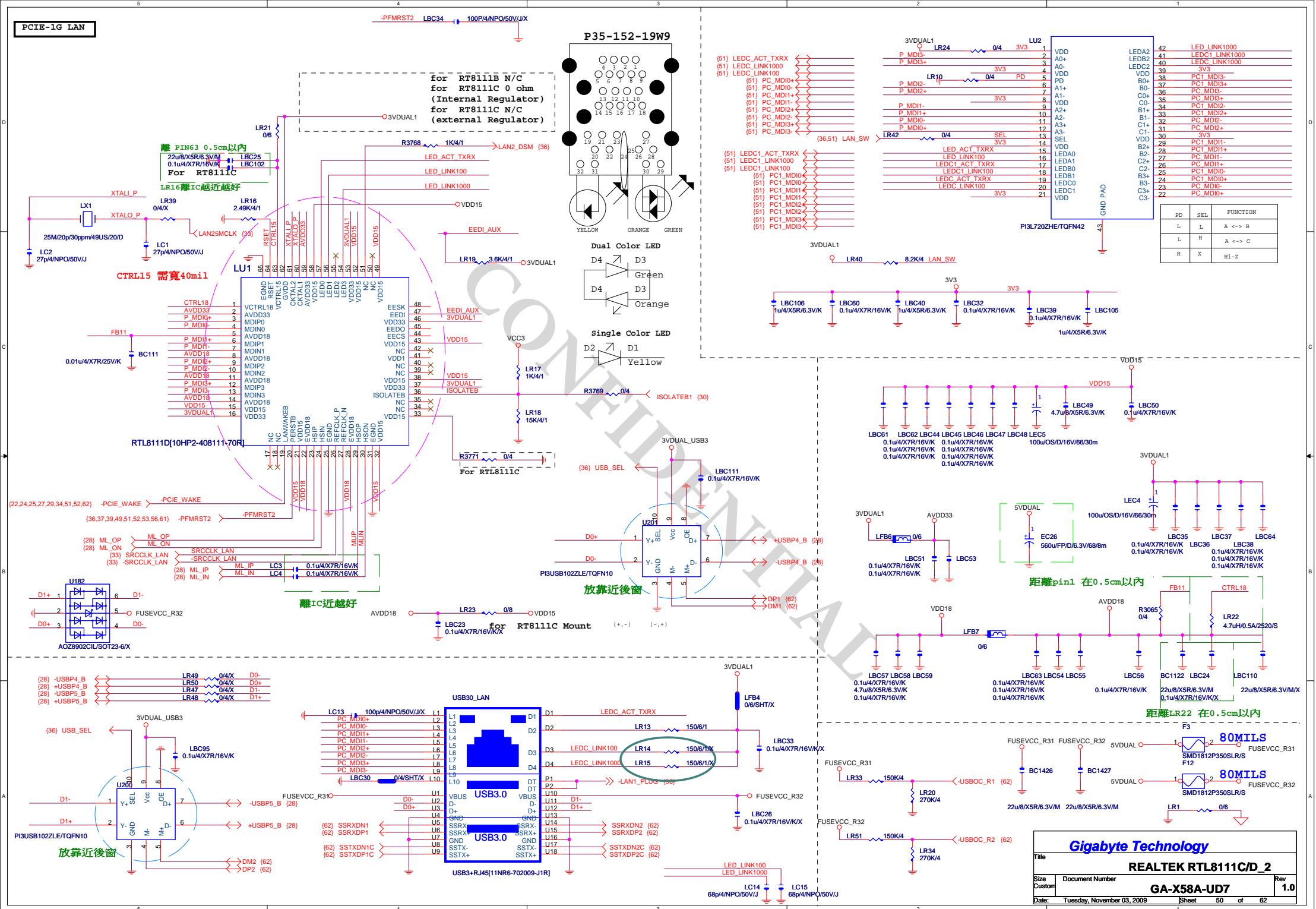
APW/2*12/IV/VA/SN/2SHK/PA66

To prevent the 5VSB under loading when boot

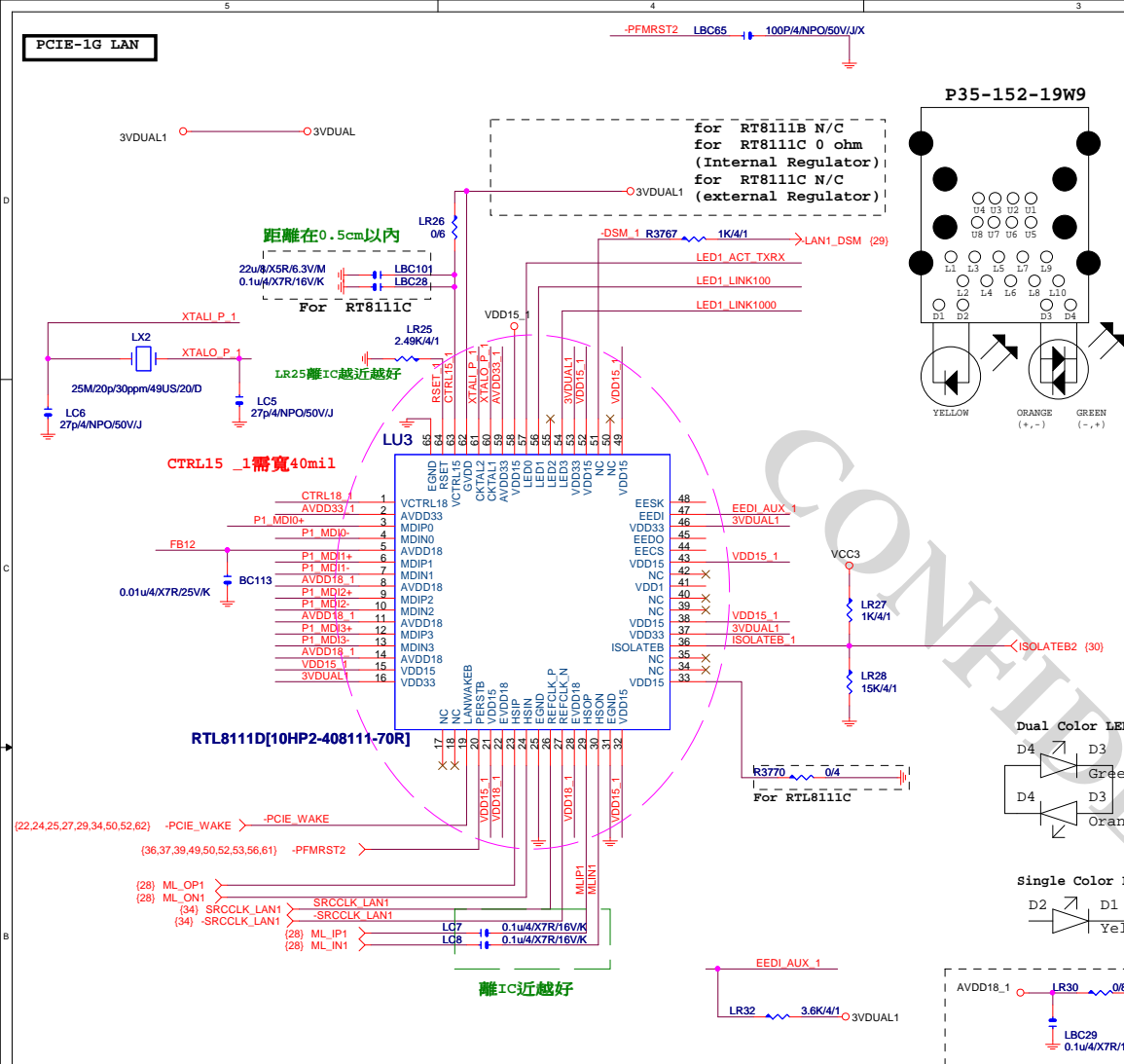
PCB 螺絲孔位置(Footprint不同)



Gigabyte Technology			
Title	ATX POWER CONNECTOR		
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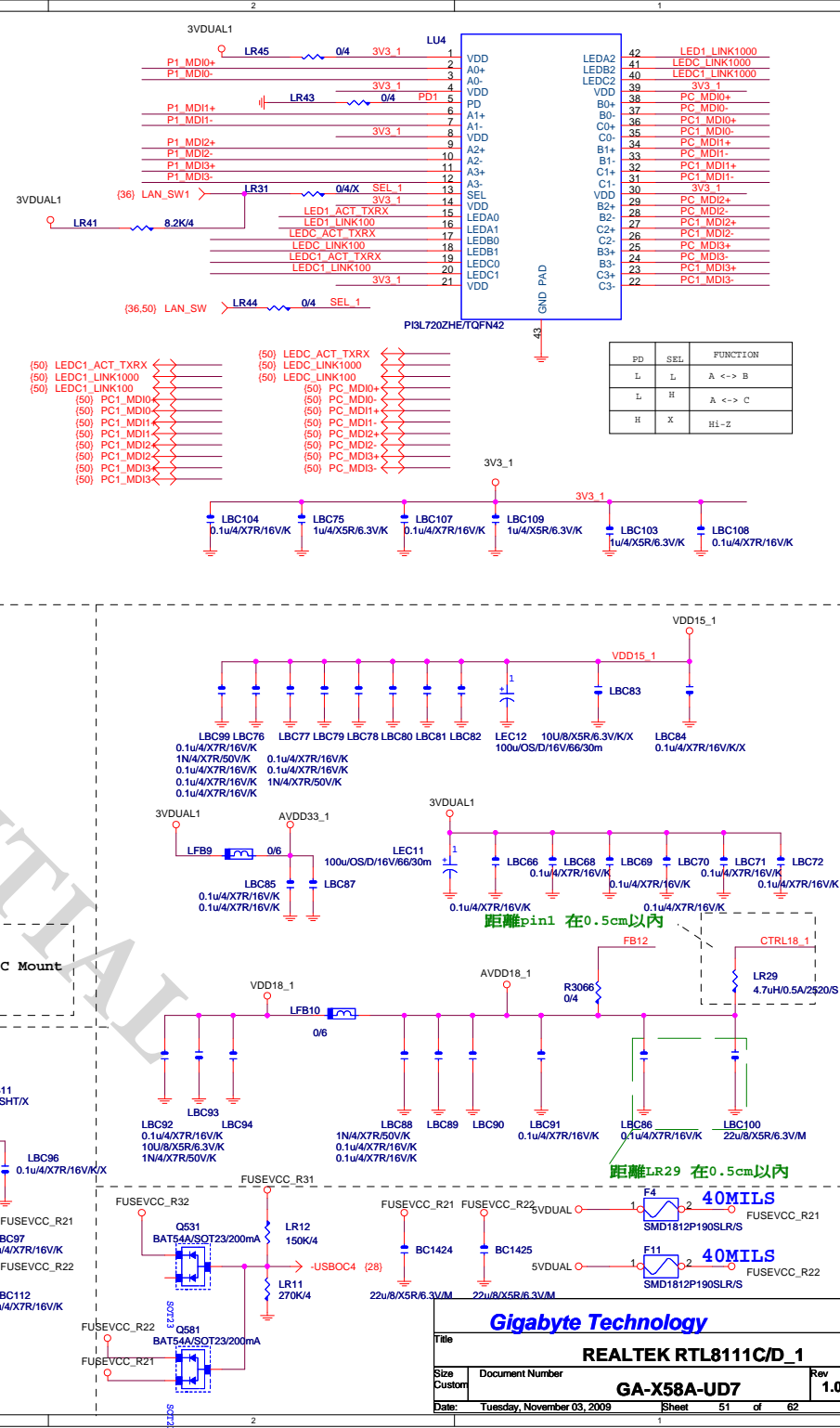
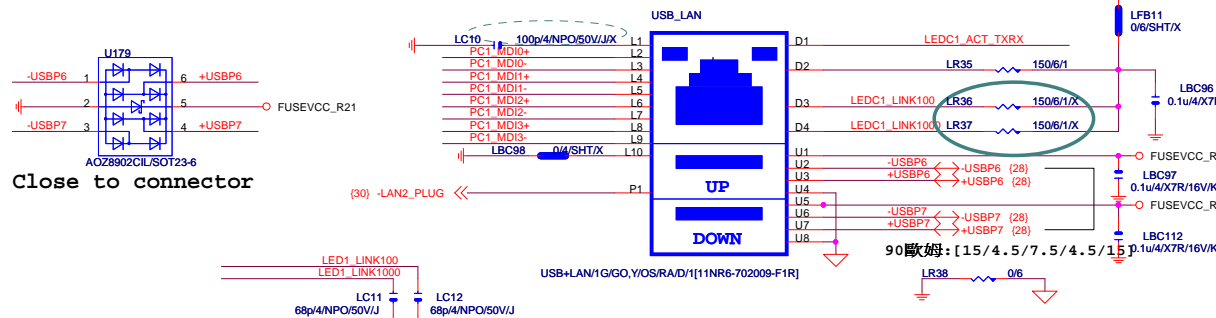


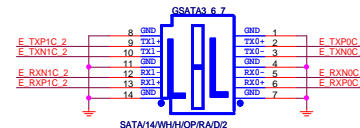
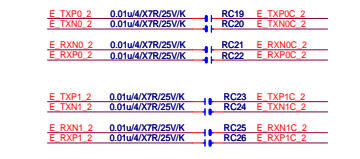
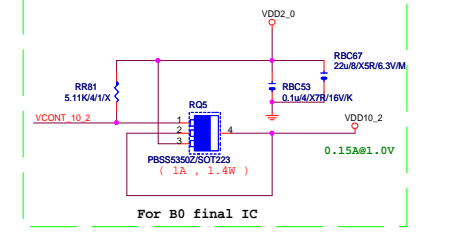
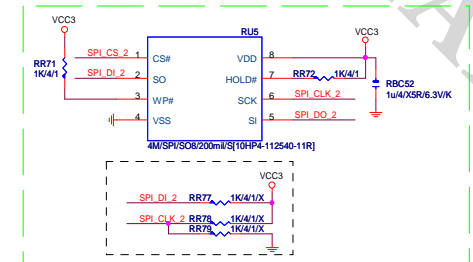
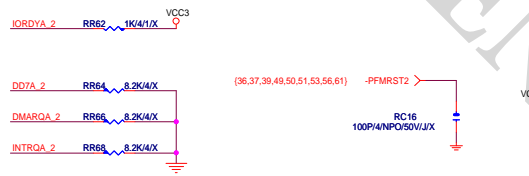
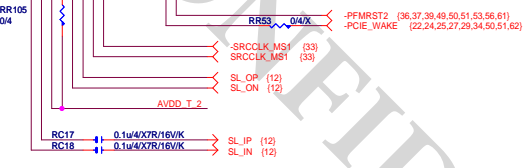
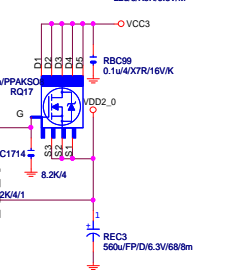
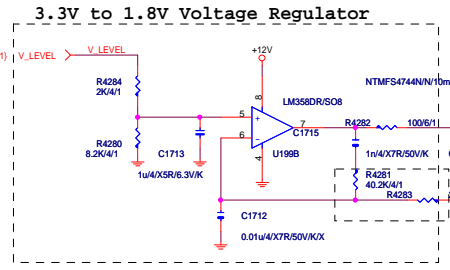
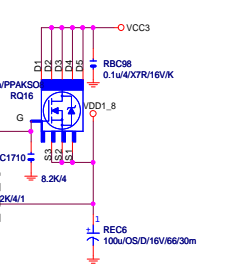
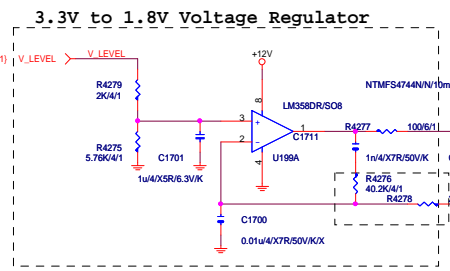
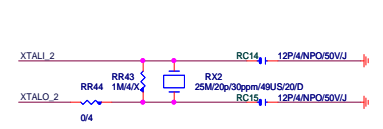
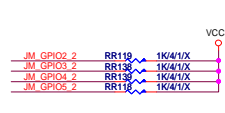
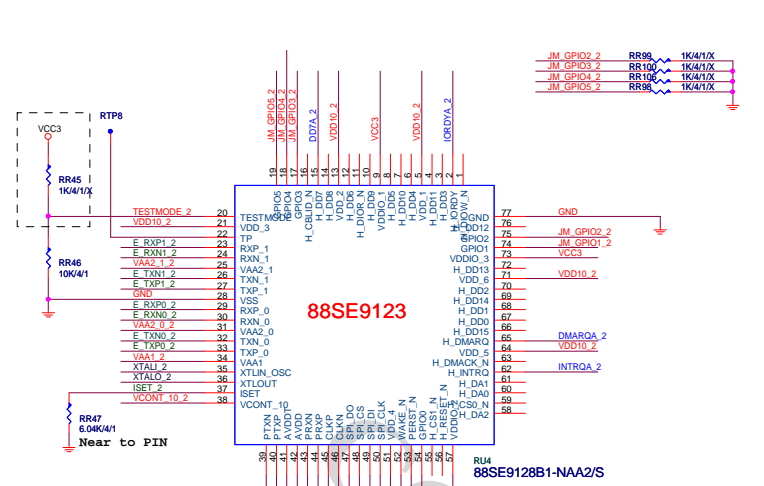
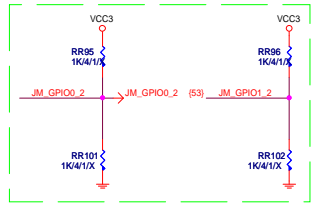
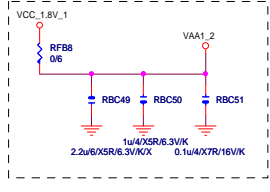
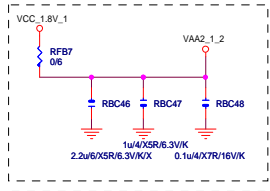
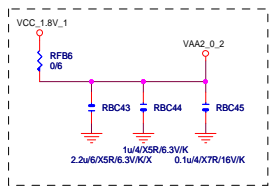
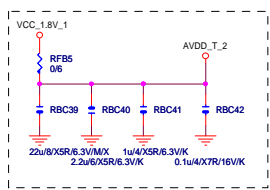
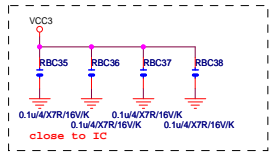
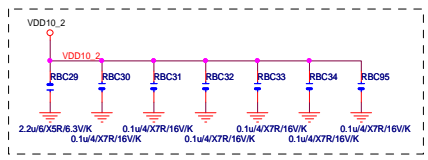
PCIE-1G LAN



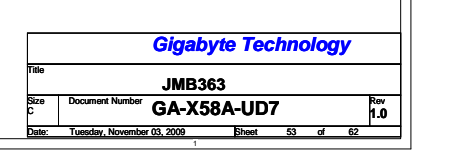
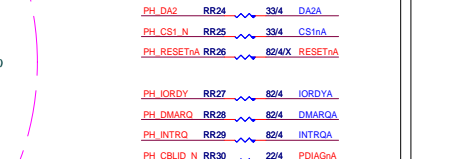
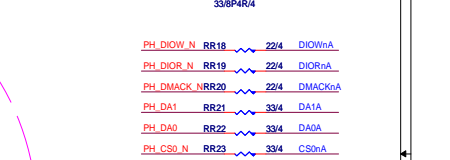
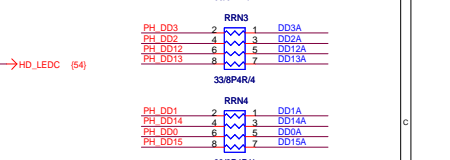
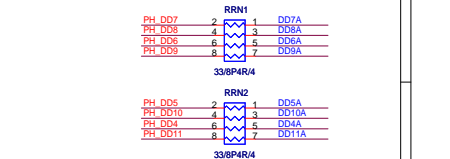
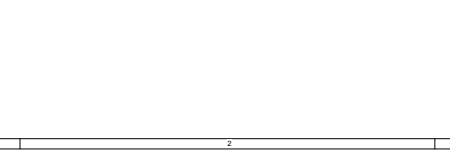
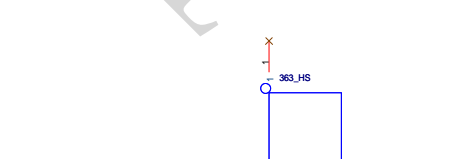
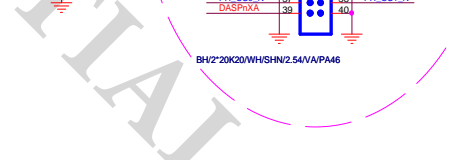
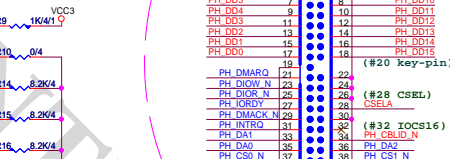
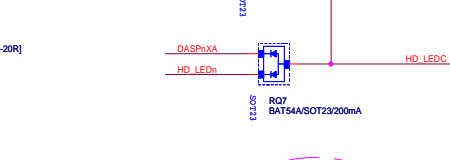
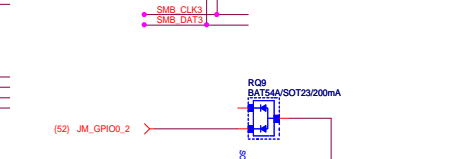
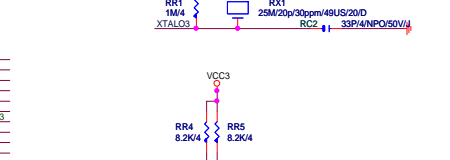
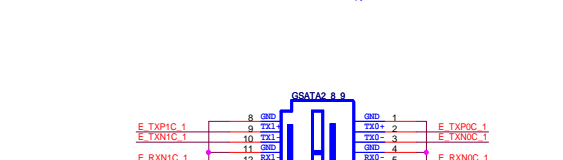
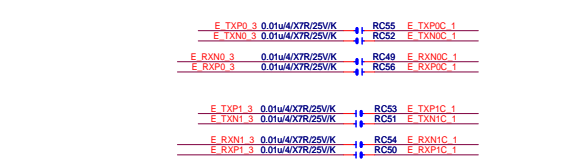
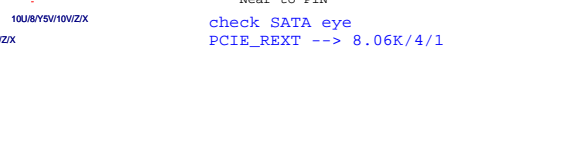
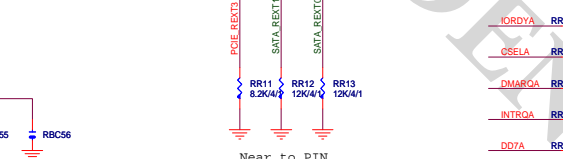
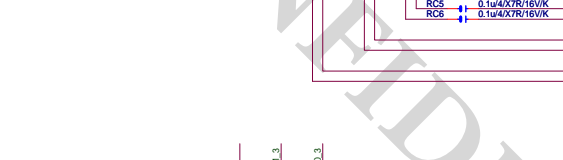
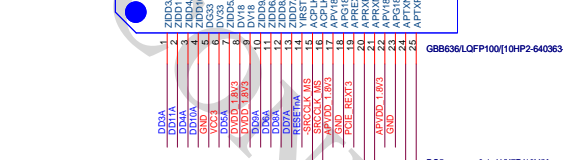
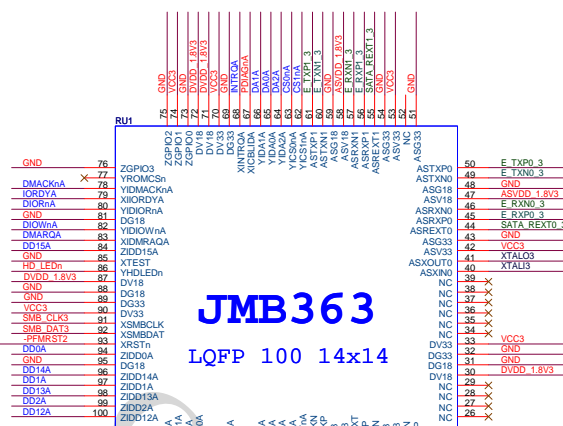
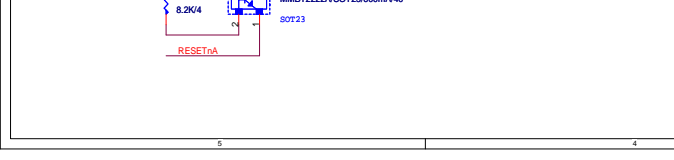
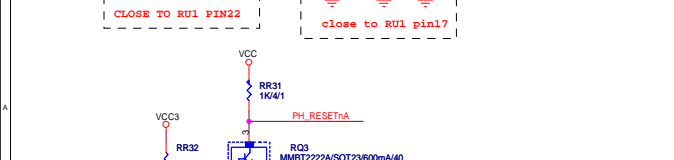
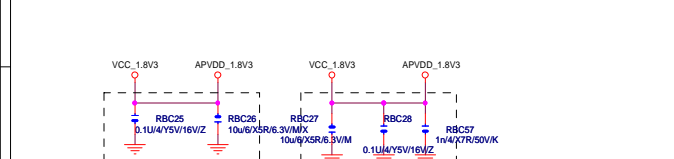
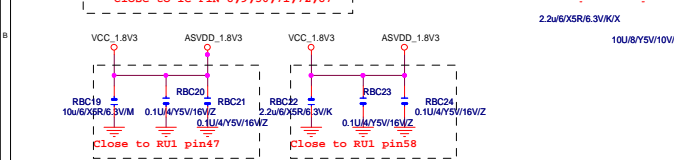
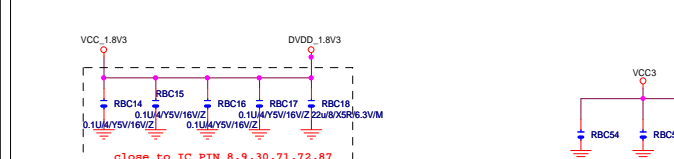
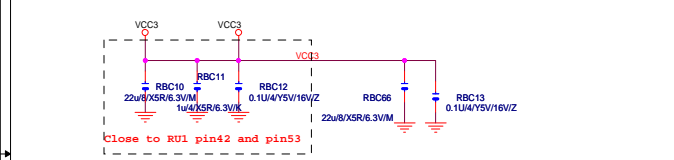
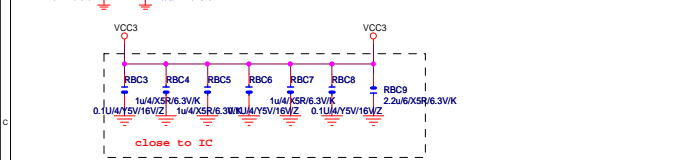
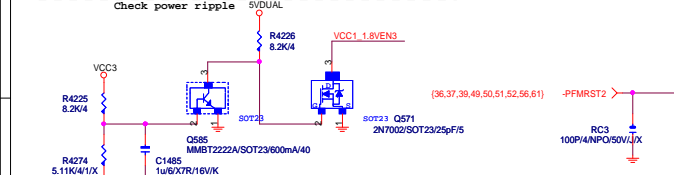
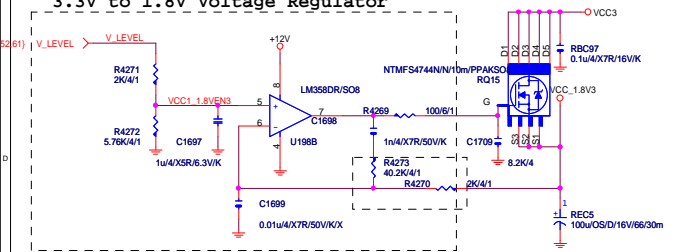
USB_LAN CONNECTOR

LAN 100歐姆:[20/7/8/7/20]



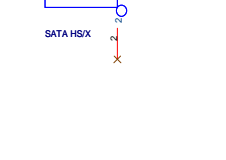
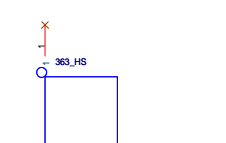
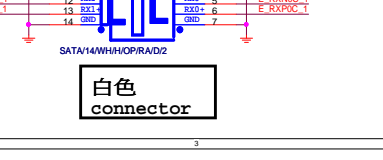
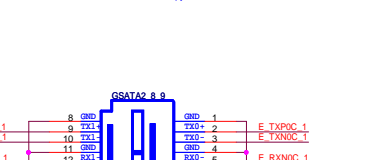
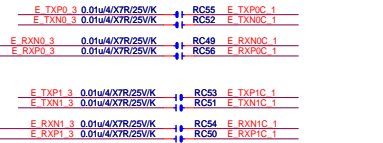
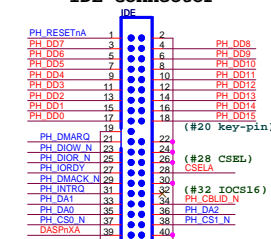


3.3V to 1.8V Voltage Regulator

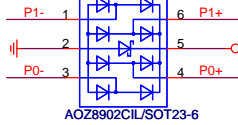
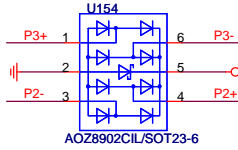
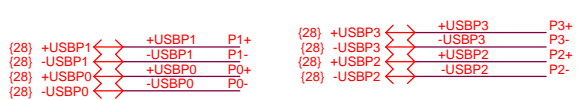


JMB363
LQFP 100 14x14

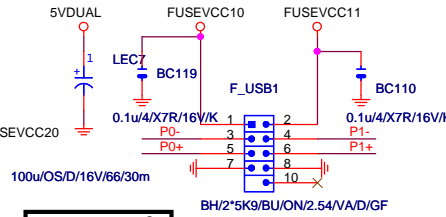
slot白色
IDE Connector



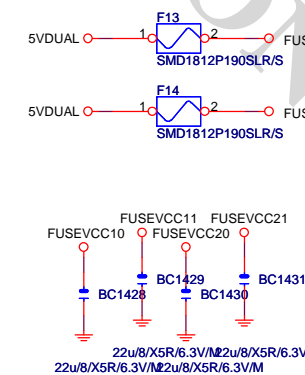
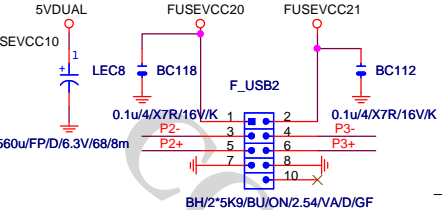
白色
connector



FRONT USB1

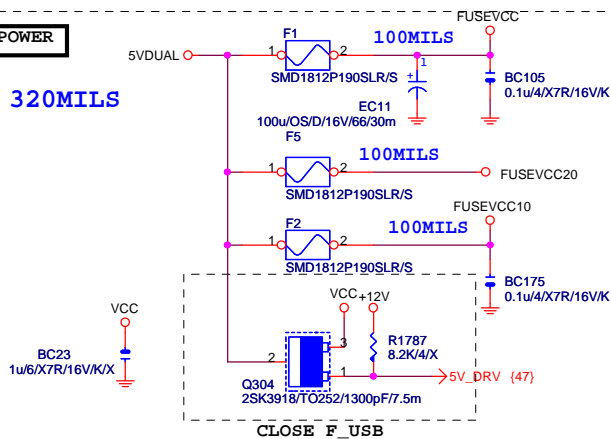


FRONT USB2



USB POWER

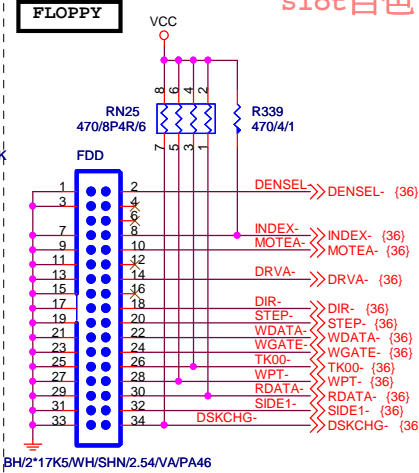
320MILS



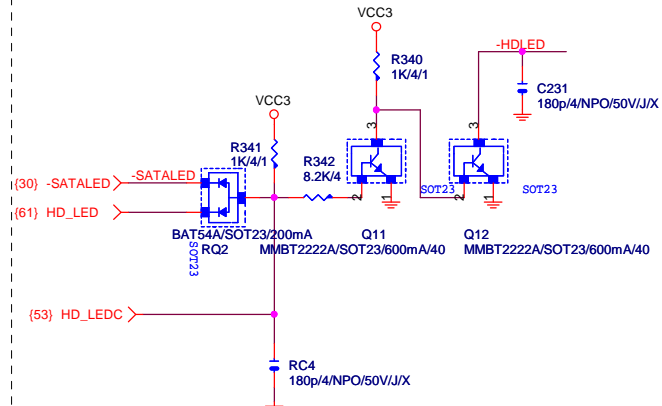
Place near the
sys_fan1 , reversed.

Place near the MH9 to reduce
the couple from VIN plane

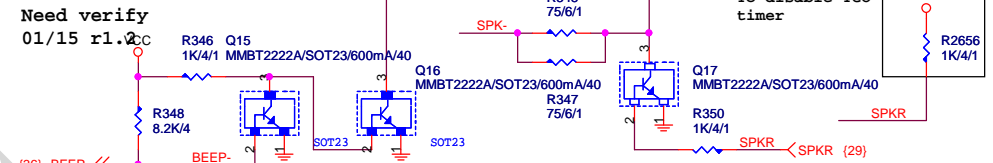
FLOPPY



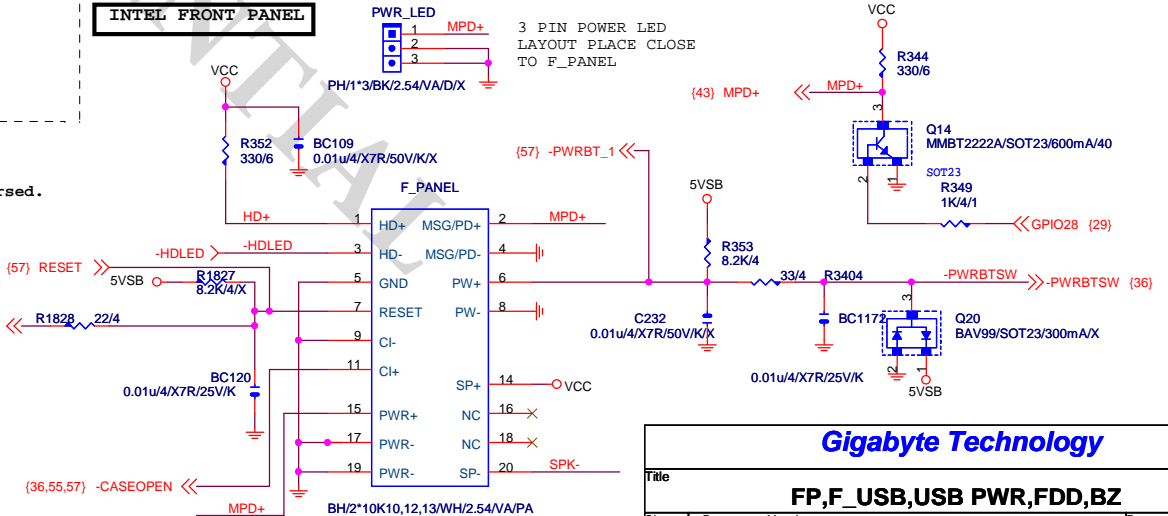
SATA LED



Need verify
01/15 r1.2c



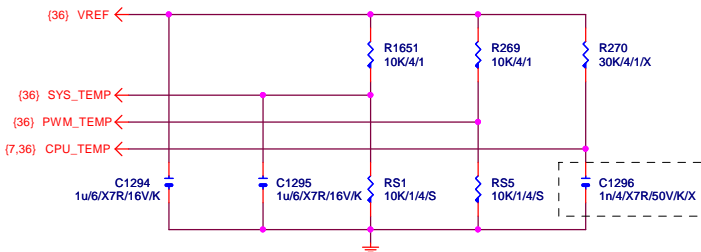
INTEL FRONT PANEL



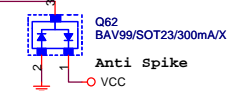
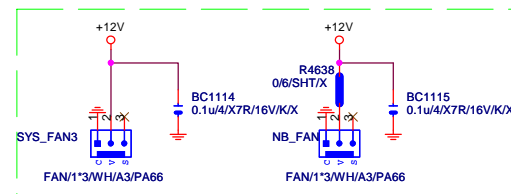
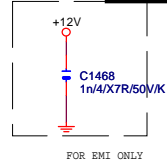
Gigabyte Technology

Title					FP,F_USB,USB PWR,FDD,BZ				
Size	Document Number	GA-X58A-UD7				Rev	1.0		
Date:	Tuesday, November 03, 2009			Sheet	54	of	62		

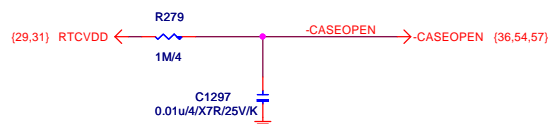
CASE OPEN



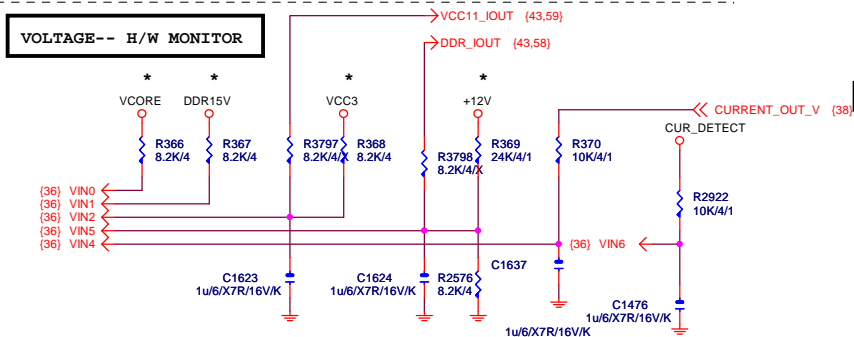
CPU SMART FAN



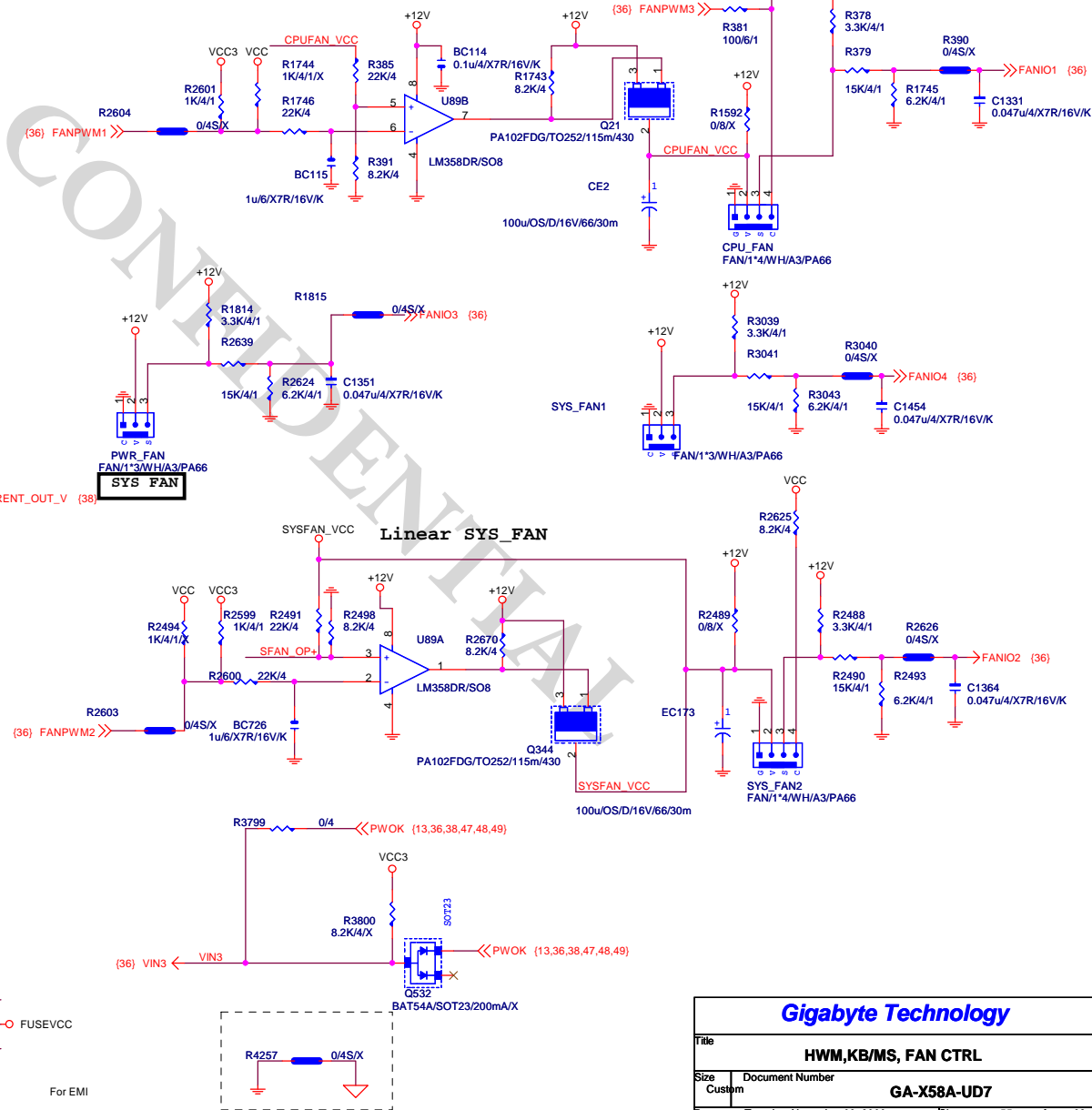
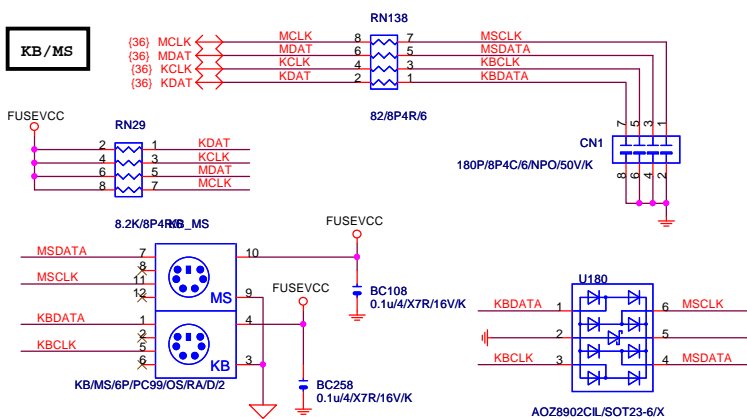
Case Open Circuits



VOLTAGE-- H/W MONITOR

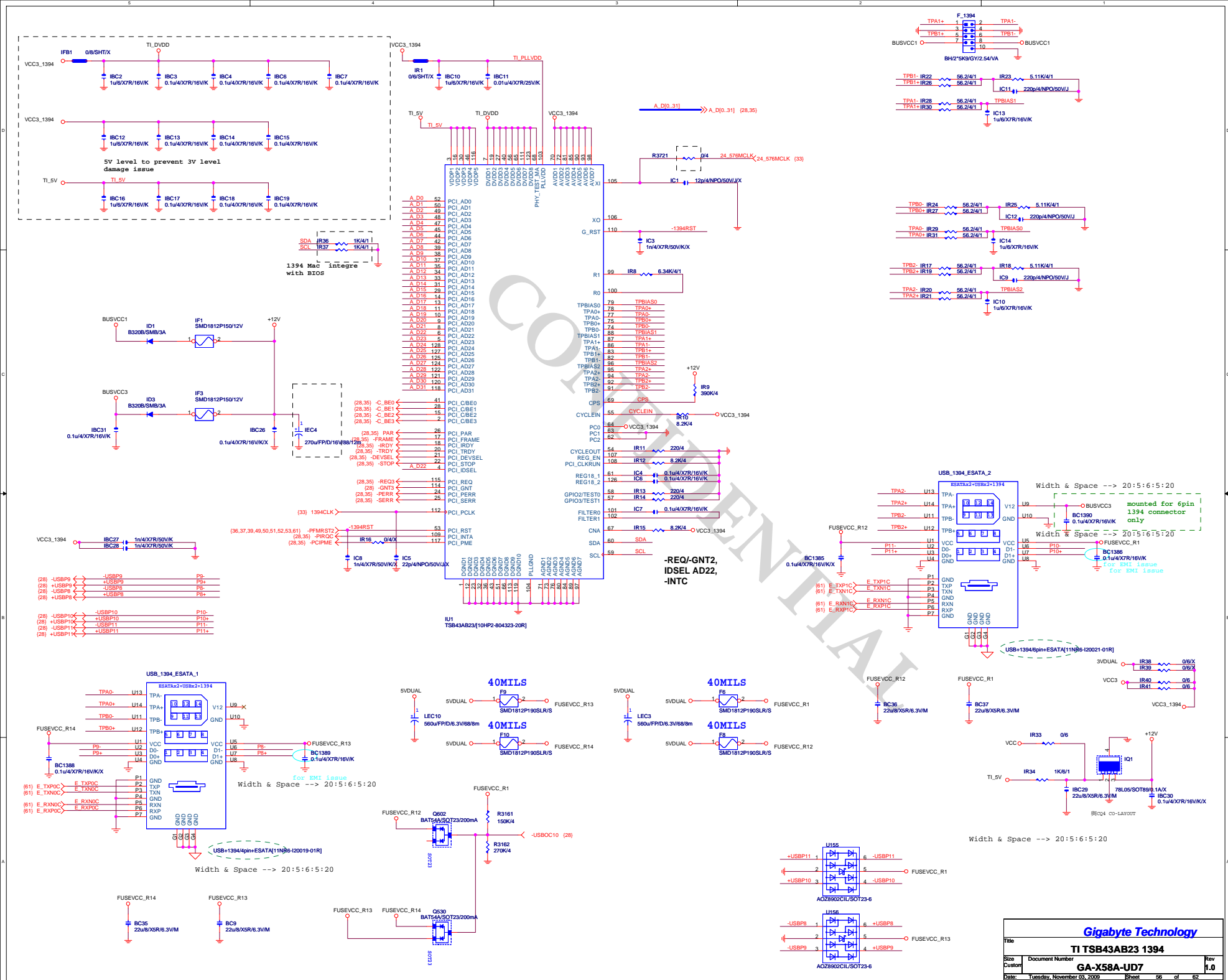


KB/MS



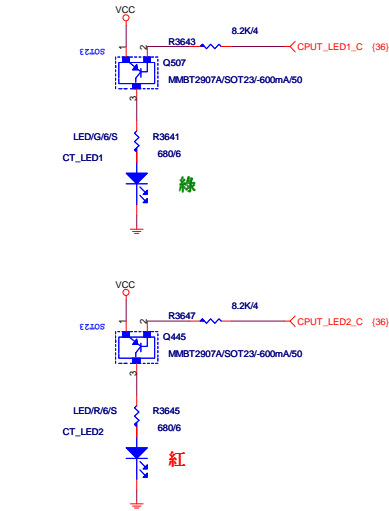
Gigabyte Technology

Title				HWM,KB/MS, FAN CTRL			
Size	Document Number						Rev
Custom	GA-X58A-UD7						1.0
Date:	Tuesday, November 03, 2009			Sheet	55	of	62



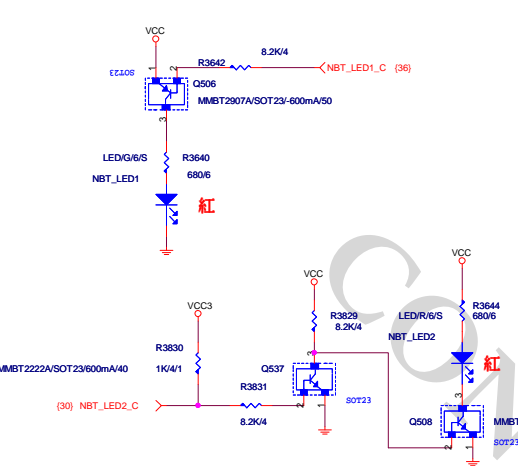
CPU溫度顯示

	I/O	Thermal
CPUT_LED1	GP63	60℃以上
CPUT_LED2	GP35	70℃以上



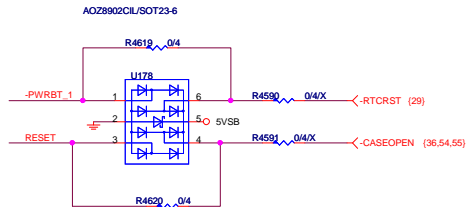
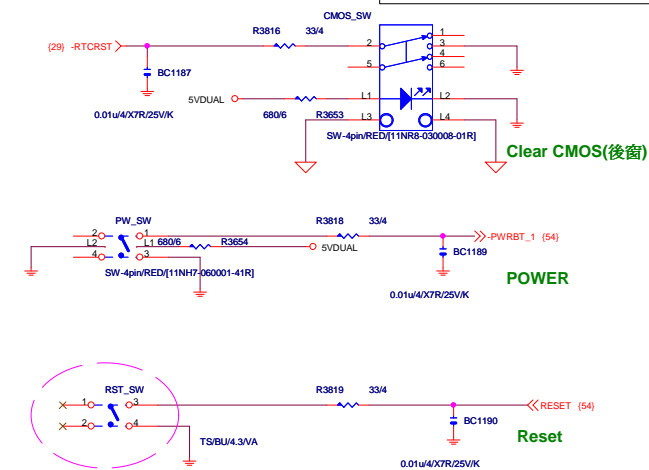
北橋(MCH)溫度顯示

	I/O	Thermal
NBT_LED1	GP30	60℃以上
NBT_LED2	GP31	70℃以上



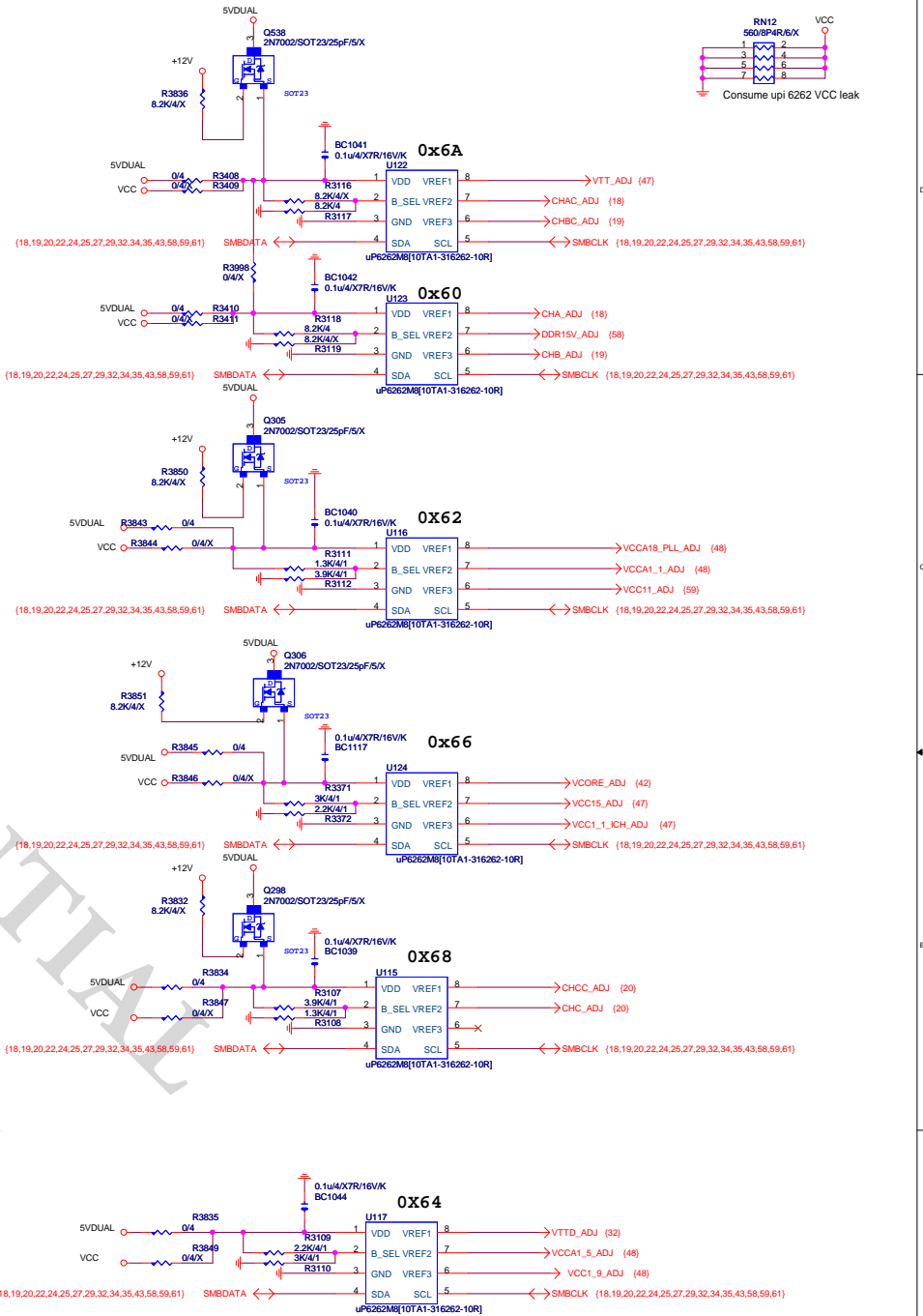
Switch 部分

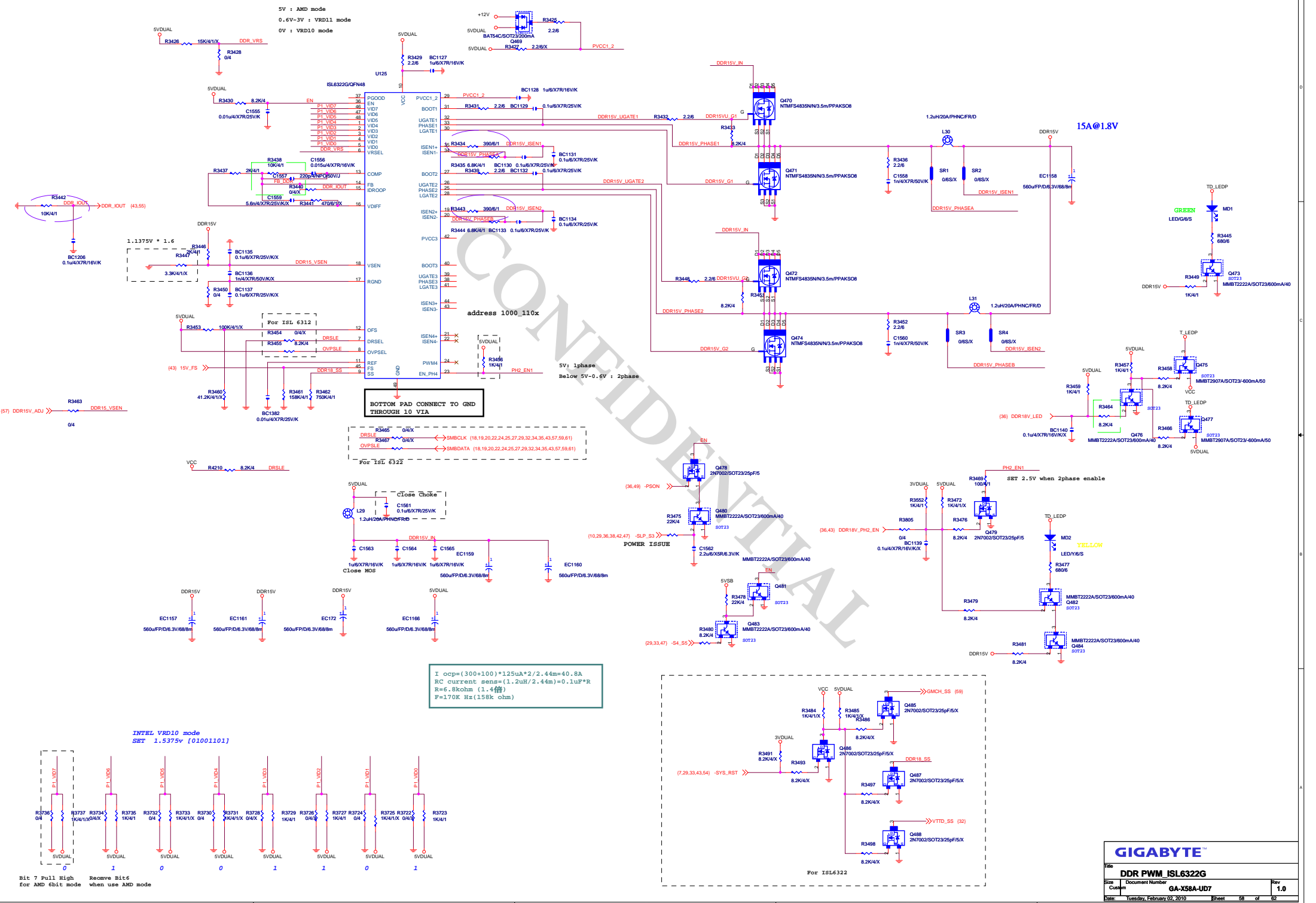
Clear CMOS 90℃料號:11NR8-030008-01R.
Clear CMOS 180℃料號:11NH7-060001-11R.
Power 180℃料號:11NH7-030001-21R.
Reset 180℃料號:11NH7-060001-51R.



UPI6262 Table

up6262	0X60-U123 (5VDUAL)	0X62-U116 (5VDUAL)	0X6A-U122 (5VDUAL)	0X66-U124 (5VDUAL)	0X68-U115 (5VDUAL)	0X64-U117 (5VDUAL)
VREF1	CHA_ADJ	VCCA18_PLL_ADJ	VTT_ADJ	VCORE_ADJ	CHCC_ADJ	VTTD_ADJ
VREF2	DDR18V_ADJ	VCCA1_1_ADJ	CHAC_ADJ	VCC15_ADJ	CHC_ADJ	VCC1_1_ICH_ADJ
VREF3	CHB_ADJ	VCC11_ADJ	CHBC_ADJ	VCCA1_5_ADJ	MCH_RAMVREF_ADJ	VCC1_9_ADJ

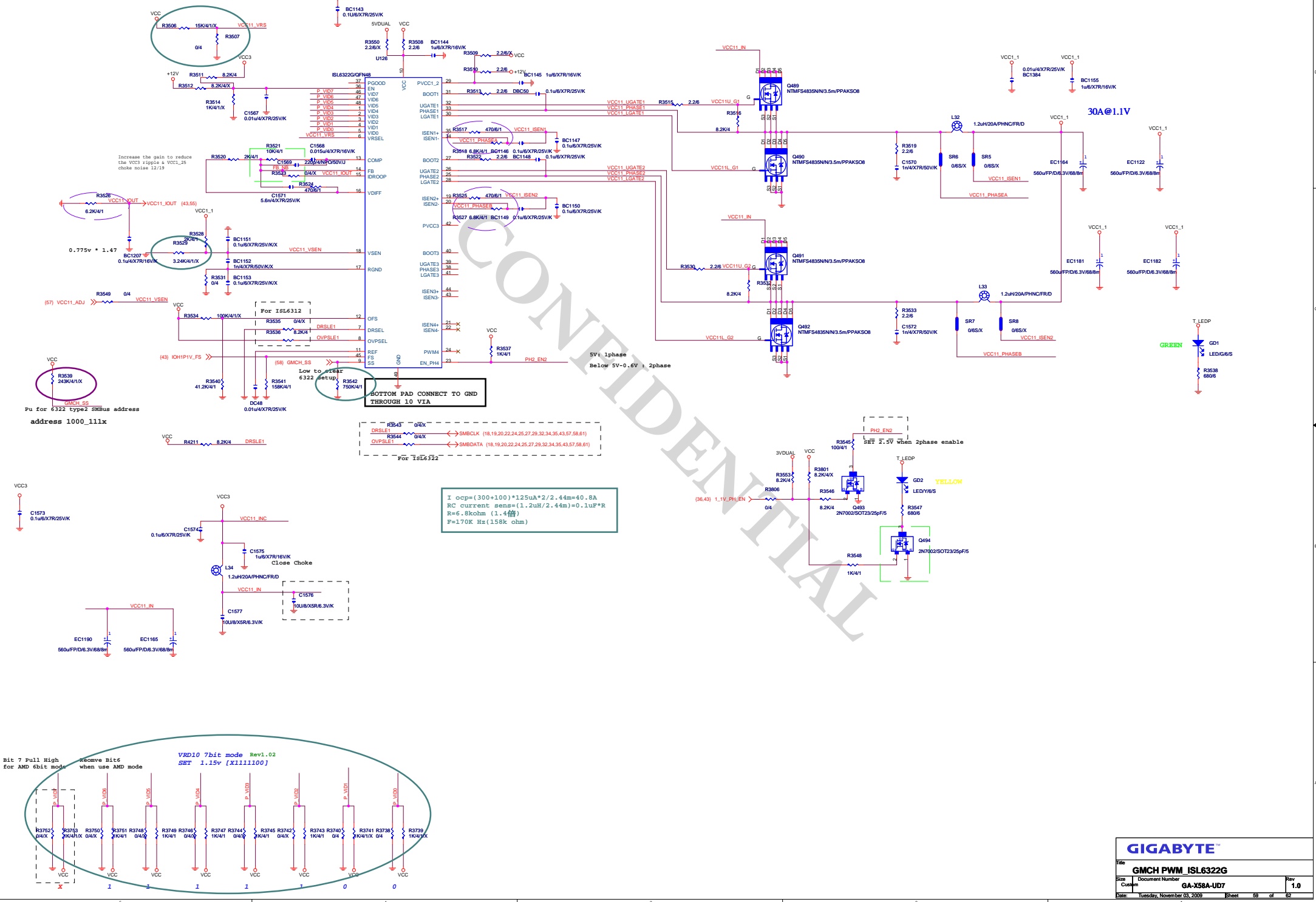


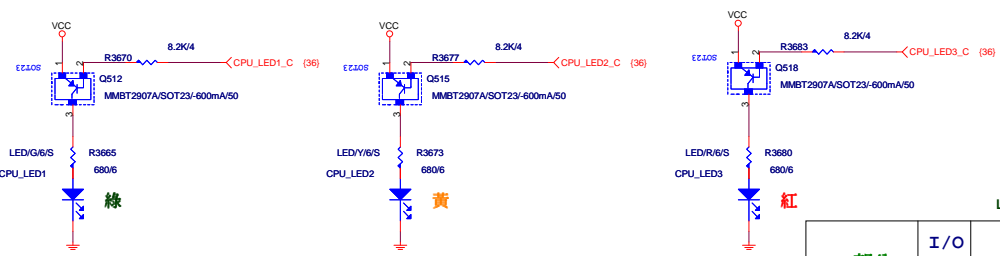


Bit 7 Pull High for AMD 6bit mode
Remove Bit6 when use AMD mode

$I_{ocp} = (300+100) * 125\mu A * 2 / 2.44m = 40.8A$
 $RC \text{ current sense} = (1.2\mu H / 2.44m) = 0.1\mu s * R$
 $R = 6.8k\Omega (1.4\text{倍})$
 $F = 170K \text{ Hz} (158k \text{ ohm})$

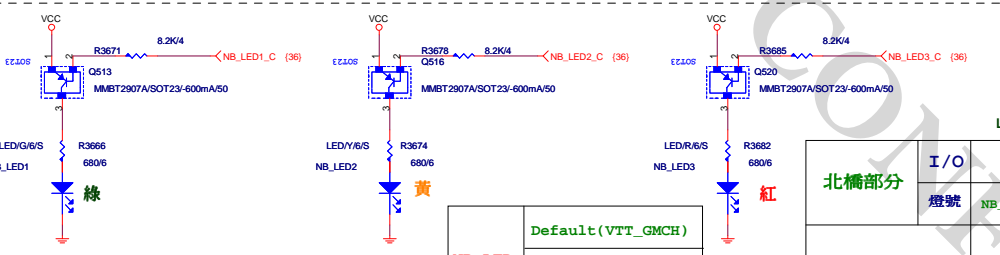
5V : AMD mode
0.6V~3V : VRD11 mode
0V : VRD10 mode





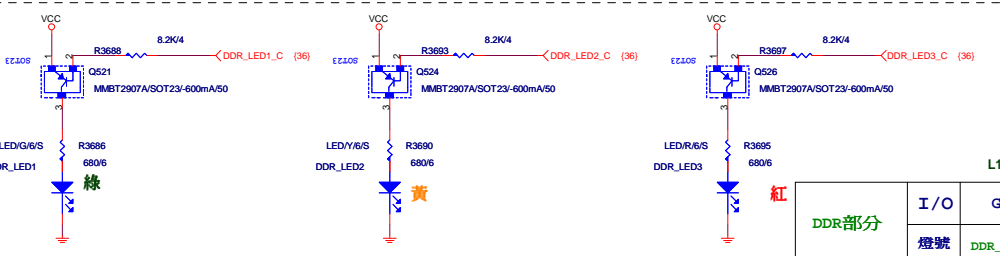
CPU LED 選擇	Default(Vcore)
	CPU PLL
	VTTD

CPU部分	I/O	GP80	GP81	GP82
	燈號	CPU_LED1_C	CPU_LED1_C	CPU_LED1_C
CPU Vcore電壓		1.45V~1.54372V	1.55V~1.59375V	1.6V以上
VTTD		1.25~1.4V	1.42~1.55V	1.56V以上

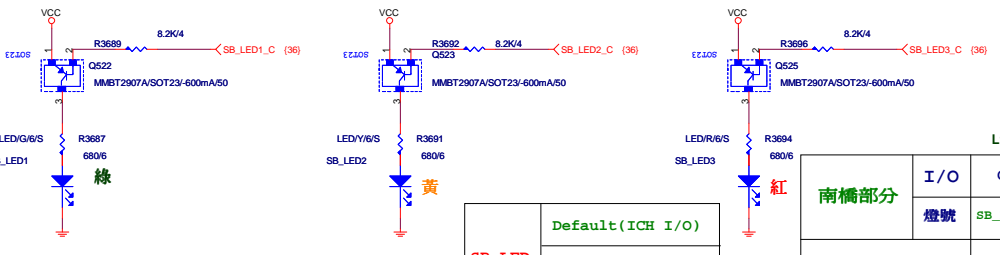


NB LED 選擇	Default(VTT_GMCH)
	VCC1_1

北橋部分	I/O	GP70	GP71	GP72
	燈號	NB_LED1_C	NB_LED2_C	NB_LED3_C
VCC1_1		1.18V~1.3V	1.32~1.5V	1.52V以上



DDR部分	I/O	GP83	GP21	GP87
	燈號	DDR_LED1_C	DDR_LED2_C	DDR_LED3_C
DDR3電壓		1.55~1.65V	1.68~1.74V	1.76V以上



SB LED 選擇	Default(ICH I/O)
	VCC1_5
	VCC1_1_ICH

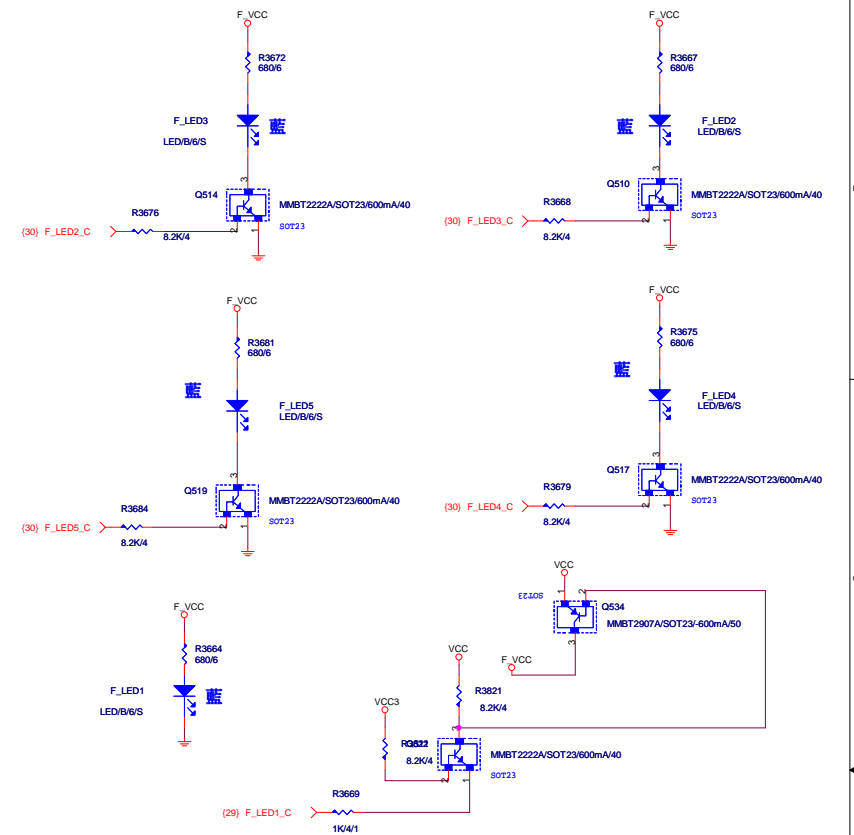
南橋部分	I/O	GP73	GP74	GP64
	燈號	SB_LED1_C	SB_LED2_C	SB_LED3_C
VCC1_5		1.56~1.68V	1.68~1.86V	1.88V以上
VCC1_1_ICH		1.18V~1.3V	1.32~1.5V	1.52V以上

CPU Voltage

IOH Voltage

DDR Voltage

ICH Voltage

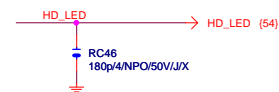
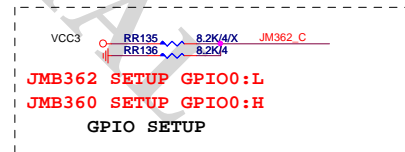
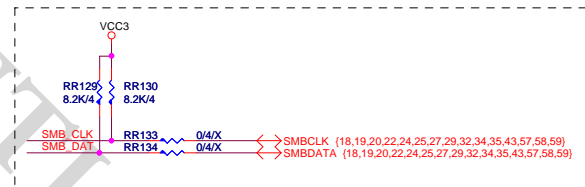
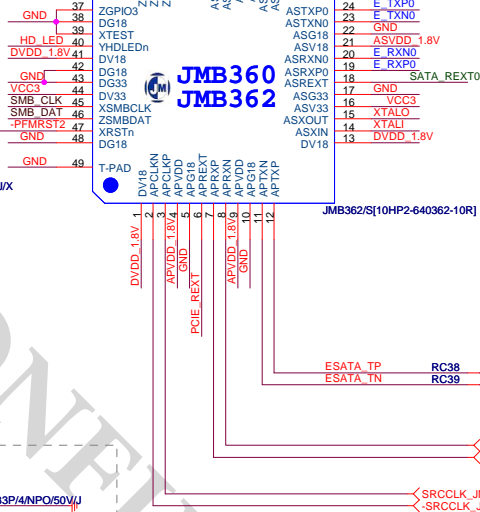
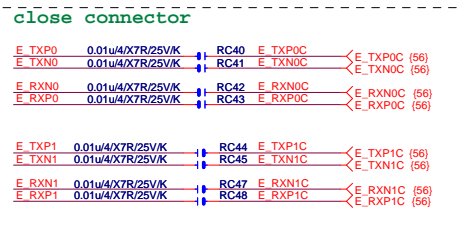
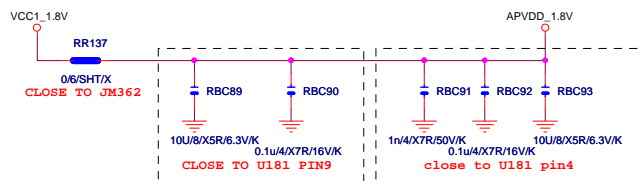
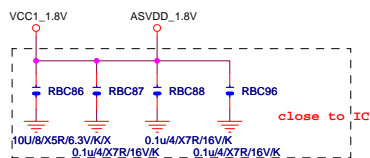
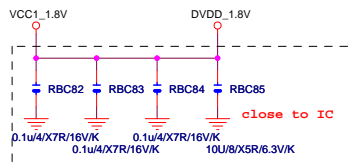
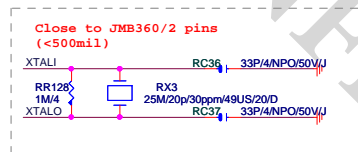
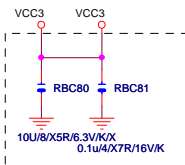
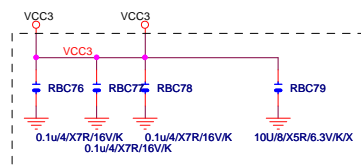
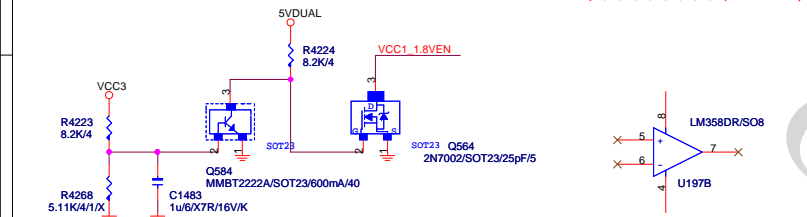
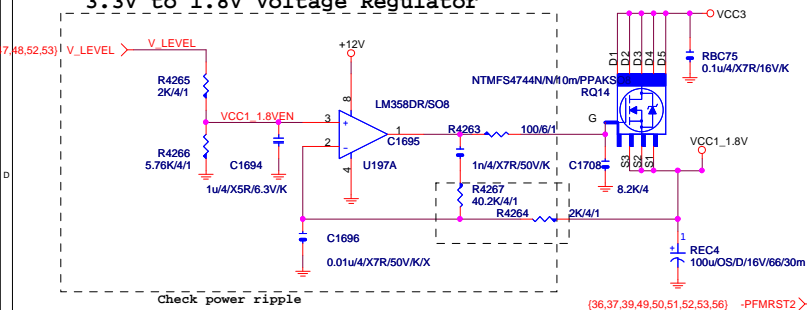


FSB LED	OFF	L1	L2	L3	L4	L5
GPIO		ICH GP57	ICH GP56	ICH GP22	ICH GP38	ICH GP21
CPU 133		145~	155~	165~	175~	185~

燈號表示方式

	L1 (LED1)	L2(LED2)	L3 (LED3)
CPU/DDR NB/SB	綠	黃	紅

3.3V to 1.8V Voltage Regulator



Gigabyte Technology			
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